**Product data sheet** 

### 1. General description

ESD protection device in a small SOD323 (SC-76) Surface-Mounted Device (SMD) plastic package designed to protect automotive In-vehicle network bus lines from the damage caused by ElectroStatic discharge (ESD) and other transients.

### 2. Features and benefits

- Reverse stand-off voltage: V<sub>RWM</sub> = 27 V
- Low clamping voltage: V<sub>CL</sub> = 36 V at I<sub>PP</sub> = 3 A
- ESD protection up to 30 kV (IEC 61000-4-2)
- ESD protection up to 30 kV (ISO 10605: C = 330 pF, R = 330 Ω)
- ISO 7637-3: Pulse a: V<sub>S</sub> = -150 V / Pulse b: V<sub>S</sub> = +100 V
- Ultra low leakage current: I<sub>RM</sub> < 1 nA</li>
- Qualified according to AEC-Q101 / Automotive grade

### 3. Applications

ESD protection for In-vehicle network lines in automotive environments

- CAN
- LIN
- FlexRay
- SENT

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	27	V
I <sub>PPM</sub>	rated peak pulse current	$t_p = 8/20 \ \mu s$	[1]	-	-	3	Α
V <sub>CL</sub>	clamping voltage	$I_{PPM} = 3 \text{ A}; t_p = 8/20  \mu\text{s}; T_{amb} = 25 ^{\circ}\text{C}$	[1]	-	36	45	V

[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.



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# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	1 2	1-12-2
2	K2	cathode (diode 2)		006aab041
			SOD323	

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
PESD1IVN27-A	SOD323	plastic, surface-mounted package; 2 leads; 1.3 mm pitch; 1.7 mm x 1.25 mm x 0.95 mm body	SOD323		

# 7. Marking

### Table 4. Marking codes

Type number	Marking code
PESD1IVN27-A	B5

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## 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1]	-	3	Α
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximu	um ratings					
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2; contact discharge	[2]	-	30	kV
	voltage	ISO 10605: contact discharge; C = 330 pF, R = 330 $\Omega$	[2]	-	30	kV
		ISO 10605: contact discharge; C = 150 pF, R = 330 $\Omega$	[2]	-	30	kV

- [1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [2] Device stressed with ten non-repetitive ESD pulses.

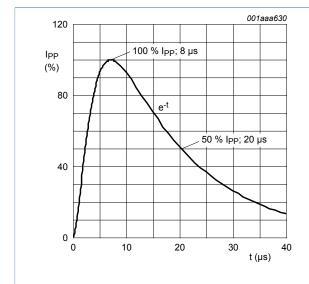


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

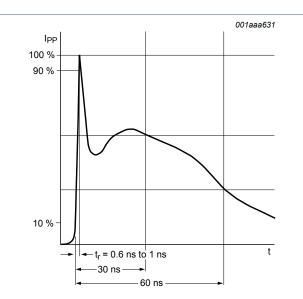


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

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### 9. Characteristics

**Table 6. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	27	V
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 10 mA; T <sub>amb</sub> = 25 °C		28	33	38	V
I <sub>RM</sub>	reverse leakage current	V <sub>R</sub> = 27 V; T <sub>amb</sub> = 25 °C		-	1	50	nA
C <sub>d</sub>	diode capacitance	$f = 1 \text{ MHz}; V_R = 0 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$		-	14	17	pF
V <sub>CL</sub>	clamping voltage	$I_{PPM}$ = 1 A; $t_p$ = 8/20 $\mu$ s; $T_{amb}$ = 25 °C	[1]	-	34	43	V
		$I_{PPM}$ = 3 A; $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C	[1]	-	36	45	V
		$I_{PP}$ = 16 A; $t_p$ = TLP; $T_{amb}$ = 25 °C	[2]	-	35	-	V
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 10 A; T <sub>amb</sub> = 25 °C	[2]	-	0.2	-	Ω

- [1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [2] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008

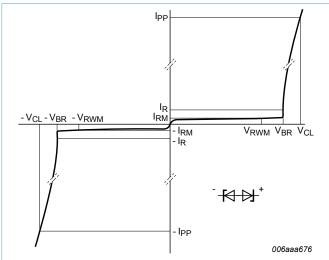


Fig. 3. V-I characteristics for a bidirectional ESD protection diode

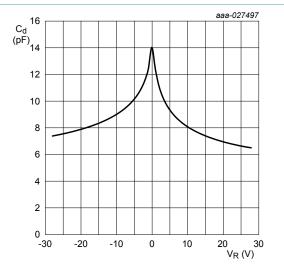


Fig. 4. Diode capacitance as a function of reverse voltage; typical values

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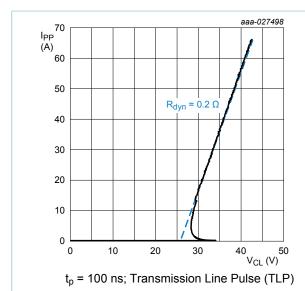


Fig. 5. Positive clamping voltage (TLP); typical values

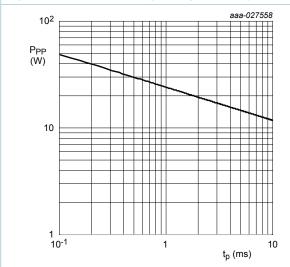
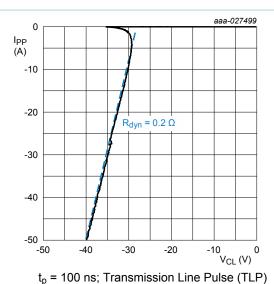


Fig. 7. Peak pulse power as a function of exponential pulse duration; typical values



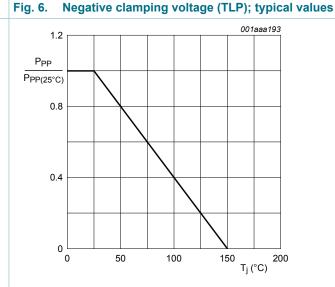
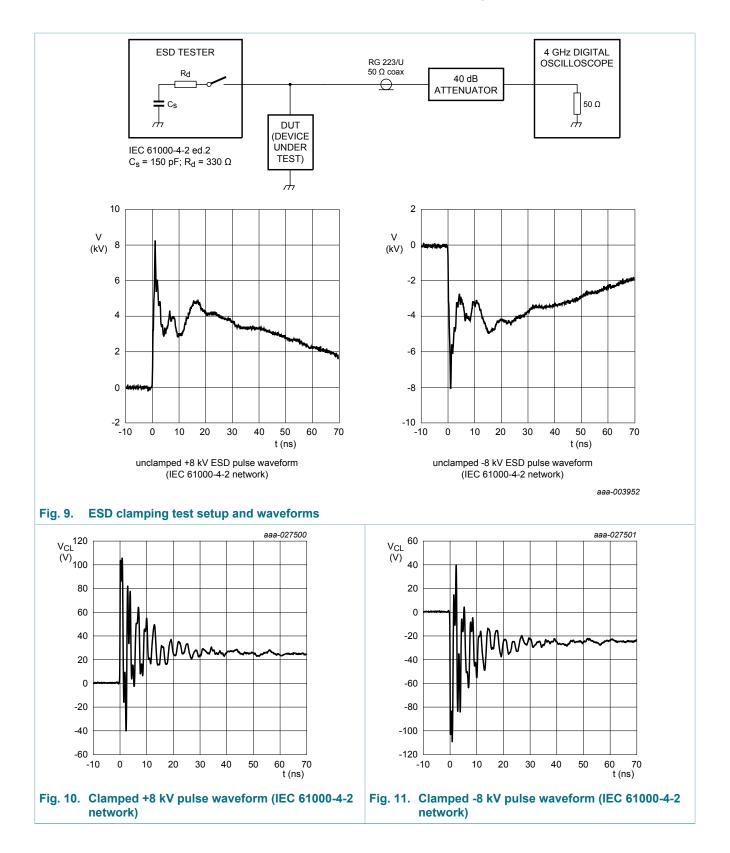


Fig. 8. Relative variation of peak pulse power as a function of junction temperature; typical values

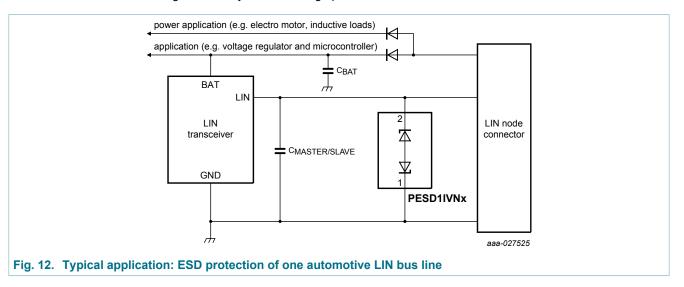
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## 10. Application information

The PESD1IVN27-A is designed for the protection of one automotive IVN bus line from the damage caused by ESD and surge pulses.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- **6.** Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- **8.** Use ground planes whenever possible. For multilayer PCBs, use ground vias.

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## 11. Package outline

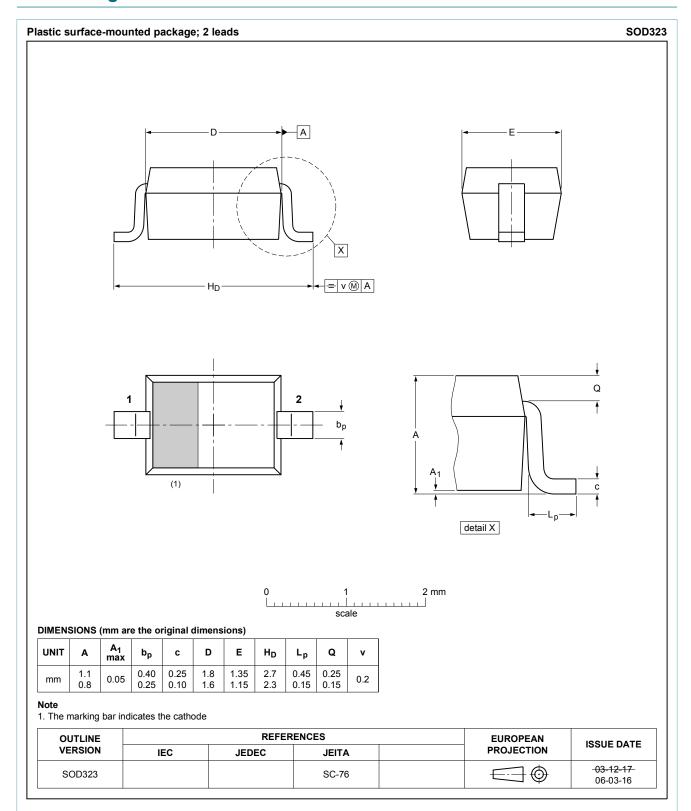
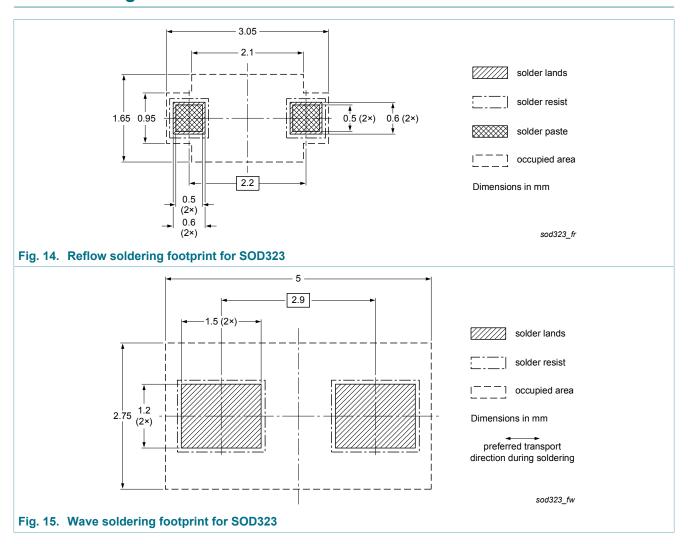


Fig. 13. Package outline SOD323

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## 12. Soldering



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# 13. Revision history

### **Table 7. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD1IVN27-A v.1	20171108	Product data sheet	-	-

### **ESD** protection for In-vehicle networks

## 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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