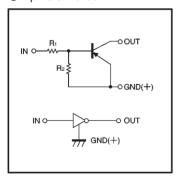
Digital transistors (built-in resistors) DTA143EE / DTA143EUA / DTA143EKA / DTA143ECA / DTA143ESA

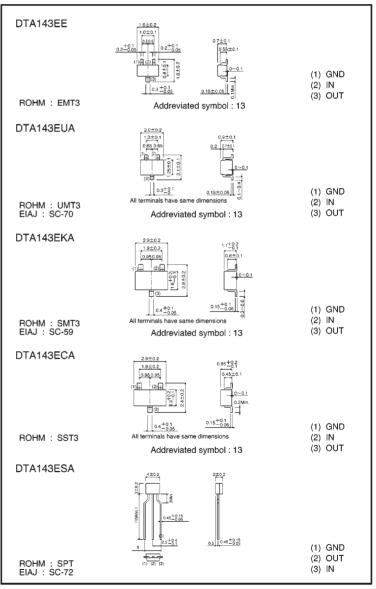
Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- The bias resistors consist of thinfilm resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making device design easy.
- StructurePNP digital transistor(with built-in resistors)

Equivalent circuit



External dimensions (Units: mm)



(96-268-A143E)



●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol		Unit				
		Е	ŲA	KA	CA	SA	Unit
Supply voltage	Vcc		٧				
Input voltage	VIN		٧				
Output current	lo		mA				
	IC(Max.)						
Power dissipation	Pd	150	200			300	mW
Junction temperature	Tj		$^{\circ}$				
Storage temperature	Tstg		Ĵ				

●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Input voltage	VI(off)	_	_	-0.5	.,	Vcc=-5V, Io=-100 μA	
	VI(on)	-3	_	_	V	Vo=-0.3V, Io=-20mA	
Output voltage	VO(on)	_	-0.1	-0.3	٧	lo/li=-10mA/-0.5mA	
Input current	li	_	_	-1.8	mA	V _I =-5V	
Output current	IO(off)	_	_	-0.5	μΑ	Vcc=-50V, Vi=0V	
DC current gain	Gı	20	_	_	_	Vo=-5V, lo=-10mA	
Input resistance	R ₁	3.29	4.7	6.11	kΩ	_	
Resistance ratio	R2/R1	0.8	1	1.2	_	_	
Transition frequency	fτ	_	250	_	MHz	V _{CE} =-10V, I _E =5mA, f=100MHz *	

^{*} Transition frequency of the device

Packaging specifications

	Package	EMT3	UMT3	SMT3	SST3	SPT
	Package type	Taping	Taping	Taping	Taping	Taping
	Code		T106	T146	T116	TP
Part No.	Basic ordering unit (pieces)	3000	3000	3000	3000	5000
DTA143EE		0	_	_	_	_
DTA143EUA		_	0	_	_	
DTA143EKA		_	_	0	_	_
DTA143ECA		_	_	_	0	_
DTA143ESA		_	_	_	_	0

Electrical characteristic curves

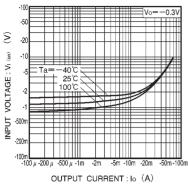


Fig.1 Input voltage vs. output current (ON characteristics)

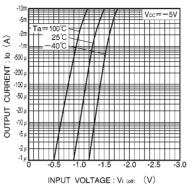


Fig.2 Output current vs. input voltage (OFF characteristics)

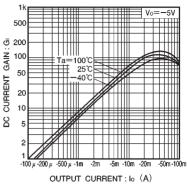


Fig.3 DC current gain vs. output current

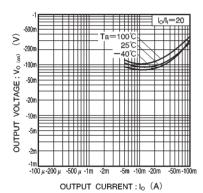


Fig.4 Output voltage vs. output current