



POWERDI[®]123

Product Summary

V _{RRM} (V)	I ₀ (A)	V _F max (V)	I _{R max} (μΑ)
100	1	0.62	1

Features and Benefits

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- Lead Free Finish, RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (See Note 4)

Description and Applications

This Schottky Barrier Rectifier is designed to meet the stringent requirements of Automotive Applications. It is ideally suited for use as:

- Polarity Protection Diode
- Re-circulating Diode
- Switching Diode

Mechanical Data

- Case: POWERDI[®]123
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208(2)
- Weight: 0.01 grams (Approximate)



Top View

Ordering Information (Note 5)

Part Number	Compliance	Case	Packaging
DFLS1100-7	Commercial	POWERDI [®] 123	3000/Tape & Reel
DFLS1100Q-7	Automotive	POWERDI [®] 123	3000/Tape & Reel

Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_compliance_definitions/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

	FOE	
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F09 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: B = 2014)

M = Month (ex: 9 = September)

Date Code Key												
Year	2013	20	14	2015	2016	20	017	2018	2019	20	20	2021
Code	А	E	В	С	D		E	F	G	ł	1	
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

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Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	100	V
RMS Reverse Voltage	V _{R(RMS)}	71	V
Forward Current rms ($T_C = +160^{\circ}C$, D = 0.5)	I _{F(RMS)}	2	А
Average Forward Current	I _{F(AV)}	1.0	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	50	А
Repetitive Peak Reverse Current tp = 2μs, f = I kHz Square	I _{RRM}	1.0	А
Repetitive Peak Avalanche Power tp = 1μs, TJ = +25°C	P _{ARM}	1500	W
Non-repetitive Peak Reverse Current tp = 100µs Square	I _{RSM}	1.0	А
Critical Rate of Rise of Reverse Voltage (Rated V_R , T_J = +25°C)	dV/dt	10000	V/µs

Thermal Characteristics

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance Junction to Soldering (Note 6)	R ₀ JS	—	7	2011
Thermal Resistance Junction to Ambient (Note 7) $T_A = +25^{\circ}C$	$R_{ heta JA}$	125	—	°C/W
Thermal Resistance Junction to Case (Note 7) $T_A = +25^{\circ}C$	R ₀ JC	21	—	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to	+175	O°

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	V _{(BR)R}	100	_	_	V	I _R = 1μA
		_	_	0.77		I _F = 1.0A, T _A = +25°C
Forward Voltage	N/	_	0.58	0.62	V	$I_F = 1.0A, T_A = +125^{\circ}C$
Forward voltage	V _F	—	_	0.86		I _F = 2.0A, T _A = +25°C
		—	0.65	0.7		$I_F = 2.0A, T_A = +125^{\circ}C$
Leakage Current (Note 8)		_		1	μA	$V_R = 100V, T_A = +25^{\circ}C$
Leakage Current (Note 6)	I _R	—	0.2	0.5	mA	V _R = 100V, T _A = +125°C
Total Capacitance	CT		36		pF	$V_R = 5V_{DC}, f = 1MHz$

Notes:

6. Theoretical R_{0JS} calculated from the top center of the die straight down to the PCB/cathode tab solder junction.
7. Part mounted on FR-4 board with 2oz., minimum recommended copper pad layout, which can be found on our website at http://www.diodes.com. 8. Short duration pulse test used to minimize self-heating effect.





= 150°C

T_A = 125°C

 $T_A = 75^{\circ}C$

 $T_A = 25^{\circ}C$

= -55°C

f = 1MHz

0.7 0.8 0.9 1.0

0.6

V_F, INSTANTANEOUS FORWARD VOLTAGE (V)

Fig. 2 Typical Forward Characteristics

10

0.1

0.01

0.001

0.0001

0.00001

100

80

60

40

20

0

100,000

10,000

1,000

100

10

1 0.01

0.

0

5

= 25°C

C_T, TOTAL CAPACITANCE (pF)

0.1

0

 $T_A = 175^{\circ}C$

0.2 0.3 0.4 0.5

10

15

V_R, DC REVERSE VOLTAGE (V)

Fig. 4 Total Capacitance vs. Reverse Voltage

20

10

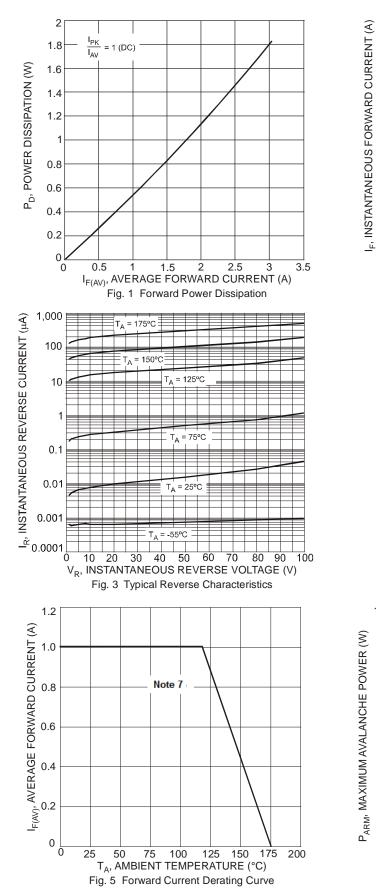
T_P, PULSE DURATION (μS)

Fig. 6 Maximum Avalanche Power Curve

100

25

30

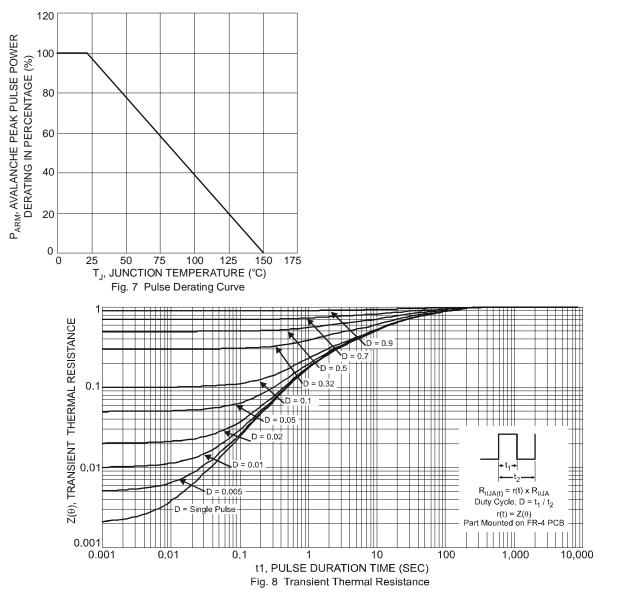


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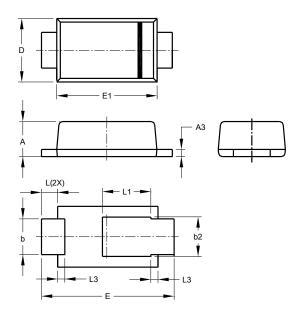






Package Outline Dimensions

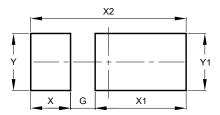
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



POWERDI [®] 123							
Dim	Min	Max	Тур				
Α	0.93	1.00	0.98				
A3	0.15	0.25	0.20				
b	0.85	1.25	1.00				
b2	1.025	1.125	1.10				
D	1.63	1.93	1.78				
E	3.50	3.90	3.70				
E1	2.60	3.00	2.80				
L	0.40	0.50	0.45				
L1	1.25	1.40	1.35				
L3	0.125	0.275	0.20				
All	Dimensi	ions in r	nm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
G	0.65
Х	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50



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