



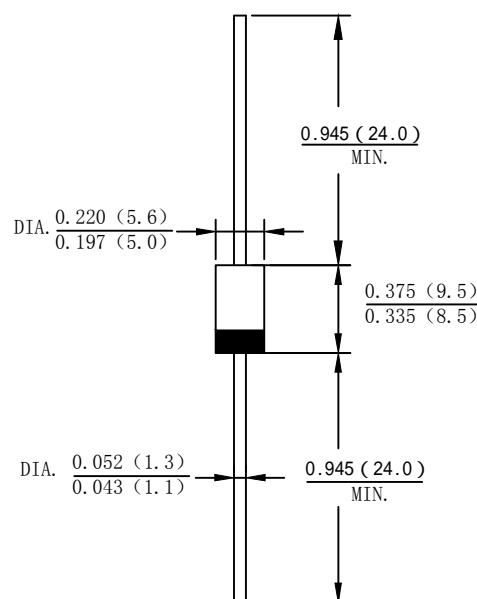
Features

- Schottky Barrier Chip
- High Thermal Reliability
- Patented Super Barrier Rectifier Technology
- High Forward Surge Capability
- Low power consumption, high efficiency
- Excellent High Temperature Stability
- Plastic material-UL flammability 94V-0

Mechanical Data

- Case: DO-201AD, molded plastic
- Terminals:Plated Leads Solderable per MIL-STD-202,Method 208
- Polarity:Cathode Band
- Mounting Position:Any
- Marking:Type Number
- Lead Free:For RoHS/Lead Free Version

Case: DO-201AD



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter	Symbol	SR10V45L	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	45	V
Working Peak Reverse Voltage	V_{RWM}		
DC blocking voltage	V_{DC}		
RMS Rectified Voltage	$V_{R(RMS)}$	32	V
Average Rectified Output Current	$I_{F(AV)}$	10	A
Non-Repetitive Peak Forward Surge 8.3ms Single Half Sine-Wave Superimposed on rated load(JEDEC Method)	I_{FSM}	245	A
I^2t Rating for Fusing ($t < 8.3\text{ms}$)	I^2t	249.1	A^2s
Forward Voltage Drop $T_A = 25^\circ\text{C}$ @ $I_F = 10\text{A}$	V_F	Max.	0.48
		Typ.	0.45
Peak Reverse Current At Rated DC Blocking Voltage	I_R	$T_A = 25^\circ\text{C}$ 0.2 $T_A = 100^\circ\text{C}$ 10	mA
Typical Junction Capacitance (Note 1)	C_J	590	pF
Typical Thermal Resistance Junction to Ambient	$R_{\theta JA}$	80	$^\circ\text{C/W}$
	$R_{\theta JL}$	15	
Operating junction temperature range	T_J	-55 to +150	$^\circ\text{C}$
storage temperature range	T_{STG}	-55 to +150	$^\circ\text{C}$

Note: 1. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C



Fig. 1 Forward Current Derating Curve

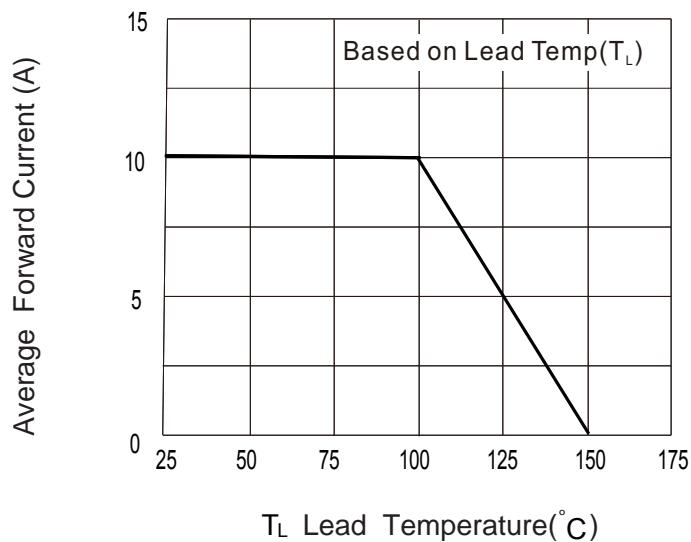


Fig. 2 Instantaneous Forward Characteristics

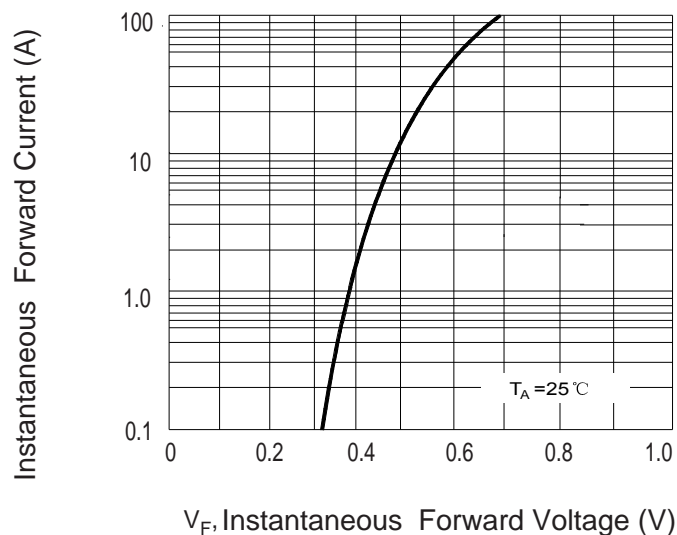


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

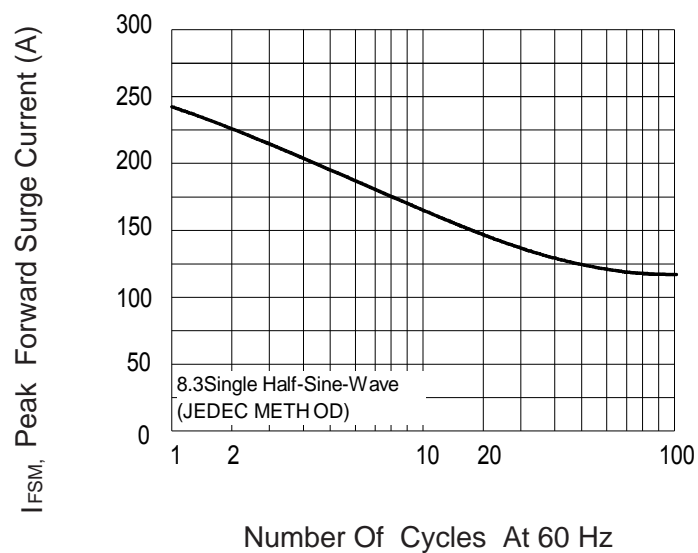
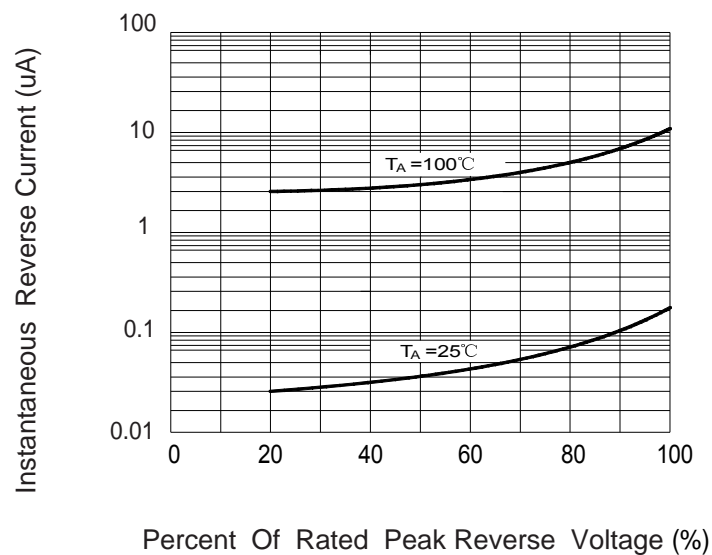


Fig. 4 Reverse Characteristics (per diode)





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