

## General Description

The TPMCP6001T is a single supply, low power CMOS operational amplifier; these amplifiers offer bandwidth of 1MHz, rail-to-rail inputs and outputs, and single-supply operation from 1.8V to 5.5V. Typical low quiescent supply current of 55µA in one operational amplifier within one chip and very low input bias current of 10pA make the devices an ideal choice for low offset, low power consumption and high impedance applications such as smoke detectors, photodiode amplifiers, and other sensors.

The TPMCP6001T is available in SOT23-5L packages.

The extended temperature range of -40 °C to +125 °C over all supply voltages offers additional design flexibility.

## Features

- Single-Supply Operation from +1.8V ~ +5.5V
- Rail-to-Rail Input / Output
- Gain-Bandwidth Product: 1MHz (Typ.)
- Low Input Bias Current: 10pA (Typ.)
- Low Offset Voltage: 5mV (Max.)
- Quiescent Current: 55µA per Amplifier (Typ.)
- Operating Temperature: -40°C ~ +125°C
- Available in SOT23-5L Packages

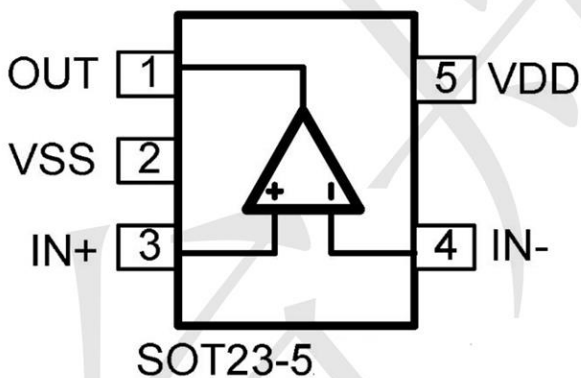
## Applications

- Portable Equipment
- Mobile Communications
- Smoke Detector
- Medical Instrumentation
- Battery-Powered Instruments
- Sensor Interface
- Handheld Test Equipment

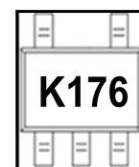
## Ordering Information

Part Number	Package	QTY Per Reel	Reel Size
TPMCP6001T-I/OT	SOT23-5	3000	7"

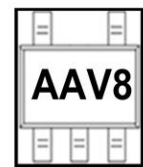
## Pin Assignments



## Marking:



Or



## Absolute Maximum Ratings

Condition	Min	Max
Power Supply Voltage ( $V_{DD}$ to $V_{SS}$ )	-0.5V	+7V
Analog Input Voltage ( $IN+$ or $IN-$ )	$V_{SS}-0.5V$	$V_{DD}+0.5V$
PDB Input Voltage	$V_{SS}-0.5V$	+7V
Operating Temperature Range	-40°C	+125°C
Junction Temperature		+150°C
Storage Temperature Range	-65°C	+150°C
Lead Temperature (soldering, 10sec)		+300°C
Package Thermal Resistance ( $T_A=+25^\circ\text{C}$ )		
SOT23-5L, $\theta_{JA}$		190°C
SOP-8L, $\theta_{JA}$		130°C

**Note:** Stress greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions outside those indicated in the operational sections of this specification are not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

## Electrical Characteristics

( $V_{DD} = +5V$ ,  $V_{SS} = 0V$ ,  $V_{CM} = 0V$ ,  $V_{OUT} = V_{DD}/2$ ,  $R_L = 100k\Omega$  tied to  $V_{DD}/2$ ,  $SHDNB = V_{DD}$ ,  $T_A = -40^\circ C$  to  $+125^\circ C$ , unless otherwise noted. Typical values are at  $T_A = +25^\circ C$ .) (Notes 1)

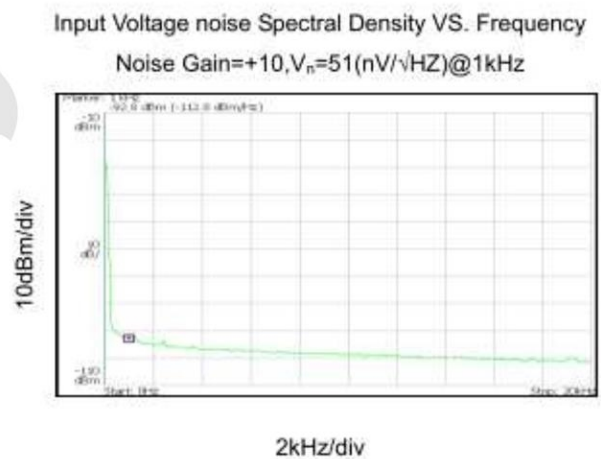
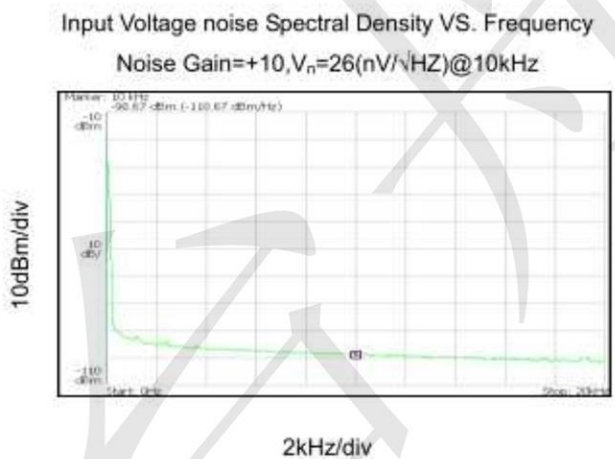
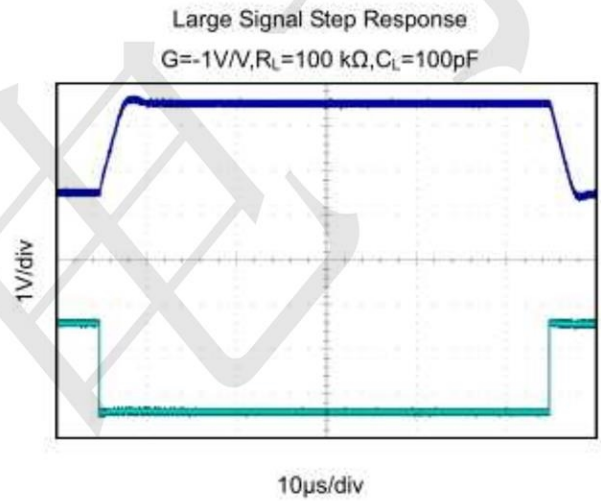
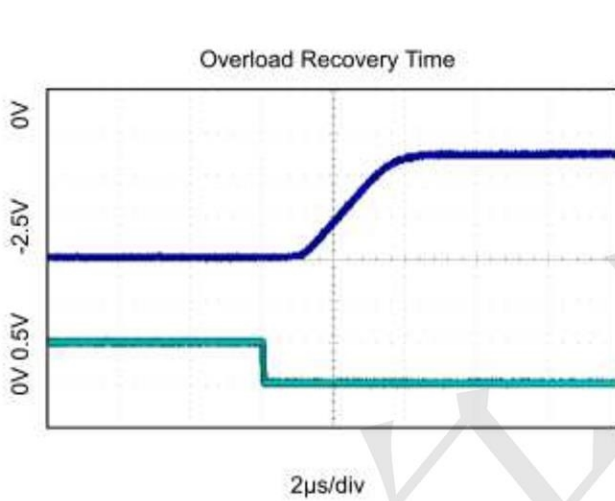
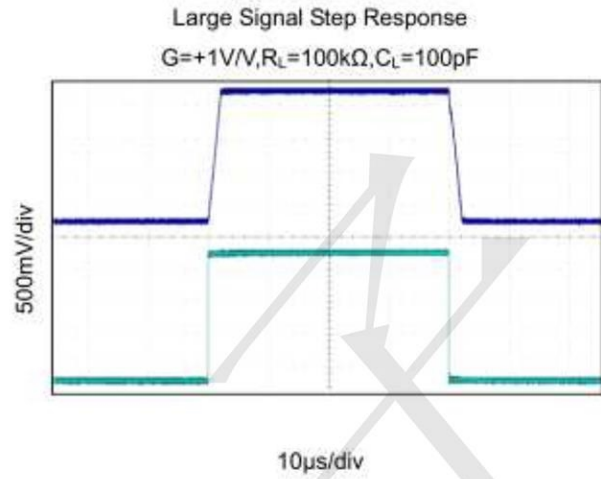
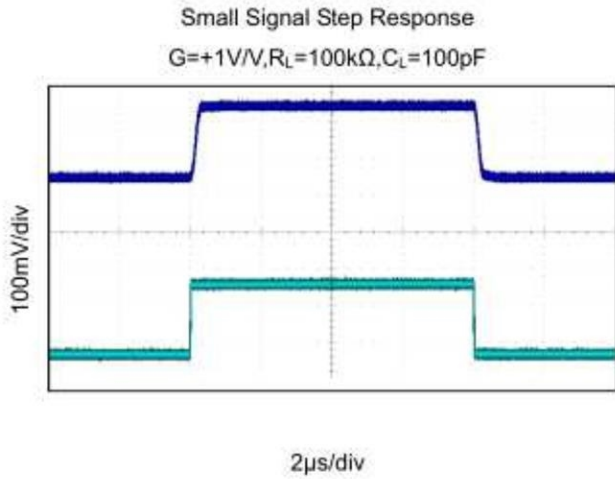
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Supply-Voltage Range	$V_{DD}$	Guaranteed by the PSRR test	1.8	-	5.5	V
Quiescent Supply Current (per Amplifier)	$I_Q$	$V_{DD} = 5V$	-	55	80	$\mu A$
Input Offset Voltage	$V_{OS}$		-	0.5	$\pm 5$	mV
Input Offset Voltage Tempco	$\Delta V_{OS}/\Delta T$		-	2	-	$\mu V/^\circ C$
Input Bias Current	$I_B$	(Note 2)	-	10	-	pA
Input Offset Current	$I_{OS}$	(Note 2)	-	10	-	pA
Input Common-Mode Voltage Range	$V_{CM}$		-0.1	-	$V_{DD}+0.1$	V
Common-Mode Rejection Ratio	CMRR	$V_{DD}=5.5 V_{SS}-0.1V \leq V_{CM} \leq V_{DD}+0.1V$	60	75	-	dB
		$V_{SS} \leq V_{CM} \leq 5V$	65	80	-	dB
Power-Supply Rejection Ratio	PSRR	$V_{DD} = +1.8V$ to $+5.5V$	75	90	-	dB
Open-Loop Voltage Gain	$A_V$	$V_{DD}=5V, R_L=100k\Omega$ , $0.05V \leq V_O \leq 4.95V$	90	100	-	dB
		$V_{DD}=5V, R_L=5k\Omega$ , $0.05V \leq V_O \leq 4.95V$	65	75	-	dB
Output Voltage Swing	$V_{OUT}$	$ V_{IN+}-V_{IN-}  \geq 10mV$ $V_{DD}-V_{OH}$	-	6	-	mV
		$R_L = 100k\Omega$ to $V_{DD}/2$ $V_{OL}-V_{SS}$	-	6	-	mV
		$ V_{IN+}-V_{IN-}  \geq 10mV$ $V_{DD}-V_{OH}$	-	60	-	mV
		$R_L = 5k\Omega$ to $V_{DD}/2$ $V_{OL}-V_{SS}$	-	60	-	mV
Output Short-Circuit Current	$I_{SC}$	Sinking or Sourcing	-	$\pm 20$	-	mA
Gain Bandwidth Product	GBW	$A_V = +1V/V$	-	1	-	MHz
Slew Rate	SR	$A_V = +1V/V$	-	0.6	-	V/ $\mu s$
Settling Time	$t_s$	To 0.1%, $V_{OUT} = 2V$ step $A_V = +1V/V$	-	5	-	$\mu s$
Over Load Recovery Time		$V_{INX}$ Gain= $V_S$	-	2	-	$\mu s$
Input Voltage Noise Density	$e_n$	$f = 1kHz$	-	50	-	$nV/\sqrt{Hz}$
		$f = 10kHz$	-	20	-	$nV/\sqrt{Hz}$

**Note 1:** All devices are 100% production tested at  $T_A = +25^\circ C$ ; all specifications over the automotive temperature range is guaranteed by design, not production tested.

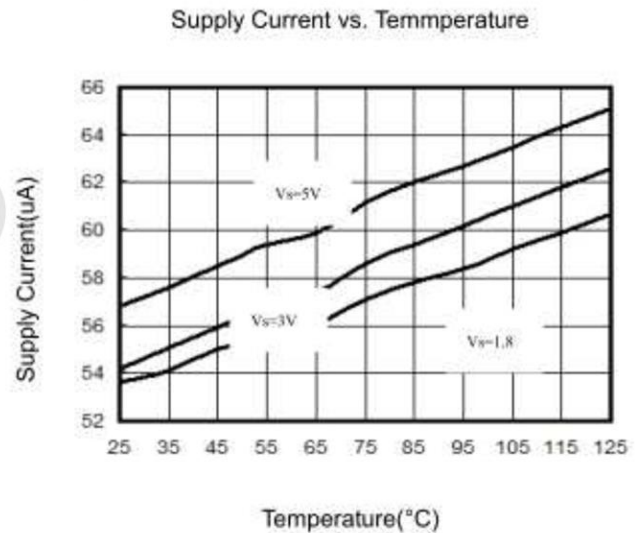
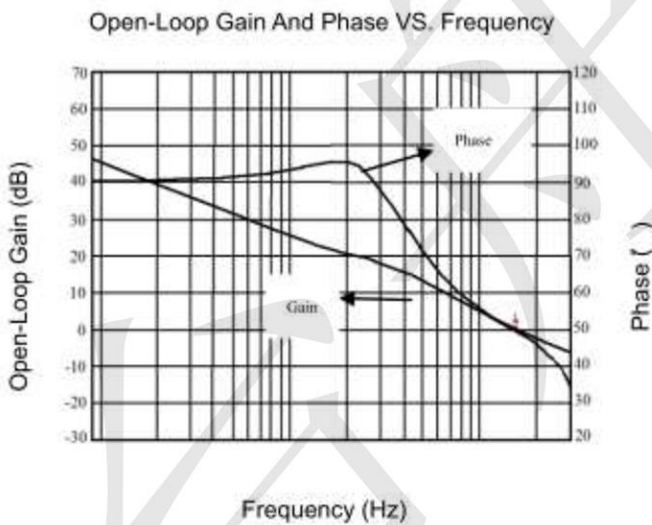
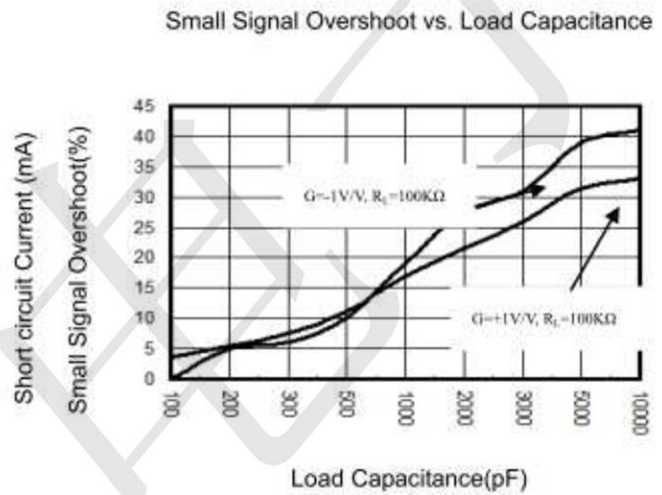
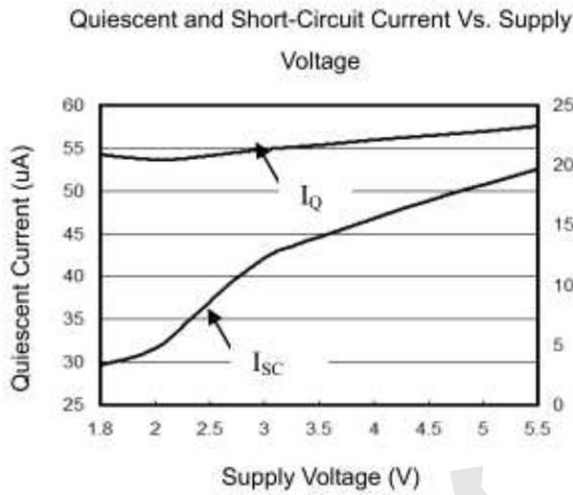
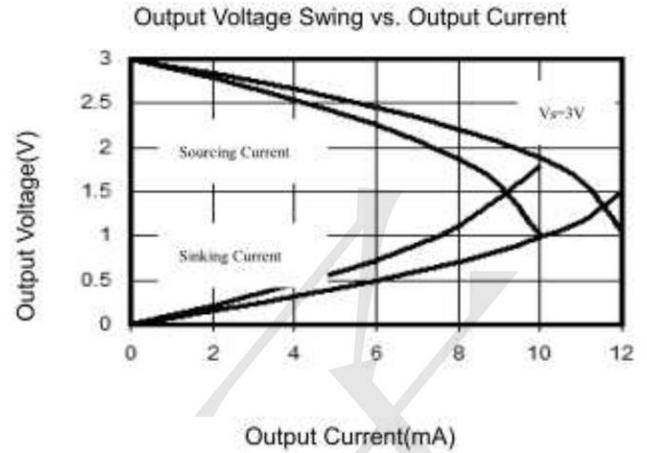
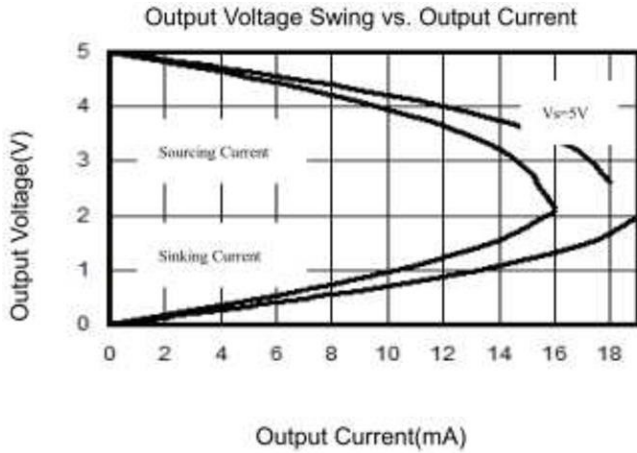
**Note 2:** Parameter is guaranteed by design.

### Typical characteristics

At  $T_A=+25^\circ\text{C}$ ,  $R_L=100\text{ k}\Omega$  connected to  $V_S/2$  and  $V_{OUT}=V_S/2$ , unless otherwise noted.



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## Package Information

### SOT23-5

