

POE-D18-00-E-02

PR PR	ODUCT SPECIFI oduct: CERAMIC DISC CAPA <u>SAFETY RECOGNIZE</u>	CATION CITOR D
	TYPE: AS SERIES	
C	USTOMER:	
	DOC. NO.: POE-D18-00-E-02	
F	APPROVED BY CUSTOM	ER
VENDOR VALSIN 566-1, KAC TAO-YUAN PAN OV NO.277,HC GUANG ZH DEVELOPP DONGG NO.638, MI	TECHNOLOGY CORPORATION SHI ROAD, YANG-MEI , TAIWAN ERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD. NG MING ROAD, EASTERN SECTION, IOU ECONOMIC AND TECHNOLOGY MENT ZONE, CHINA JAN WALSIN TECHNOLOGY ELECTRONICS CO., LTD. EI JING WEST ROAD, XINIUPO, ADMINISTRATIVE	wish
ZONE, DAL MANUFA PAN OVE NO.277, HON GUANG ZH DEVELOPM DONGG NO.638, NE	ANGTOWN,DONGGUAN CITY, GUANGDONG PROVINCE CTURE SITE : RSEAS (GUANGZHOU) ELECTRONIC CO.,LTD. G MING ROAD,EASTERN SECTION, DU ECONOMIC AND TECHNOLOGY ENT ZONE,CHINA JAN WALSIN TECHNOLOGY ELECTRONICS CO., LTD. JING WEST ROAD,XINIUPO,ADMINISTRATIVE NGTOWN DONGGUAN CITY, GUANGDONG PROVINCE	



POE-D18-00-E-02

SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES

Ver: 2

Page: 2 / 18

Record of change						
Date	Version	Description	page			
2014/11/19	1	First edition.	all			
2016/1/27	2	 Review the Available lead code of Lead Configuration. Revised standard NO. of VDE. 	5 9			

Э



Ver: 2

Page: 3 / 18

Table of Contents

•

No.	Item	Page
1	Part number for SAP system	4
2	Mechanical	5
3	Part numbering/T.C/Capacitance/ Tolerance/Diameter	6
4	Taping format	7
5	Marking	8
6	Scope	9
7	Specification and test method	10~13
8	Packing specification	14
9	Notices	15~17
10	Drawing of Internal Structure and material list	18

								F	OE-D18-00-E-0)2			
SAI INS	SAFETY STANDARDS REGULATED, REINFORCEDVer: 2INSULATION TYPE, AS SERIES									Page: 4 / 18	>		
1. Par	. Part number for SAP system:												
(E	x.)	YU	0AS	472	M	14	0	L	20	С	0	H	
		(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	
(1)]	Гетр	erature c	haracteris	tic (ident	ified cod	e)							
[CODE	YP	(Y5P)	YU (Y5U)]						
Cap. Change ±10% -55% to +20%													
(2)]	(2)TYPE (identified by 3-figure code) : $0AS = AS Type(X1:760V \sim /Y1:500V \sim)$,												

(3)Capacitance (identified by 3-figure code):EX.472=4700pF

(4)Capacitance tolerance (identified by code): $K:\pm 10\%, M:\pm 20\%$

(5)Nominal body diameter dimension (identified by 2-figure code): 07--Dmax8.0mm, 08--Dmax9.0mm...

(6)Internal code: 0--Normal, other code--Special control

(7)Lead Style : Refer to "2. Mechanical".

(8)Packing mode and lead length (identified by 2-figure code)

Taping Code	Description
AM	Ammo box and product pitch : 25.4 mm

Bulk Code	Description						
3E	Lead length : 3.5mm						
04	Lead length : 4.0mm						
4E	Lead length : 4.5mm						
20	Lead length : 20mm						

(9)Length tolerance

Code	Description
А	±0.5 mm
	(only for kink lead type)
В	±1.0 mm
С	Min.
D	Taping special purpose

(10)Pitch

Code	Description
0	10±1 mm
А	10±0.5 mm
С	12.5± 0.8 mm

(11)Epoxy Resin Code

Code	Description
Н	Halogen and Pb free, epoxy resin.



Page: 5 / 18

Ver: 2

2. Mechanical:

Encapsulation : Epoxy resin, flammability UL94 V-0

Available lead code (unit: mm):

Lead type	SAP P/N (13-17)digits	Pitch (F)	Lead Length (L)	Packing	Lead Configuration		
Lead style : L Type L	L20C0	10 ± 1.0	20 min.	Bulk	Dmax. Tmax.		
Straight long lead	L20CC	12.5 ± 0.8	20 min.	Bulk			
Lead style : B Type B Straight long lead	BAMD0	10 ± 1.0	Refer to "4. Taping format"	Tap. Ammo	Ød		
	L03B0	10 ± 1.0	3.0 ± 1.0		Dmax. Tmax.		
Lead style:L	L4EB0	10 ± 1.0	4.5 ± 1.0				
Type L Straight short	L05B0	10 ± 1.0	5.0 ± 1.0	Bulk			
lead	L04BC	12.5 ± 0.8	4.0 ± 1.0				
	D3EA0	10 ± 1.0	3.5 ± 0.5		Dmax. Tmax.		
Lead style:D Type D	D04A0	10 ± 1.0	4.0 ± 0.5	Bulk			
Vertical kink lead	D04AC	12.5 ± 0.8	4.0 ± 0.5		¢d 4 4		
icut	DAMD0	10 ± 1.0	Refer to "4. Taping format"	Tap. Ammo	╵╧╼╢╾║╘┇ ┍╼╧╼┤		
	X3EA0	10 ± 1.0	3.5 ± 0.5		Dmax. Tmax.		
Land style ' V	X04A0	10 ± 1.0	4.0 ± 0.5	Bulk			
Type X	X05B0	10 ± 1.0	5.0 ± 1.0				
Outside kink lead	XAMD0	10 ± 1.0	Refer to "4. Taping format"	Tap. Ammo			

POE

* Lead diameter Φ d: 0.55 +/-0.05mm

*e (Coating extension on leads): 3.0mmMax for straight lead style, not exceed the kink for kink lead.



POE-D18-00-E-02

SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES

Ver: 2 Pag

Page: 6 / 18

3. Part numbering/T.C/Capacitance/ Tolerance/Diameter :

SAP P/N	тс	Canacitance(nE)	Tolerance	Dimension (unit:mm)			nm)
5/11/11	1.C.	Capacitance(pr)	Tolerance	D(max.)	T(max.)	F	Φd
YP*AS101K070*		100 pF		8.0			
YP*AS151K070*		150 pF		8.0	7.0	10.0	0.55+/-0.05
YP*AS221K070*		220 pF		8.0			
YP*AS331K070*	V5D	330 pF	+10%	8.0			
YP*AS471K080*	135	470 pF	1070	9.0			
YP*AS561K090*		560 pF		10.0			
YP*AS681K090*		680 pF		10.0			
YP*AS102K110*		1000 pF		12.0	7.0		
YU*AS102M080*		1000 pF		9.0			
YU*AS152M090*		1500 pF		10.0			
YU*AS222M120*	Y5U	2200 pF	+ 2004	13.0			
YU*AS332M120*		3300 pF	±20%	13.0			
YU*AS392M130*		3900 pF	1	14.0			
YU*AS472M140*		4700 pF]	15.0			

POE

• The minimum thickness of coating (reinforced insulation) is 0.4mm.

		POE-D18-00-E-02
SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES	Ver: 2	Page: 7 / 18

4. Taping format:

• 25.4mm pitch/lead spacing 10.0mm taping



POE Part Number		*BAMD0 / *DAMD0 / *XAMD0
Item	Symbol	Dimensions(mm)
Pitch of component	Р	25.4 ± 2
Pitch of sprocket	P0	12.7 ± 0.3
Lead spacing	F	10.0 ± 1.0
Length from hole center to component center	P2	12.7 ± 1.5
Length from hole center to lead	P1	7.7 ± 1.5
Body diameter	D	See the "3. Part numbering/T.C/Capacitance/ Tolerance/Diameter"
Deviation along tape, left or right	$ riangle \mathbf{S}$	0 ± 2.0
Carrier tape width	W	18.0 +1/ -0.5
Position of sprocket hole	W1	9.0 ± 0.5
Lead distance between the kink and center of sprocket hole	H0 H0 (For: *DAMD0 & *XAMD0)	
Lead distance between the bottom of body and the center of sprocket hole	Н	20.0+1.5/-1.0 (For: *BAMD0)
Length from the terminal of the lead wire to the edge of carrier tape	l	2.0min (Or the end of lead wire may be inside the hole-down tape.)
Diameter of sprocket hole	D0	4.0 ± 0.2
Lead diameter	φd	0.55 ± 0.05
Total tape thickness	t1	0.6 ± 0.3
Total thickness, tape and lead wire	t2	1.5 max.
Deviation across tang	\triangle h1	2.0 max.
Deviation across tape	\triangle h2	2.0 max
Portion to cut in case of defect	L	11.0 max.
Hole-down tape width	W0	8.0 min
Hole-down tape distortion	W2	1.5 ± 1.5
Coating extension on leads	e	3.0 max for straight lead style; Not exceed the kink leads for kink lead.
Body thickness	Т	See the "3. Part numbering/T.C/Capacitance/ Tolerance/Diameter"

5

Ver: 2

POE-D18-00-E-02

5. Marking:



POE

		POE-D18-00-E-02
SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES	Ver: 2	Page: 9 / 18

6. Scope:

This specification applies to ceramic insulated capacitors disk type used in electronic equipment.

6.1Applicable safety standard

This specification applies to the VDE, ENEC10,UL/CUL approved ceramic capacitors disc type for antenna coupling, line-by-pass and across-the-line. X1, Y1 capacitor based on IEC60384-14.

	11	U		
Safety Standard	Standard No.	Subclass	W.V.	Recognized No.
UL/CUL	ANSI/UL 60384-14:2009	X1	760VAC	E146544(FOWX2/FOWX8)
		Y1	500VAC	
VDE	EN 60384-14:2013	X1	760VAC	40039265
(ENEC)	IEC60384-14:2013	Y1	500VAC	+0037205

6.2 Safety standards approval and recognized no.



7. Specification and test method:

7.1 Operating Temperature Range: -40 to +125°C

7.2 Test condition:

Test and measurement shall be made at the standard condition. (temperature $15 \sim 35^{\circ}$ C, relative humidity $45 \sim 75\%$ and atmospheric pressure $860 \sim 1060$ hpa). Unless otherwise specified herein.

If doubt occurred on the value of measurement, and measurement was requested by customer capacitors shall be measured at the reference condition. (temperature $20\pm2^{\circ}$ C or $25\pm2^{\circ}$ C, relative humidity 60~70% and atmospheric pressure 860~1060hpa.)

7.3 Performance:

No	Items		Performance	Testing method	
7.3.1	Appo And d	earance imension	The appearance and dimension shall be as given in section 3.	Visual check.	
7.3.2	Marking		The marking shall be easily legible. (As given section 5)	Visual check.	
		Between terminals	No failure.	The capacitors shall not be damage when AC4000V (rms.) are applied between the lead wires for 60sec. (Charge/Discharge current \Box 50mA.)	
7.3.3 Withstand voltage Body Insulation		No failure.	First. The terminals of the capacitor shall be closely wrapped around the body of the capacitor distance of about 3 to 4mm from each terminal. Then, the capacitor shall be inserted into a container filled with metal balls of about 1mm diameter. Finally, AC4000 ^o (rms.) is applied for 60sec between the capacitor lead wires and metal balls. (Charge/Discharge current \Box 50mA.)		
7.3.4	Insulation Resistance	Between terminals	10000MΩ or more.	The insulation resistance shall be measured with DC500±50V within 60±5sec of charging.	
7.3.5	Capa	citance	Within specified tolerance.	Y5P&Y5U: The capacitance shall be measured at $20\pm2^{\circ}$ C with 1kHz $\pm20\%$ and 5V(rms.) or less.	
7.3.6	5 Dissipation Factor(tanδ) or Q		Y5P、Y5U∶D.F.≦2.5%		
7.3.7	Temperature Characteristic		Char.Capacitance ChangeY5PWithin ± 10%Y5UWithin ± 20 %	The capacitance measurement shall be made at each step specified in Table 1. Table 1 Step Temperature (°C) 1 +20 \pm 2 2 -25 \pm 2 3 +20 \pm 2 4 +85 \pm 2 5 +20 \pm 2 Pre-treatment: Capacitor shall be stored at 85 \pm 2°C for 1hour, then placed at ^{**1} room condition for 24 \pm 2hours before measurements.	
7.3.8	Solderability of Leads		Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.	The lead wire of capacitor should be dipped into molten solder for 5 ± 0.5 sec. The depth of immersion is up to about 1.5 to 2.0 mm from the root of lead wires. Temp. of solder : Lead Free Solder (Sn-3Ag-0.5Cu) 245±5°C	



POEscerer POE

SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES

Ver: 2

Page: 11 / 18

No	Items		Performance	Testing method		
		Tensile	Lead wire shall not cut off. Capacitor shall not be broken.	With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; the tensile force of 10N shall be applied to the termination in the direction of its axis and acting in a direction away from the body of the specimen.		
7.3.9	Robustness of Terminations	Bending	Lead wire shall not cut off. Capacitor shall not be broken.	With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a mass applying a force of 5N is then suspended from the end of the termination. The body of the specimen is then inclined, within a period of 2 to 3sec, through an angle of approximately 90 in the vertical plane and then returned to its initial position over the same period of time; this operation constitutes one bend. One bend immediately followed by a second bend in the opposite direction.		
		Appearance	No marked defect.	As shown in figure, the lead wires should be immersed in solder of 350 ± 10 °C or 260 ± 5 °C up to 1.5 to 2.0 mm from		
		I.R.	1000 MΩ min.	the root of terminal for 3.5 ± 0.5 sec (10 ± 1 sec. for 260 ± 5 °C).		
		Dielectric Strength	Per item7.3. 3	Thermal Capacitor Screen		
7.3.10	Soldering Effect (Non-Preheat)	Capacitance	Y5P,Y5U: Within ±10 %	Pre-treatment: Capacitor shall be stored at 85±2°C for 1hour.then placed at ^{**1} room condition for 24±2hours before initial measurements. Post-treatment: Capacitor shall be stored for 1 to 2hours at ^{**1} room condition.		
		Appearance	No marked defect.	First the capacitor should be stored at $120+0/-5$ °C for 60 +0/-5 sec. Then , as in figure , the lead wires should be immersed solder of $260+0/-5$ °C up to 1.5 to 2.0 mm from the root of terminal for 7.5+0/-1 sec. Thermal Capacitor Screen 1.5 1.5		
7.3.11	Soldering	I.R.	1000 M Ω min.			
	Effect (On-Preheat)	Dielectric Strength	Per item 7.3.3			
		Capacitance	Y5P,Y5U : Within ±10 %	Pre-treatment: Capacitor shall be stored at 85±2°C for 1hour.then placed at ^{**1} room condition for 24±2hours before initial measurements. Post-treatment: Capacitor shall be stored for 1 to 2hours at ^{**1} room condition.		



SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES

Ver: 2

Page: 12 / 18

No	Iten	ns	Performance	Testing method	
Appearance No marked defect.					
	Humidity	Capacitance	Y5P : Within $\pm 10\%$ Y5U : Within $\pm 20\%$	Set the capacitor for 500±12hours at $40\pm2^{\circ}$ C in 90 to 95%	
7.3.12	(Under steady	D.F.	Y5P,Y5U : 5.0% max.	Then capacitor shall be stored for 1 to 2 hours at *1 room	
	State)	I.R.	Y5P&Y5U : 3000MΩmin.	condition.	
		Dielectric Strength	Per Item 7.3.3		
		Appearance	No marked defect.	-	
	TT	Capacitance	Y5P: Within ±10% Y5U: Within ±20%	Apply the rated voltage for 500 ± 12 hours at $40\pm2^{\circ}$ C in	
7.3.13	Loading	D.F.	Y5P,Y5U : 5.0% max.	90 to 95% relative humidity and set it for 1 to 2 hours	
	Louding	I.R.	Y5P&Y5U : 3000MΩmin.	at ^{**1} room condition.	
		Dielectric Strength	Per Item 7.3.3		
		Appearance	No marked defect.	Impulse Voltage	
7.3.14	Life	Capacitance	Y5P&Y5U:Within ±20%	Each individual capacitor shall be subjected to 8kV impulses for three times. After the capacitors are applied to life test. Fig. 2 Vp UCR Cx tr $tdWF$ WS WS	
		I.R.	3000MΩ min.	$ \begin{array}{c} \frac{\partial \mathcal{G}}{\partial \mathcal{G}} \\ \frac{\partial \mathcal{G}}{\partial \mathcal{G}} $	
		Dielectric Strength	Per Item 7.3 3	Time The specimen capacitors are placed in a circulating air oven for a period of 1000 hours. The air in the oven is maintained at a temperature of $125\pm3^{\circ}$ C. Throughout the test, the capacitors are subjected to an AC850Vrms. alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(rms.) for 0.1 sec.	
7.3.15	7.3.15 Flame Test The capacitor flame Cycle Time 1~4 30sec max. 5 60sec max.		Cycle Time 1~4 30sec max. 5 60sec max.	The capacitor shall be subjected to applied for 15 sec and then removed for 15 sec until 5 cycles. Fig. 5	



POE-D18-00-E-02

SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES

Page: 13 / 18

No		Items	Performance	Testing method		
7.3.16	Fla	Active mmability	The cheesecloth shall not be on fire.	The specimens shall be individually wrapped in at least one but more than two complete layers of cheesecloth. The specimens shall be subjected to 20 discharges. The interval between successive discharges shall be 5 sec. The UAC shall be maintained for 2 min after the last discharge. Fig. 6 fig. 6 C1.2 : 1µF±10% C1 = C2 = C3 = C4 = C4 = C4 = U4 C3 = 0.033µF±5% 10kV C4 = 3µF±5% 10kV C5 = 0.033µF±5% 10kV C5 = C1.2 : UAC = U4 = 1.5mH±20% C6 = C1.2 : 1µF±10% C6 = C2 = C3 = C4 = C4 = U4 C6 = C4 = U4 C7 = C4 = U4 C8 = C4 = U4 C9 = U4 C1.2 : 1µF±10% C9 = U4 C1.2 : 1µF±10% C1.2 : 1µF±10% C1 = C4 C1.2 : 1µF±10% C1 = C4 C1		
7.3.17	7 Passive Flammability		The burning time shall not be exceeded the time 30 sec. The tissue paper shall not ignite.	The capacitor under test shall be held in the position which best promotes burning. Each specimen shall only be exposed once to flame. Time of exposure to flame: 30sec. Length of flame : 12±1mm Gas burner : Length 35mm min. Inside Dia. : 0.5±0.1mm Outside Dia. : 0.9mm max. Gas : Butane gas Purity 95% min. Fig. 7		
		Appearance	No marked defect	The capacitor should be subjected to 5 temperature cycles,		
7.3.18	Temperat	$\begin{array}{c} \text{Char. Cap.}\\ \text{Change}\\ \text{Y5P} \leq \pm 10\%\\ \text{Y5U} \leq \pm 20\% \end{array}$	DF DF≦5.0% DF≦7.5%	Step Temperature Cycle time: 5cycles> 1 -40+0/-3 30 2 Room temp. 3 3 125+3/-0 30		
/.3.18	ure Cycle	I.R. Dielectric strength	3000MΩ min. Per Item 7.3.3	4 Room temp. 3 Pre-treatment: Capacitor shall be stored at 85±2°C for 1hour.then placed at ^{**} ¹ room condition for 24±2hours. Post-treatment: Capacitor shall be stored for 1 to 2hours at ^{**1} room condition.		

POE

*1"room condition" Temperature:15~35, Relative humidity: 45~75%, Atmospheric pressure:86~106kPa



8.Packing Baggage :





8.2 Packing quantity:

Packing type	The code of 14th to15th in SAP P/N	MPQ (Kpcs/Box)
Taping	AM (The size code ≤ 110)	1
	AM (The size code \geq 120)	0.5

Packing type	Lead length	Size code of 10th to 11th in SAP P/N	MPQ (Kpcs/Bag)	Kpcs/Box
Bulk	Long lead	06~12	0.5	1.5
	(L \geq 20mm)	13-15	0.5	1
	Short lead (L<20mm)	06~14	0.5	2
		15	0.2	1
	All	16	0.2	1

		POE-D18-00-E-02
SAFETY STANDARDS REGULATED, REINFORCED	Ver: 2	Page: 15 / 18
INSULATION TYPE, AS SEKIES		0

9. Notices:

9.1 Caution (Rating):

(1). Operating Voltage

Be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range.

When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing this irregular voltage.



(2). Operating Temperature and Self-generated Heat

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or the like, it may have the self-generated heat due to dielectric-loss.

Applied voltage should be the load such as self-generated heat is within 20° C on the condition of atmosphere temperature 25° C. When measuring, use a thermocouple of small thermal capacity-K of φ 0.1mm and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings. Excessive heat my lead to deterioration of the capacitor's characteristics and reliability.

- (3). Test condition for withstanding Voltage
 - I. Test Equipment

Test equipment for AC withstanding voltage shall be used with the performance of the wave similar to 50/60 Hz sine waves.

If the distorted sine wave or over load exceeding the specified voltage value is applied, the defective may be caused.

II. Voltage Applied Method

When the withstanding voltage is applied, capacitor's lead or terminal shall be firmly connected to the output of the withstanding voltage test equipment, and then the voltage shall be raised from near zero to the test voltage.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the *zero cross. At the end of the test time, the test voltage shall be reduced to near zero, and then capacitor's lead or terminal shall be taken off the output of the withstanding voltage test equipment.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may arise, and therefore, the defective may be caused.

ZERO CROSS is the point where voltage sine wave pass 0V.- See the right figure.



(4). Fail-Safe

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

9.2 Caution (Storage and operating condition):

Operating and storage environment

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed –10 to 40 degrees centigrade and 15 to 85 %. Use capacitors within 6 months.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

9.3 Caution (Soldering and Mounting):

9.3.1 Vibration and impact:

Do not expose a capacitor or its leads to excessive shock or vibration during use.



9.3.2 Soldering:

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip: 400 degrees C. max.

Soldering iron wattage: 50W max.

Soldering time: 3.5 sec. max.

9.3.3 Cleaning (ultrasonic cleaning):

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

Rinsing time:5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

9.4 Caution (Handling):

Vibration and impact

Do not expose a capacitor or its leads to excessive shock or vibration during use.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

		POE-D18-00-E-02
SAFETY STANDARDS REGULATED, REINFORCED INSULATION TYPE, AS SERIES	Ver: 2	Page: 18 / 18

10. Drawing of internal structure and material list:



Remarks :

No.	Part name	Material	Model/Type	Component
1	Insulation Coating	Epoxy polymer	1.EF-150 2.PCE-300	Epoxy resin、Pigment (Blue / UL 94 V-0) The minimum thickness of coating (reinforced insulation) is 0.4mm
2	Dielectric Element	Ceramic	Y5P/Y5U	BaTiO ₃
3	Solder	Tin-silver	Sn96.5-Ag3-Cu0.5	Sn96.5-Ag3-Cu0.5
4	Electrodes	Ag	1.SP-160PL 2.SP-260PL	Silver Glass frit
5	Leads wire	Tinned copper clad steel wire	0.55±0.05mm	Substrate metal: Fe & Cu Surface plating: Sn 100%(3~7µm)