BAV101 SERIES Taiwan Semiconductor

200mA, 250V Surface Mount Switching Diode

FEATURES

- Low power loss, high efficiency
- High surge current capability
- Hermetically sealed glass
- RoHS Compliant

APPLICATIONS

- Switching mode power supply (SMPS)
- Adapters
- Lighting application
- On-board DC/DC converter

MECHANICAL DATA

- Case: MMELF
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Polarity: Indicated by cathode band
- Weight: 30.60mg (approximately)

KEY PARAMETERS				
PARAMETER	VALUE	UNIT		
I _F	200	mA		
V _{RRM}	250	V		
I _{FSM}	4	А		
V_F at I_F = 100mA	1	V		
T _{J MAX}	200	°C		
Package	MMELF			
Configuration	Single die			



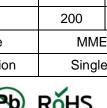


MMELF



ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)				
PARAMETER		SYMBOL	VALUE	UNIT
Repetitive peak reverse voltage		V _{RRM}	250	V
Forward current		I _F	200	mA
Non repetitive peak forward ourse oursent			1	А
Non-repetitive peak forward surge current	t = 1µs	IFSM	4	А
Junction temperature range		TJ	-65 to +200	°C
Storage temperature range		T _{STG}	-65 to +200	°C

THERMAL PERFORMANCE			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-ambient thermal resistance	R _{eja}	300	°C/W







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ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)						
PARAMETER		CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
Forward voltage ⁽¹⁾		$I_F = 100 \text{mA}, T_J = 25^{\circ}\text{C}$	V _F	-	1	V
Reverse current @ rated V _P ⁽²⁾	BAV101	$V_{R} = 100V, T_{J} = 25^{\circ}C$	I _R	-	100	nA
	BAV103	$V_R = 200V, T_J = 25^{\circ}C$		-	100	nA
Junction capacitance		1MHz, $V_R = 0V$	CJ	-	4	pF

Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

ORDERING INFORMATION			
ORDERING CODE	PACKAGE	PACKING	
BAV101 L0G	MMELF	10,000 / 13" Tape & Reel	
BAV103 L0G	MMELF	10,000 / 13" Tape & Reel	



CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

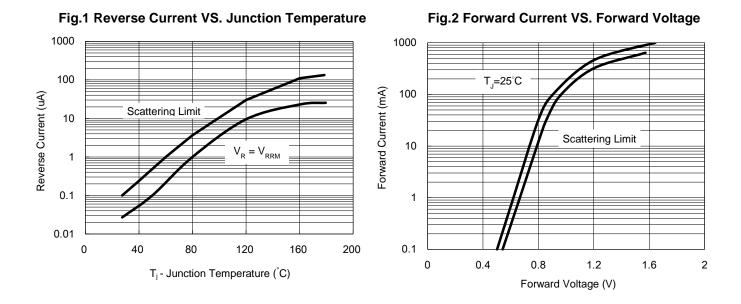
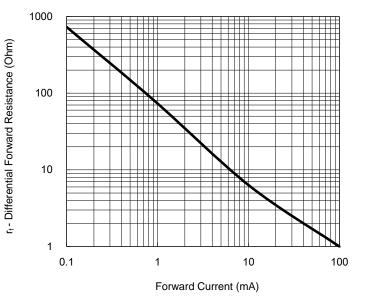
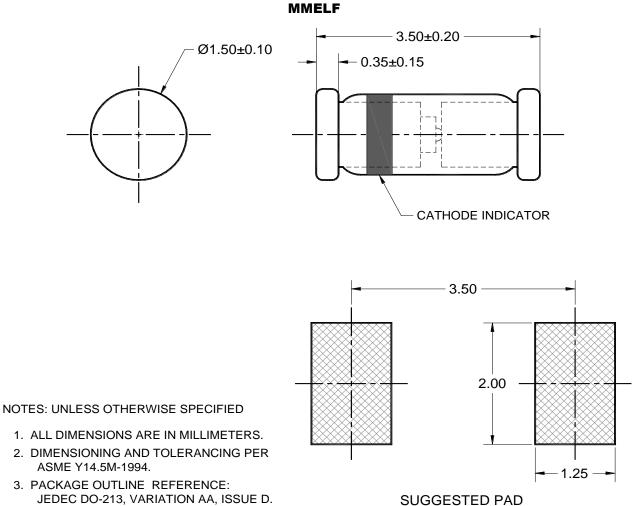


Fig.3 Differential Forward Resistance VS. Forward Current





PACKAGE OUTLINE DIMENSIONS



4. DWG NO. REF: HQ2SD07-MMELFG-044 REV A.

SUGGESTED PAD LAYOUT



Taiwan Semiconductor

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