

TIP29A TIP29C

NPN power transistors

Features

NPN transistors

Applications

■ Audio, linear and switching applications

Description

The devices are manufactured in Planar technology with "Base Island" layout. The resulting transistor shows exceptional high gain performance coupled with very low saturation voltage. The PNP types are TIP30A and TIP30C.

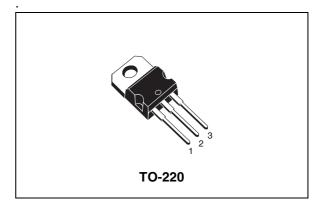


Figure 1.	Internal	schematic	diagram
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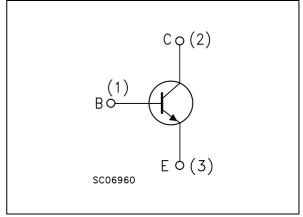


Table	1.	Device	summary
Iabic		DCVICC	Summary

Order codes	Marking	Package	Packaging
TIP29A	TIP29A	TO-220	Tube
TIP29C	TIP29C	TO-220	Tube

1 Absolute maximum ratings

Symbol	Parameter	Value		Unit
		TIP29A	TIP29C	
V _{CBO}	Collector-base voltage $(I_E = 0)$	60	100	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	60	100	V
V _{EBO}	Emitter-base voltage ($I_C = 0$)	5 V		V
۱ _C	Collector current	1 A		
I _{СМ}	Collector peak current (t _p < ms)	3 A		А
۱ _B	Base current	0.4 A		А
P _{TOT}	Total dissipation at $T_c \le 25^{\circ}C$ 30		W	
. 101	Total dissipation at $T_{amb} \le 25^{\circ}C$		2	W
T _{stg}	Storage temperature	temperature -65 to 150		°C
TJ	Max. operating junction temperature 150		°C	

2 Electrical characteristics

 $(T_{case} = 25^{\circ}C; unless otherwise specified)$

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{CEO}	Collector cut-off current	for TIP29A V _{CE} =30V			0.3	mA
	(I _B = 0)	for TIP29C V _{CE} =60V			0.3	mA
	Collector cut-off current	for TIP29A V _{CE} =60V			0.2	mA
I _{CES}	(V _{BE} = 0)	for TIP29C V _{CE} =100V			0.2	mA
I _{EBO}	Emitter cut-off current	V _{FB} =5V			1	mA
'EBO	$(I_{\rm C} = 0)$	LEB-01				
	Collector-emitter	I _C =30mA				
V _{CEO(sus)} ⁽¹⁾	sustaining voltage	for TIP29A	60			V
	(I _B = 0)	for TIP29C	100			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C =1A I _B =125mA			0.7	v
$V_{BE}^{(1)}$	Base-emitter voltage	I _C =1A V _{CE} =4V			1.3	V
h _{FE} ⁽¹⁾	DC surrent asis	I _C =0.2A V _{CE} =4V	40			
UFE` '	DC current gain	$I_{C} = 1A$ $V_{CE} = 4V$	15		75	

 Table 3.
 Electrical characteristics

1. Pulsed duration = 300 ms, duty cycle $\ge 1.5\%$.



HV31050

=150 °C

2.1 Electrical characteristic (curves)



Figure 3. DC current gain

TTL

T_J=25 °C

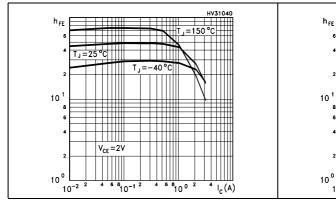
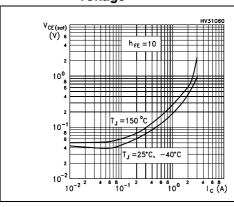


Figure 4. Collector-emitter saturation Figure 5. voltage



 $\frac{10^{\circ} \frac{10^{\circ} - 2^{\circ} - 4^{\circ} + 6^{\circ} + 10^{\circ} - 1^{\circ} - 4^{\circ} + 6^{\circ} + 10^{\circ} - 2^{\circ} + 1_{c}(A)}{10^{-2^{\circ} - 2^{\circ} - 4^{\circ} + 6^{\circ} + 10^{\circ} - 2^{\circ} + 1_{c}(A)}$ $= 5. \quad \text{Base-emitter saturation}$

T」=−40°C

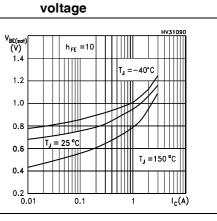


Figure 6. Base-emitter on voltage



57

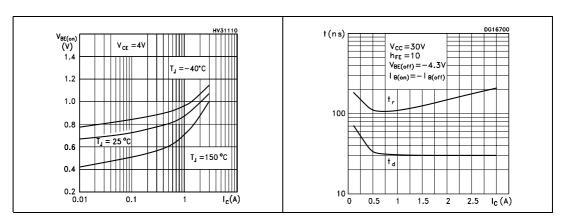
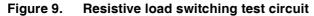
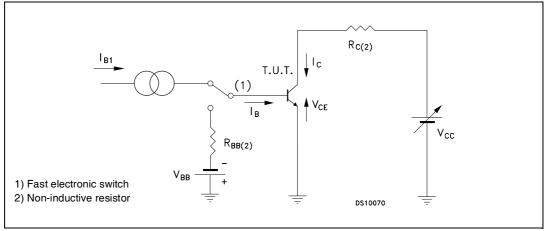


Figure 8. Resistive load switching time

2.2 Test circuit





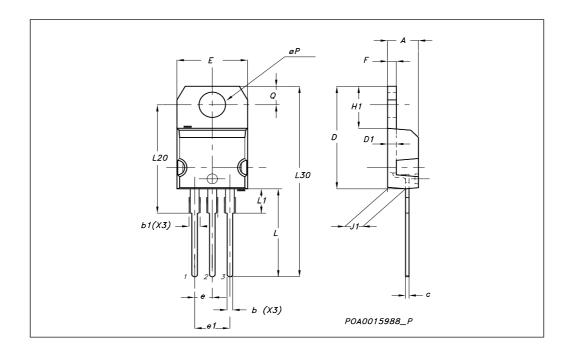


3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



TO-220 Mechanical data			
DIM.		mm.	
DIM.	MIN.	ТҮР	MAX.
A	4.40		4.60
b	0.61		0.88
b1	1.14		1.70
с	0.49		0.70
D	15.25		15.75
D1		1.27	
E	10		10.40
е	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13		14
L1	3.50		3.93
L20		16.40	
L30		28.90	
øP	3.75		3.85
Q	2.65		2.95





4 Revision history

Table 4. Revision history

Date	Revision	Changes	
01-Jan-2000	1	Initial Release	
11-Jul-2007	2	Figures 1,2,3,4,5,6,7,8 and figure 9 have been added.	



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