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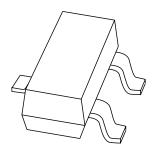
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Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS

DATA SHEET



BC859; BC860 PNP general purpose transistors

Product data sheet Supersedes data of 1999 May 28 2004 Jan 16



PNP general purpose transistors

BC859; BC860

FEATURES

• Low current (max. 100 mA)

• Low voltage (max. 45 V).

APPLICATIONS

• Low noise input stages of audio frequency equipment.

DESCRIPTION

PNP transistor in a SOT23 plastic package. NPN complements: BC849 and BC850.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾	TYPE NUMBER	MARKING CODE ⁽¹⁾	
BC859B	4B*	BC860B	4F*	
BC859C	4C*	BC860C	4G*	

Note

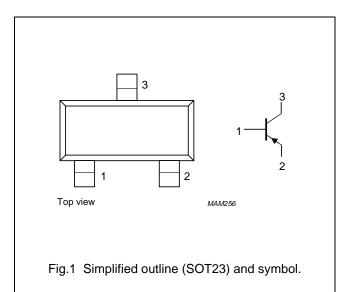
1. * = p: Made in Hong Kong.

* = t : Made in Malaysia.

* = W : Made in China.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



ORDERING INFORMATION

TYPE	PACKAGE		
NUMBER	NAME	DESCRIPTION	VERSION
BC859B	_	plastic surface mounted package; 3 leads	SOT23
BC859C			
BC860B			
BC860C			

PNP general purpose transistors

BC859; BC860

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BC859		_	-30	V
	BC860		_	-50	V
V _{CEO}	collector-emitter voltage	open base			
	BC859		_	-30	V
	BC860		_	-45	V
V _{EBO}	emitter-base voltage	open collector	_	- 5	V
I _C	collector current (DC)		_	-100	mA
I _{CM}	peak collector current		-	-200	mA
I _{BM}	peak base current		_	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

^{1.} Transistor mounted on an FR4 printed-circuit board.

PNP general purpose transistors

BC859; BC860

CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
collector cut-off current	$I_E = 0$; $V_{CB} = -30 \text{ V}$	_	-1	-15	nA
	$I_E = 0$; $V_{CB} = -30 \text{ V}$; $T_j = 150 ^{\circ}\text{C}$	_	_	-4	μΑ
emitter cut-off current	$I_C = 0; V_{EB} = -5 \text{ V}$	_	_	-100	nA
DC current gain	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V};$				
BC859B; BC860B	see Figs 2 and 3	220	_	475	
BC859C; BC860C		420	_	800	
collector-emitter saturation	$I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}$	_	-75	-300	mV
voltage	$I_C = -100 \text{ mA}; I_B = -5 \text{ mA}$	_	-250	-650	mV
base-emitter saturation voltage	$I_C = -10 \text{ mA}$; $I_B = -0.5 \text{ mA}$; note 1	_	-700	_	mV
	$I_C = -100 \text{ mA}$; $I_B = -5 \text{ mA}$; note 1	_	-850	_	mV
base-emitter voltage	$I_C = -2 \text{ mA}$; $V_{CE} = -5 \text{ V}$; note 2	-600	-650	-750	mV
	$I_C = -10 \text{ mA}$; $V_{CE} = -5 \text{ V}$; note 2	_	-	-820	mV
collector capacitance	$I_E = I_e = 0$; $V_{CB} = -10 \text{ V}$; $f = 1 \text{ MHz}$	_	4.5	_	pF
emitter capacitance	$I_C = I_c = 0$; $V_{EB} = -500 \text{ mV}$; $f = 1 \text{ MHz}$	_	10	_	pF
transition frequency	$I_C = -10 \text{ mA}; V_{CE} = -5 \text{ V}; f = 100 \text{ MHz}$	100	_	_	MHz
noise figure	$I_C = -200 \mu A$; $V_{CE} = -5 V$; $R_S = 2 k\Omega$;				
BC859B; BC860B; BC859C; BC860C	f = 30 Hz to 15 kHz	_	_	4	dB
noise figure BC859B; BC860B;	I_C = -200 μA; V_{CE} = -5 V; R_S = 2 kΩ; f = 1 kHz; B = 200 Hz	_	_	4	dB
	collector cut-off current emitter cut-off current DC current gain BC859B; BC860B BC859C; BC860C collector-emitter saturation voltage base-emitter saturation voltage base-emitter voltage collector capacitance emitter capacitance transition frequency noise figure BC859B; BC860B; BC859C; BC860C noise figure	$ \begin{array}{c} \text{Collector cut-off current} & I_E = 0; V_{CB} = -30 \text{V} \\ I_E = 0; V_{CB} = -30 \text{V}; T_j = 150 ^{\circ}\text{C} \\ \\ \text{emitter cut-off current} & I_C = 0; V_{EB} = -5 \text{V} \\ \\ \text{DC current gain} & I_{C} = -2 \text{mA}; V_{CE} = -5 \text{V}; \\ \text{See Figs 2 and 3} \\ \\ \text{BC859B; BC860B} & \text{See Figs 2 and 3} \\ \\ \text{BC859C; BC860C} & \\ \\ \text{Collector-emitter saturation} & I_{C} = -10 \text{mA}; I_{B} = -0.5 \text{mA} \\ \\ \text{I}_{C} = -100 \text{mA}; I_{B} = -5 \text{mA} \\ \\ \text{I}_{C} = -100 \text{mA}; I_{B} = -0.5 \text{mA}; \text{note 1} \\ \\ \text{I}_{C} = -100 \text{mA}; I_{B} = -5 \text{mA}; \text{note 1} \\ \\ \text{I}_{C} = -100 \text{mA}; I_{B} = -5 \text{mA}; \text{note 1} \\ \\ \text{I}_{C} = -100 \text{mA}; V_{CE} = -5 \text{V}; \text{note 2} \\ \\ \text{I}_{C} = -10 \text{mA}; V_{CE} = -5 \text{V}; \text{note 2} \\ \\ \text{I}_{C} = -10 \text{mA}; V_{CE} = -5 \text{V}; \text{note 2} \\ \\ \text{I}_{C} = -10 \text{mA}; V_{CE} = -5 \text{V}; \text{note 2} \\ \\ \text{I}_{C} = -10 \text{mA}; V_{CE} = -5 \text{V}; \text{note 2} \\ \\ \text{I}_{C} = -10 \text{mA}; V_{CE} = -5 \text{V}; \text{note 2} \\ \\ \text{I}_{C} = -10 \text{mA}; V_{CE} = -5 \text{V}; \text{note 2} \\ \\ \text{I}_{C} = -10 \text{mA}; V_{CE} = -5 \text{V}; \text{note 2} \\ \\ \text{I}_{C} = -10 \text{mA}; V_{CE} = -5 \text{V}; \text{note 2} \\ \\ \text{I}_{C} = -10 \text{mA}; V_{CE} = -5 \text{V}; \text{note 2} \\ \\ \text{I}_{C} = -10 \text{mA}; V_{CE} = -5 \text{V}; \text{note 2} \\ \\ \text{I}_{C} = -10 \text{mA}; V_{CE} = -5 \text{V}; \text{note 2} \\ \\ \text{I}_{C} = -10 \text{mA}; V_{CE} = -5 \text{V}; \text{N}_{CE} = -5 \text{V}; \text{N}$	$ \begin{array}{c} \text{collector cut-off current} & I_{E} = 0; V_{CB} = -30 V & - \\ I_{E} = 0; V_{CB} = -30 V; T_{j} = 150 ^{\circ}\text{C} & - \\ \\ \text{emitter cut-off current} & I_{C} = 0; V_{EB} = -5 V & - \\ \\ DC \text{current gain} & I_{C} = -2 \text{mA}; V_{CE} = -5 V; \\ \text{see Figs 2 and 3} & 220 \\ \\ BC859B; BC860B & \text{see Figs 2 and 3} & 220 \\ \\ BC859C; BC860C & 420 \\ \\ \text{collector-emitter saturation} & I_{C} = -10 \text{mA}; I_{B} = -0.5 \text{mA} & - \\ \\ log and mathematical collector mathematica$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c} \text{collector cut-off current} & I_E = 0; V_{CB} = -30 \text{V} & - & -1 & -15 \\ I_E = 0; V_{CB} = -30 \text{V}; T_j = 150 ^{\circ}\text{C} & - & - & -4 \\ \hline \text{emitter cut-off current} & I_C = 0; V_{EB} = -5 \text{V} & - & - & -100 \\ \hline DC \text{current gain} & I_C = -2 \text{mA;} V_{CE} = -5 \text{V}; \\ \text{see Figs 2 and 3} & 220 & - & 475 \\ \hline BC859B; BC860B & \text{see Figs 2 and 3} & 220 & - & 475 \\ \hline BC859C; BC860C & 420 & - & 800 \\ \hline \text{collector-emitter saturation} & I_C = -10 \text{mA;} I_B = -0.5 \text{mA} & - & -75 & -300 \\ \hline \text{lc} = -100 \text{mA;} I_B = -5 \text{mA} & - & -250 & -650 \\ \hline \text{base-emitter saturation voltage} & I_C = -10 \text{mA;} I_B = -0.5 \text{mA;} \text{note 1} & - & -250 & -650 \\ \hline \text{lc} = -100 \text{mA;} I_B = -5 \text{mA;} \text{note 1} & - & -850 & - \\ \hline \text{lc} = -100 \text{mA;} I_B = -5 \text{mA;} \text{note 1} & - & -850 & - \\ \hline \text{lc} = -10 \text{mA;} V_{CE} = -5 \text{V;} \text{note 2} & -600 & -650 & -750 \\ \hline \text{lc} = -10 \text{mA;} V_{CE} = -5 \text{V;} \text{note 2} & - & -820 \\ \hline \text{collector capacitance} & I_E = I_e = 0; V_{CB} = -10 \text{V;} f = 1 \text{MHz} & - & 4.5 & - \\ \hline \text{emitter capacitance} & I_C = I_0 = 0; V_{EB} = -500 \text{mV;} f = 1 \text{MHz} & - & 4.5 & - \\ \hline \text{emitter capacitance} & I_C = -10 \text{mA;} V_{CE} = -5 \text{V;} f = 1 \text{MHz} & - & - & - \\ \hline \text{lc} = -200 \mu \text{A;} V_{CE} = -5 \text{V;} f = 100 \text{MHz} & - & - & - \\ \hline \text{lc} = -200 \mu \text{A;} V_{CE} = -5 \text{V;} R_S = 2 \text{k}\Omega; \\ \hline \text{lc} = -200 \mu \text{A;} V_{CE} = -5 \text{V;} R_S = 2 \text{k}\Omega; \\ \hline \text{lc} = -200 \mu \text{A;} V_{CE} = -5 \text{V;} R_S = 2 \text{k}\Omega; \\ \hline \text{lc} = 1 \text{kHz;} B = 200 \text{Hz} & - & - & 4 \\ \hline \end{array}$

Notes

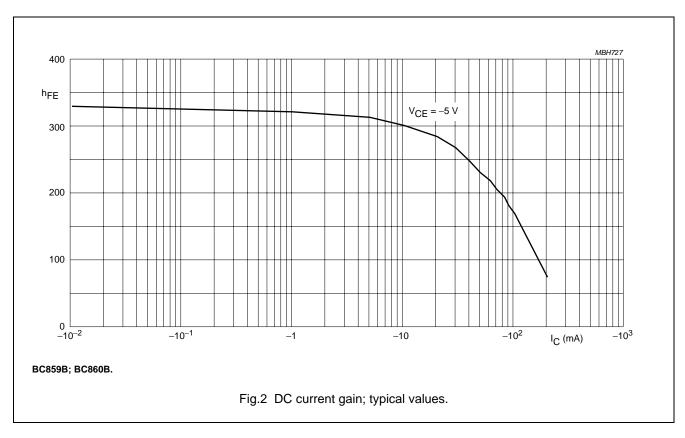
1. V_{BEsat} decreases by about $-1.7\ mV/K$ with increasing temperature.

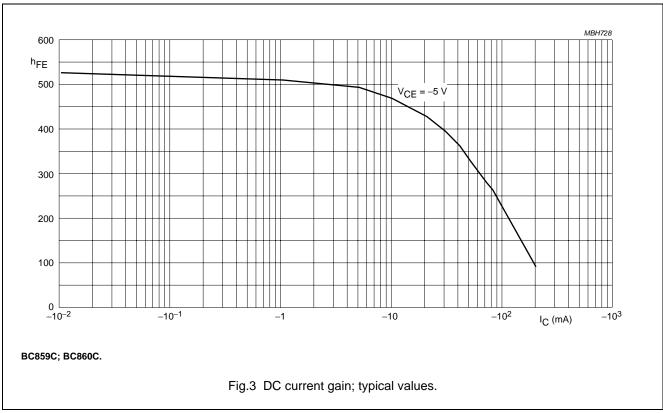
2. V_{BE} decreases by about -2 mV/K with increasing temperature.

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PNP general purpose transistors

BC859; BC860

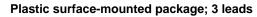




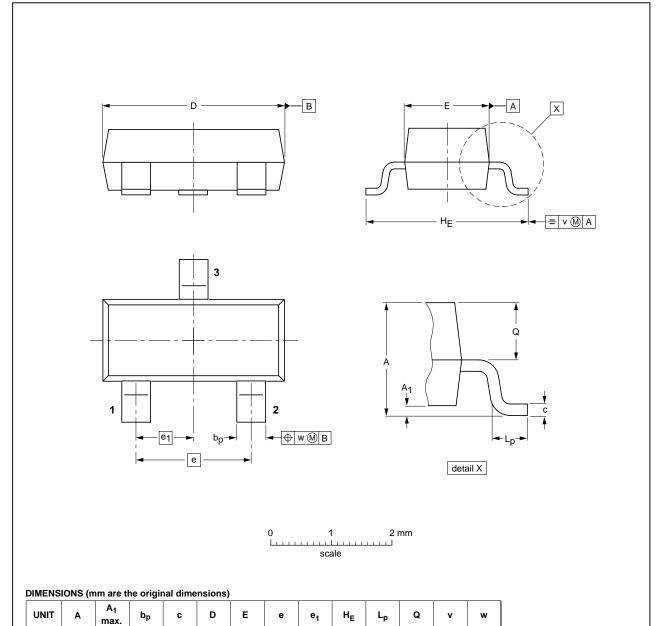
PNP general purpose transistors

BC859; BC860

PACKAGE OUTLINE



SOT23



OUTLINE	REFERENCES			EUROPEAN	ICCUIE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				-04-11-04 06-03-16

0.45

0.1

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0.38

0.9

PNP general purpose transistors

BC859; BC860

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

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NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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