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MC240064GD6W-FPTLW	128 x 64	LCD Module				
	Specification					
Version: 4		Date: 13/04/2018				
Revision						
1	15/10/2014	First Issue.				
		Modify Length of Cable.				
		Modify Precautions in use of LCM and Static Electricity Test.				
4	07/02/2017	Modify VIL.				

Display F	Display Features				
Resolution	240 x 64				
Appearance	Black on White				
Logic Voltage	3.3V		1		
Interface	Parallel		CHS		
Font Set	N/A	\ V	NoHS ompliant		
Display Mode	Transflective		mphant		
LC Type	FSTN				
Module Size	180.00 x 65.00 x 12.30 mm				
Operating Temperature	-20°C ~ +70°C				
Construction	СОВ	Box Quantity	Weight / Display		
LED Backlight	White				

* - For full design functionality, please use this specification in conjunction with the RA6963 specification. (Provided Separately)

Display Accessories					
Part Number	Description				
	20 Way Dual in line-to-Dual in line interconnect cable.				

Optional Variants				
Appearances	Voltage			
White on Blue				

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1.General Specification

The Features is described as follow:

■ Module dimension: 180.0 x 65.0 x 12.3 (max.) mm

■ View area: 133.0 x 39.0 mm

Active area: 127.16 x 33.88 mm

■ Number of dots: 240 x 64

■ Dot size: 0.49 x 0.49 mm

■ Dot pitch: 0.53 x 0.53 mm

■ LCD type: FSTN Positive Transflective

■ Duty: 1/64

■ View direction: 6 o'clock

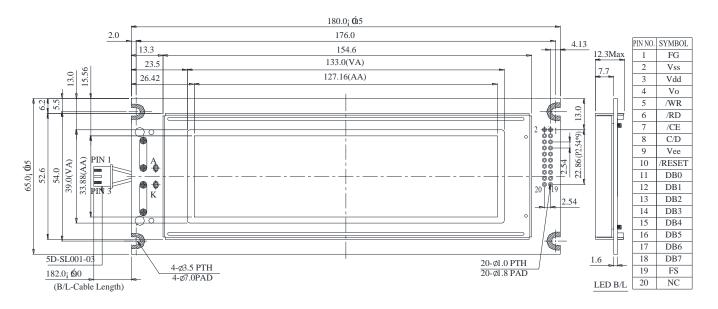
■ Backlight Type: LED White

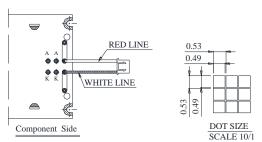
■ IC:RA6963

3.Interface Pin Function

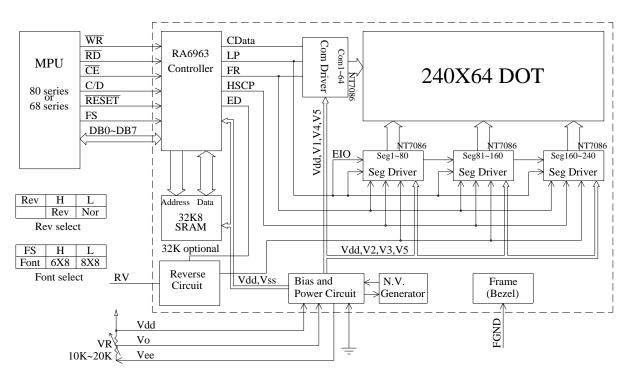
Pin No.	Symbol	Level	Description
1	FG	_	Frame ground (Connected to bezel)
2	Vss		GND
3	Vdd		Power supply
4	Vo		Power supply for LCD driver
5	/WR	L	Data write. Write data into RA6963 when WR = L
6	/RD	L	Data read. Read data from RA6963 when RD = L
7	/CE	L	L : Chip enable
8	C/D	H/L	WR=L , C/D=H : Command Write
			RD=L , C/D=H : Status Read C/D=L: Data read
9	Vee	_	Neg <mark>at</mark> ive voltage output
10	/RESET	H/L	H : Normal ; L : Initialize RA6963
11	DB0	H/L	Data bus line
12	DB1	H/L	Data bus line
13	DB2	H/L	Data bus line
14	DB3	H/L	Data bus line
15	DB4	H/L	Data bus line
16	DB5	H/L	Data bus line
17	DB6	H/L	Data bus line
18	DB7	H/L	Data bus line
19	FS	H/L	Pins for selection of font; H:6*8, L:8*8
20	NC		No connection

4. Contour Drawing & Block Diagram





The non-specified tolerance of dimension is ; $\acute{0}3~\text{mm}$.



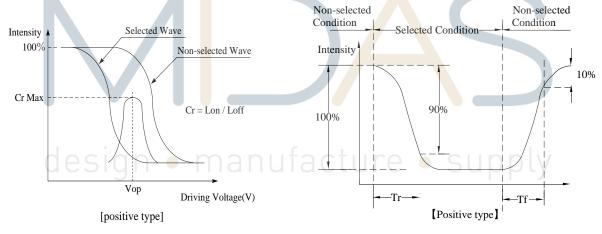
External contrast adjustment.

5.Optical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
View Angle	θ	CR≧2	0	_	30	ψ= 180°
	θ	CR≧2	0	_	60	ψ= 0°
view Angle	θ	CR≧2	0	_	45	ψ= 90°
	θ	CR≧2	0	_	45	ψ= 270°
Contrast Ratio	CR	_	_	5	_	_
	T rise	_	_	200	300	ms
Response Time	T fall	_	_	250	350	ms

Definition of Operation Voltage (Vop)

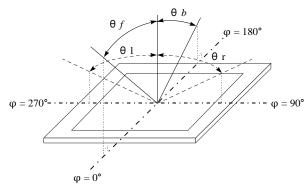
Definition of Response Time (Tr , Tf)



Conditions:

Frame Frequency: 64 HZ Driving Waveform: 1/N duty, 1/a bias

Definition of viewing angle(CR≥2)



6.Absolute Maximum Ratings

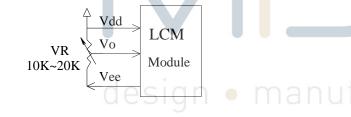
Item	Symbol	Min	Тур	Max	Unit
Operating Temperature	Тор	-20	_	+70	°C
Storage Temperature	Тѕт	-30	_	+80	°C
Input Voltage	V _{IN}	-0.3	_	V _{DD} +0.3	V
Supply Voltage For Logic	VDD-VSS	-0.3	_	+7.0	V



7. Electrical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltage For Logic	V _{DD} -V _{SS}	_	3.0	_	5.5	V
Supply Voltage For LCD		Ta=-20°C	_	_	13.9	V
Supply Voltage For LCD *Note	V_{DD} - V_0	Ta=25°C	12.1	12.5	12.9	V
Note		Ta=70°C	10.1	_	_	V
Input High Volt.	Vıн	_	0.8V _{DD}	_	V_{DD}	V
Input Low Volt.	VıL	_	0	_	0.15 V _{DD}	V
Output High Volt.	Vон	_	VDD-0.3	_	V _{DD}	V
Output Low Volt.	VoL	_	0	_	0.3	V
Supply Current	IDD	-	12	16	20	mA

^{*} Note: Please design the VOP adjustment circuit on customer's main board



8.Backlight Information

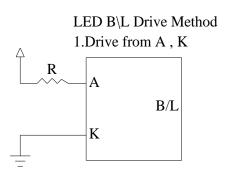
Specification

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	ILED	_	80	100	mA	V=3.5V
Supply Voltage	V	3.4	3.5	3.6	v	_
Reverse Voltage	VR	_	_	5	v	_
Luminance (Without LCD)	IV	520	650	_	CD/M ²	ILED=80mA
LED Life Time (For Reference only)		1	50K	_		ILED=80mA 25°C,50-60%RH, (Note 1)
Color	White					

Note: The LED of B/L is drive by current only, drive voltage is for reference only.

drive voltage can make driving current under safety area (current between minimum and maximum).

Note 1:50K hours is only an estimate for reference.



9.Reliability

Content of Reliability Test (Wide temperature, -20°C~70°C)

	Environmental Test							
Test Item	Content of Test	Test Condition	Note					
High Temperature storage Low Temperature	Endurance test applying the high storage temperature for a long time. Endurance test applying the low storage temperature	80°C 200hrs -30°C	2 1,2					
storage	for a long time.	200hrs	1,2					
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 200hrs						
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	1					
High Temperature/ Humidity storage	The module should be allowed to stand at 60°C,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60°Հ90%RH 96hrs	1,2					
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20°C 25°C 70°C 30min 5min 30min 1 cycle	-20°C/70°C 10 cycles						
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude: 1.5mm Vibration Frequency: 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3					
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact), ±800v(air), RS=330 Ω CS=150pF 10 times						

Note1: No dew condensation to be observed.

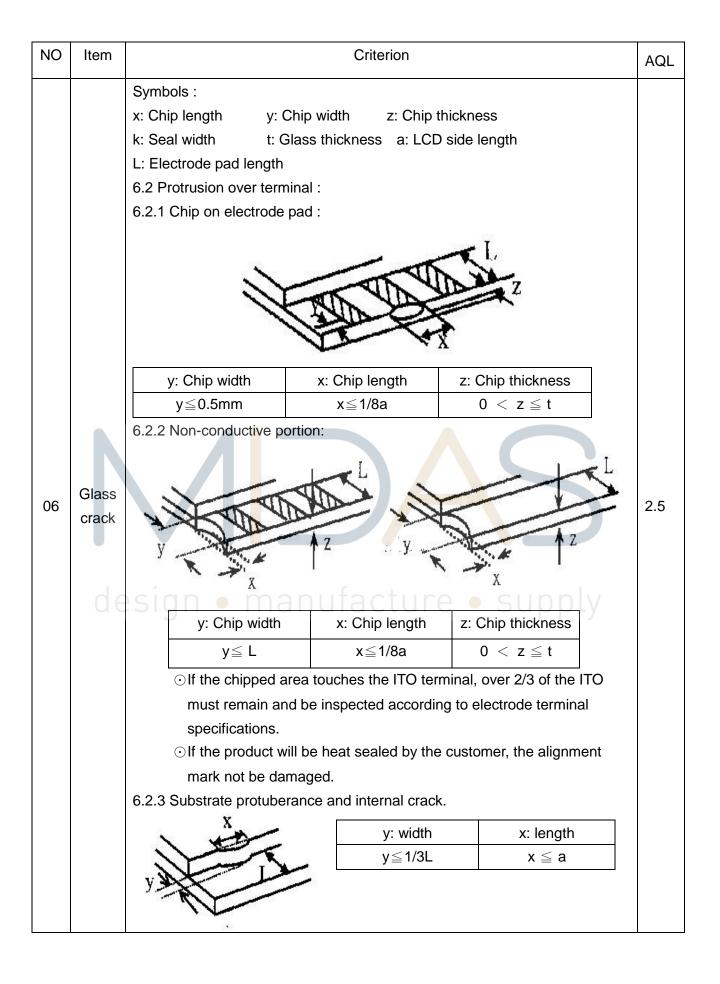
Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

10.Inspection specification

NO	Item			Criterion		AQL
01	Electrical Testing	 1.1 Missing vertical, defect. 1.2 Missing charact 1.3 Display malfund 1.4 No function or n 1.5 Current consum 1.6 LCD viewing an 1.7 Mixed product ty 1.8 Contrast defect. 	er, do tion. to displaytion e gle def	t or icon. lay. exceeds product sp		0.65
02	Black or white spots on LCD (display only)	2.1 White and black spots on display ≤0.25mm, no more than three white or black spots present.2.2 Densely spaced: No more than two spots or lines within 3mm				
03	LCD black spots, white spots,	3.1 Round type : As $\Phi = (x + y)/2$ $X \qquad \qquad$	Y	fing drawing $\Phi \le 0.10$ $0.10 < \Phi \le 0.20$ $0.20 < \Phi \le 0.25$ $0.25 < \Phi$	Acceptable Q TY Accept no dense 2 1 0	2.5
	contamination (non-display)	→ L + L	ollowin ength ≦3.0 ≤2.5	$\begin{array}{c c} \text{ width} \\ \text{W} \! \leq \! 0.02 \\ 0.02 \! < \! \text{W} \! \leq \! 0.03 \\ 0.03 \! < \! \text{W} \! \leq \! 0.05 \\ 0.05 \! < \! \text{W} \end{array}$	Acceptable Q TY Accept no dense 2 As round type	2.5
04	Polarizer bubbles	If bubbles are visible judge using black specifications, not e to find, must check specify direction.	pot easy	Size Φ $Φ \le 0.20$ $0.20 < Φ \le 0.50$ $0.50 < Φ \le 1.00$ $1.00 < Φ$ Total Q TY	Acceptable Q TY Accept no dense 3 2 0 3	2.5

NO	Item	Criterion					
05	Scratches	Follow NO.3 LCD black	spots, white spots, cor	ntamination			
06	Chipped glass	Symbols Define: x: Chip length y: 0	Chip width z: Chip Glass thickness a: LCI face and crack betweer y: Chip width Not over viewing area Not exceed 1/3k chips, x is total length	thickness D side length n panels: x: Chip length x≤1/8a x≤1/8a	2.5		
		Z≦1/2t	Not over viewing	x≦1/8a			
			area				
		1/2t < z ≦ 2t	Not exceed 1/3k	x≦1/8a			
		⊙ If there are 2 or more	chips, x is the total len	gth of each chip.			



NO	Item	Criterion			
07	Cracked glass	The LCD with extensive crack is not acceptable.			
08	Backlight elements	 8.1 Illumination source flickers when lit. 8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards. 8.3 Backlight doesn't light or color wrong. 			
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.9.2 Bezel must comply with job specifications.			
10	PCB · COB desig	 9.2 Bezel must comply with job specifications. 10.1 COB seal may not have pinholes larger than 0.2mm or contamination. 10.2 COB seal surface may not have pinholes through to the IC. 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. 10.9 The Scraping testing standard for Copper Coating of PCB 			
11	Soldering	 11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB. 	2.5 2.5 2.5 0.65		

NO	Item	Criterion		
		12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.		
	General appearance	12.2 No cracks on interface pin (OLB) of TCP.12.3 No contamination, solder residue or solder balls on product.	0.65	
		12.4 The IC on the TCP may not be damaged, circuits.		
		12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to	2.5 2.5	
12		sever. 12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5	
		12.7 Sealant on top of the ITO circuit has not hardened.12.8 Pin type must match type in specification sheet.12.9 LCD pin loose or missing pins.		
		12.10 Product packaging must the same as specified on packaging specification sheet.		
		12.11 Product dimension and structure must conform to product	0.65	
		specification sheet.		
		12.12 Visual defect outside of VA is not considered to be rejection.		

11.Precautions in use of LCD Modules

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3) Don't disassemble the LCM.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.
- (8) Midas have the right to change the passive components, including R3,R6 & backlight adjust resistors. (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)
- (9) Midas have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, Midas have the right to modify the version.)
- (10) To ensure the stability of the display screen, please apply screen saver after showing 30 mins of fixed display content.

12.Material List of Components for RoHs

1. Midas hereby declares that all of or part of products (with the mark "#"in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A: The Harmful Material List

Material	(Cd)	(Pb)	(Hg)	(Cr6+)	PBBs	PBDEs			
Limited Value	100 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm			
Above limited value is set up according to RoHS.									

2.Process for RoHS requirement: (only for RoHS inspection)

- (1) Use the Sn/Ag/Cu soldering surface; the surface of Pb-free solder is rougher than we used before.
- (2) Heat-resistance temp. :

Reflow: 250°C30 seconds Max.;

Connector soldering wave or hand soldering: 320°C, 10 seconds max.

(3) Temp. curve of reflow, max. Temp. : 235±5°C;

Recommended customer's soldering temp. of connector: 280°C, 3 seconds.

13. Recommendable Storage

- 1. Place the panel or module in the temperature 25°C±5°C and the humidity below 65% RH
- 2. Do not place the module near organics solvents or corrosive gases.
- 3. Do not crush, shake, or jolt the module.

