PNP -100mA -50V Digital Transistor (Bias Resistor Built-in Transistor)

AEC-Q101 Qualified

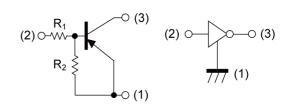
| Parameter | Value |
|----------------------|--------|
| V _{CC} | -50V |
| I _{C(MAX.)} | -100mA |
| R ₁ | 4.7kΩ |
| R ₂ | 4.7kΩ |

Outline SOT-323 SC-70 (2) (1) (UMT3)

Features

- 1) Built-In Biasing Resistors, $R_1 = R_2 = 4.7k\Omega$
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary NPN Types: DTC143EU3 HZG

•Inner circuit



- (1) GND (+) (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

| Part No. | Package | Package size | Taping code | Reel size (mm) | Tape width (mm) | Basic ordering unit.(pcs) | Marking |
|---------------|-------------------|-----------------|----------------|-------------------|--------------------|---------------------------------|---------|
| DTA143EU3 HZG | SOT-323 (UMT3) | 2021 | T106 | 180 | 8 | 3000 | 13 |

● Absolute maximum ratings (T_a = 25°C)

| Parameter | Symbol | Values | Unit |
|------------------------------|------------------------|-------------|------|
| Supply voltage | V _{CC} | -50 | V |
| Input voltage | V _{IN} | -30 to 10 | V |
| Output current | Io | -100 | mA |
| Collector current | I _{C(MAX)} *1 | -100 | mA |
| Power dissipation | P _D *2 | 200 | mW |
| Junction temperature | Tj | 150 | °C |
| Range of storage temperature | T _{stg} | -55 to +150 | °C |

● Electrical characteristics (T_a = 25°C)

| Daramatar | Cymah ol | Canditions | Values | | | I India | |
|----------------------|--------------------------------|---|--|------|------|---------|--|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit | |
| Input voltage | $V_{I(off)}$ | $V_{CC} = -5V, I_{O} = -100\mu A$ | $_{\text{C}} = -5\text{V}, I_{\text{O}} = -100\mu\text{A}$ - $ -0$ | | -0.5 | V | |
| Input voltage | V _{I(on)} | $V_O = -0.3V$, $I_O = -20$ mA | $I_{O} = -0.3$ V, $I_{O} = -20$ mA -3 | | - |] | |
| Output voltage | V _{O(on)} | $I_O = -10 \text{mA}, I_I = -0.5 \text{mA}$ | - | -100 | -300 | mV | |
| Input current | I _I | I ₁ V ₁ = -5V | | - | -1.8 | mA | |
| Output current | I _{O(off)} | V _{CC} = -50V, V _I = 0V | - | - | -500 | nA | |
| DC current gain | G _I | $V_O = -5V, I_O = -10mA$ | 30 | - | - | - | |
| Input resistance | R ₁ | - | 3.29 | 4.7 | 6.11 | kΩ | |
| Resistance ratio | R ₂ /R ₁ | - | 0.8 | 1 | 1.2 | - | |
| Transition frequency | f _T *1 | $V_{CE} = -10V, I_{E} = 5mA,$ f = 100MHz | | 250 | - | MHz | |

^{*1} Characteristics of built-in transistor.

^{*2} Each terminal mounted on a reference land.

● Electrical characteristic curves (T_a =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

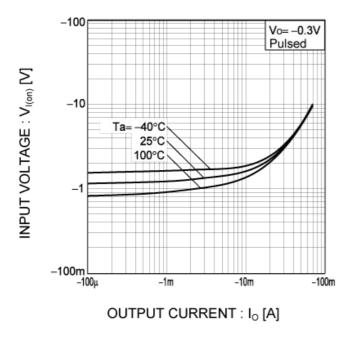


Fig.2 Output current vs. input voltage (OFF characteristics)

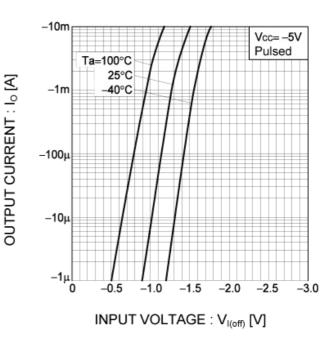


Fig.3 Output current vs. output voltage

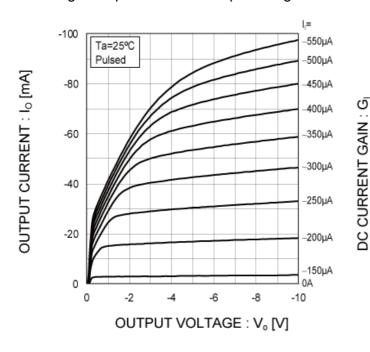
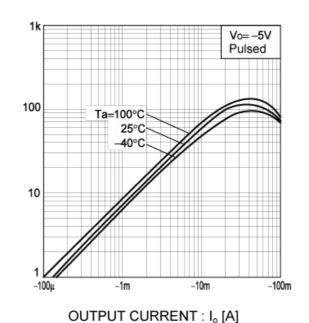
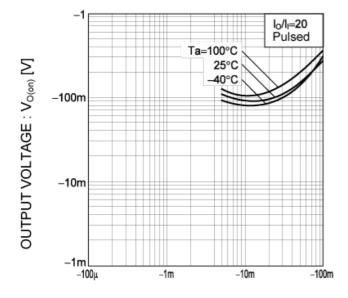


Fig.4 DC current gain vs. output current



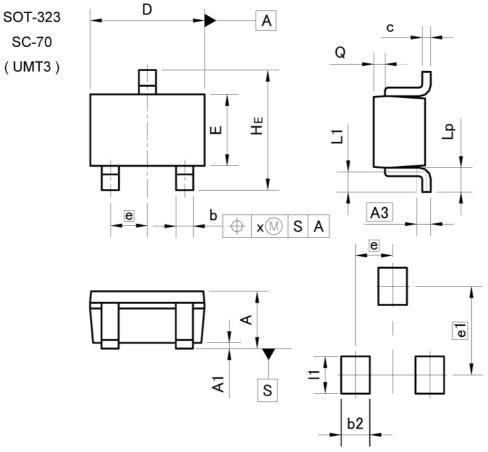
● Electrical characteristic curves (T_a =25°C)

Fig.5 Output voltage vs. output current



OUTPUT CURRENT : I_{\circ} [A]

Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

| DIM | MILIM | MILIMETERS | | HES | |
|-----|-------|------------|-------|-------|--|
| DIM | MIN | MAX | MIN | MAX | |
| Α | 0.80 | 1.00 | 0.031 | 0.039 | |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 | |
| A3 | 0.5 | 25 | 0.010 | | |
| b | 0.25 | 0.40 | 0.010 | 0.016 | |
| С | 0.10 | 0.20 | 0.004 | 0.008 | |
| D | 1.90 | 2.10 | 0.075 | 0.083 | |
| E | 1.15 | 1.35 | 0.045 | 0.053 | |
| е | 0. | 65 | 0.0 | 26 | |
| HE | 2.00 | 2.20 | 0.079 | 0.087 | |
| L1 | 0.10 | 0.40 | 0.004 | 0.016 | |
| Lp | 0.25 | 0.55 | 0.010 | 0.022 | |
| Q | 0.10 | 0.30 | 0.004 | 0.012 | |
| х | _ | 0.10 | - | 0.004 | |

| DIM | MILIMETERS | | INCHES | | |
|-----|------------|------|--------|-------|--|
| DIM | MIN | MAX | MIN | MAX | |
| b2 | _ | 0.50 | _ | 0.020 | |
| e1 | 1.55 | | 0.0 | 61 | |
| 11 | - | 0.65 | - | 0.026 | |

Dimension in mm/inches

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| JAPAN | USA | EU | CHINA |
|---------|---------|------------|--------|
| CLASSⅢ | OL ACOM | CLASS II b | ОГУООШ |
| CLASSIV | CLASSⅢ | CLASSⅢ | CLASSⅢ |

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
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 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [c] the Products are exposed to direct sunshine or condensation
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- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
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- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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