

29 December 2022

Product data sheet

1. General description

NPN/NPN double transistor in a SOT666 ultra small and flat lead Surface-Mounted Device (SMD) plastic package.

PNP complement: PEMT1

2. Features and benefits

- 300 mW total power dissipation
- Very small 1.6 mm x 1.2 mm ultra thin package
- Replaces two SC-75/SC-89 packaged transistors on same PCB area
- Reduced required PCB area
- Reduced pick and place costs

3. Applications

General purpose switching and amplification

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	tor						
V _{CEO}	collector-emitter voltage	open base		-	-	40	V
I _C	collector current			-	-	100	mA
h _{FE}	DC current gain	V_{CE} = 6 V; I _C = 1 mA; T _{amb} = 25 °C		120	-	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	6 5 4	C1 B2 E2
2	B1	base TR1		
3	C2	collector TR2		
4	E2	emitter TR2		
5	B2	base TR2		E1 B1 C2
6	C1	collector TR1	SOT666	sym020



6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PEMX1	SOT666	plastic, surface-mounted package; 6 leads; 0.5 mm pitch; 1.6 mm x 1.2 mm x 0.55 mm body	<u>SOT666</u>			

7. Marking

Table 4. Marking codes					
Type number	Marking code				
PEMX1	ZZ				

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or					
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	100	mA
I _{CM}	peak collector current			-	200	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
Per device						
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics							
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	416	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

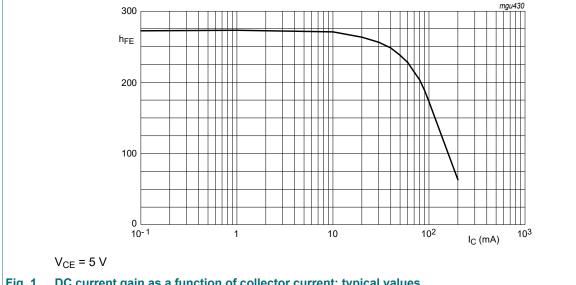
[2] Reflow soldering is the only recommended soldering method.

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10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transist	tor	· · ·				
I _{CBO}	collector-base cut-off	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
	current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C	-	-	10	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 4 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 6 V; I _C = 1 mA; T _{amb} = 25 °C	120	-	-	
V _{CEsat}	collector-emitter saturation voltage	I_C = 50 mA; I_B = 5 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	200	mV
C _c	collector capacitance	$V_{CB} = 12 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ i}_{e} = 0 \text{ A}; \text{ f} = 1 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	-	-	1.5	pF
f _T	transition frequency	V_{CE} = 12 V; I _C = 2 mA; f = 100 MHz; T _{amb} = 25 °C	100	-	-	MHz



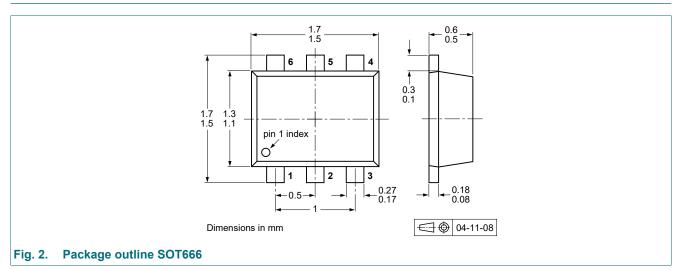


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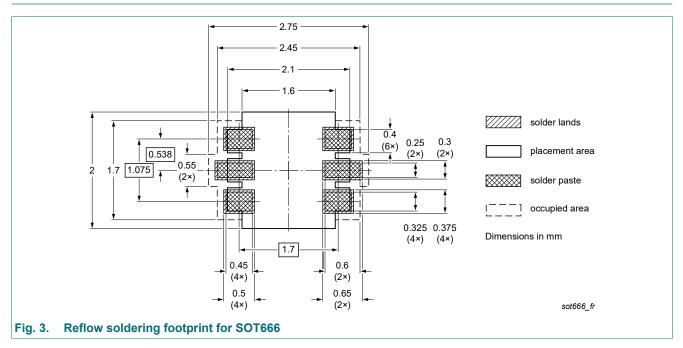
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11. Package outline



12. Soldering



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13. Revision history

Table 8. Revision h	nistory						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PEMX1 v.3	20221229	Product data sheet	-	PEMX1 v.2			
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Product(s) changed to non-automotive qualification. 						
PEMX1 v.2	20011107	Product data sheet	-	PEMX1 v.1			
PEMX1 v.1	20010830	Product data sheet	-	-			

PEMX1

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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NPN/NPN general purpose double transistor

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