Product data sheet



1. General description

The 74AUP1G04 provides the single inverting buffer.

Schmitt trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire V_{CC} range from 0.8 V to 3.6 V.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.2 V to 1.95 V)
 - JESD8-5 (1.8 V to 2.7 V)
 - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - HBM JESD22-A114F Class 3A exceeds 5000 V
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption; $I_{CC} = 0.9 \,\mu A$ (maximum)
- Latch-up performance exceeds 100 mA per JESD 78B Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

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3. Ordering information

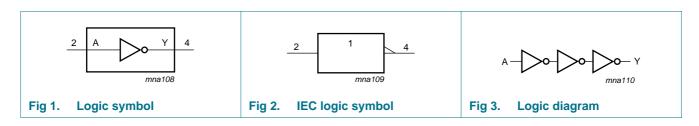
Table 1. Ordering	g information			
Type number	Package			
	Temperature range	Name	Description	Version
74AUP1G04GV	–40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753
74AUP1G04GW	–40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1
74AUP1G04GM	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body $1\times1.45\times0.5~\text{mm}$	SOT886
74AUP1G04GF	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 \times 1 \times 0.5 mm	SOT891
74AUP1G04GN	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $0.9 \times 1.0 \times 0.35$ mm	SOT1115
74AUP1G04GS	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $1.0 \times 1.0 \times 0.35$ mm	SOT1202
74AUP1G04GX	–40 °C to +125 °C	X2SON5	X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body $0.8 \times 0.8 \times 0.35$ mm	SOT1226

4. Marking

Table 2. Marking	
Type number	Marking code ^[1]
74AUP1G04GV	p04
74AUP1G04GW	pC
74AUP1G04GM	pC
74AUP1G04GF	pC
74AUP1G04GN	pC
74AUP1G04GS	pC
74AUP1G04GX	pC

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

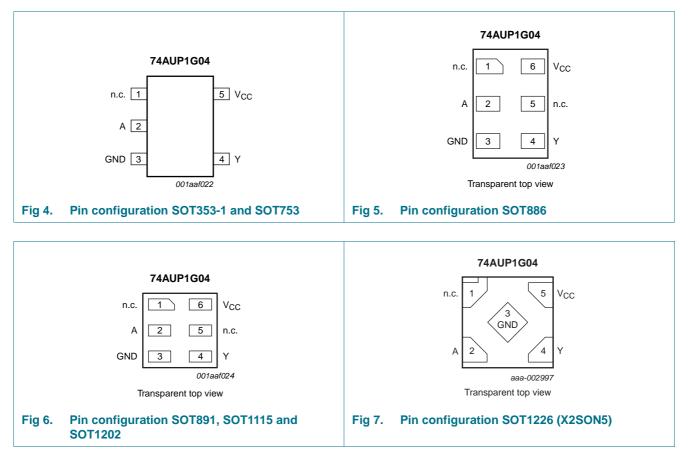
5. Functional diagram



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6. Pinning information

6.1 Pinning



6.2 Pin description

Table 3.	Pin description		
Symbol	Pin		Description
	TSSOP5 and X2SON5	XSON6	
n.c.	1	1	not connected
А	2	2	data input
GND	3	3	ground (0 V)
Y	4	4	data output
n.c.	-	5	not connected
V _{CC}	5	6	supply voltage

7. Functional description

Table 4.	Function table ^[1]	

Input	Output
Α	Y
L	Н
Н	L

[1] H = HIGH voltage level; L = LOW voltage level.

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+4.6	V
I _{IK}	input clamping current	V _I < 0 V	-50	-	mA
VI	input voltage		[1] -0.5	+4.6	V
I _{OK}	output clamping current	V _O < 0 V	-50	-	mA
Vo	output voltage	active mode	<u>[1]</u> –0.5	V _{CC} + 0.5	V
		power-down mode	<u>[1]</u> –0.5	+4.6	V
l _O	output current	$V_{O} = 0 V$ to V_{CC}	-	±20	mA
I _{CC}	supply current		-	+50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 \ ^{\circ}C \text{ to } +125 \ ^{\circ}C$	[2] _	250	mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

For XSON6 and X2SON5 packages: above 118 °C the value of P_{tot} derates linearly with 7.8 mW/K.

9. Recommended operating conditions

Table 6.	Recommended operating condition	ons			
Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	active mode	0	V _{CC}	V
		power-down mode; $V_{CC} = 0 V$	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C
$\Delta t / \Delta V$	input transition rise and fall rate	$V_{CC} = 0.8 V \text{ to } 3.6 V$	0	200	ns/V

^[2] For TSSOP5 and SC-74A packages: above 87.5 °C the value of Ptot derates linearly with 4.0 mW/K.

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10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	∕5 °C					
VIH	HIGH-level input voltage	$V_{CC} = 0.8 V$	$0.70 \times V_{CC}$	-	-	V
		$V_{CC} = 0.9 V$ to 1.95 V	$0.65 \times V_{CC}$	-	-	V
		V_{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	2.0	-	-	V
V _{IL}	LOW-level input voltage	$V_{CC} = 0.8 V$	-	-	$0.30 \times V_{CC}$	V
		$V_{CC} = 0.9 V$ to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		V_{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	-	-	0.9	V
V _{ОН}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = –20 $\mu A;$ V_{CC} = 0.8 V to 3.6 V	$V_{CC} - 0.1$	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.75 \times V_{CC}$	-	-	V
		$I_0 = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	1.11	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.32	-	-	V
		$I_0 = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	2.05	-	-	V
		$I_0 = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.9	-	-	V
		$I_0 = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.72	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.6	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_O = 20 $\mu A; V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		$I_{O} = 1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	-	-	$0.3 \times V_{CC}$	V
		$I_{O} = 1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	-	-	0.31	V
		I_{O} = 1.9 mA; V_{CC} = 1.65 V	-	-	0.31	V
		I_{O} = 2.3 mA; V_{CC} = 2.3 V	-	-	0.31	V
		$I_{O} = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.44	V
		$I_0 = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.31	V
		I_{O} = 4.0 mA; V_{CC} = 3.0 V	-	-	0.44	V
I	input leakage current	$V_{\rm I}$ = GND to 3.6 V; $V_{\rm CC}$ = 0 V to 3.6 V	-	-	±0.1	μΑ
I _{OFF}	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.2	μΑ
ΔI_{OFF}	additional power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.2	μΑ
l _{cc}	supply current	$\label{eq:VI} \begin{array}{l} V_{I} = GND \text{ or } V_{CC}; \ I_{O} = O \ A; \\ V_{CC} = 0.8 \ V \ to \ 3.6 \ V \end{array}$	-	-	0.5	μΑ
ΔI _{CC}	additional supply current		-	-	40	μΑ
CI	input capacitance	V_{CC} = 0 V to 3.6 V; V_{I} = GND or V_{CC}	-	0.8	-	pF
Co	output capacitance	$V_O = GND; V_{CC} = 0 V$	-	1.7	-	pF

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Low-power inverter

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -	40 °C to +85 °C					
VIH	HIGH-level input voltage	$V_{CC} = 0.8 V$	$0.70 \times V_{\text{CC}}$	-	-	V
		$V_{CC} = 0.9 V$ to 1.95 V	$0.65 \times V_{CC}$	-	-	V
		V_{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	2.0	-	-	V
V _{IL}	LOW-level input voltage	$V_{CC} = 0.8 V$	-	-	$0.30 \times V_{CC}$	V
		$V_{CC} = 0.9 V$ to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		V_{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	-	-	0.9	V
V _{ОН}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = –20 $\mu\text{A};$ V_{CC} = 0.8 V to 3.6 V	$V_{CC} - 0.1$	-	-	V
		$I_0 = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.7 imes V_{CC}$	-	-	V
		$I_0 = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	1.03	-	-	V
		$I_0 = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.30	-	-	V
		$I_0 = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.97	-	-	V
		$I_0 = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.85	-	-	V
		$I_0 = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.67	-	-	V
		$I_0 = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.55	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_O = 20 \ \mu\text{A}; \ V_{CC} = 0.8 \ V \ \text{to} \ 3.6 \ V$	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	$0.3 imes V_{CC}$	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		$I_0 = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.33	V
		$I_0 = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.45	V
		$I_0 = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.33	V
		$I_0 = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.45	V
I	input leakage current	$V_I = GND$ to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.5	μΑ
OFF	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.5	μΑ
∆I _{OFF}	additional power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.6	μΑ
СС	supply current	$\label{eq:VI} \begin{array}{l} V_{I} = GND \text{ or } V_{CC}; \ I_{O} = 0 \ A; \\ V_{CC} = 0.8 \ V \text{ to } 3.6 \ V \end{array}$	-	-	0.9	μΑ
∆I _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	-	-	50	μΑ

Table 7. Static characteristics ...continued

Low-power inverter

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -	40 °C to +125 °C					
V _{IH}	HIGH-level input voltage	$V_{CC} = 0.8 V$	$0.75 \times V_{CC}$	-	-	V
		$V_{CC} = 0.9 V$ to 1.95 V	$0.70 \times V_{CC}$	-	-	V
		V_{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	2.0	-	-	V
V _{IL}	LOW-level input voltage	$V_{CC} = 0.8 V$	-	-	$0.25 \times V_{CC}$	V
		$V_{CC} = 0.9 V$ to 1.95 V	-	-	$0.30 \times V_{CC}$	V
		V_{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	-	-	0.9	V
V _{он}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} – 0.11	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.6 imes V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	0.93	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.17	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.77	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.67	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.40	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.30	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = 20 μ A; V_{CC} = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	$0.33 \times V_{CC}$	V
		$I_0 = 1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		$I_0 = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.36	V
		$I_0 = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.50	V
		$I_0 = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.36	V
		$I_0 = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.50	V
I	input leakage current	$V_I = GND$ to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.75	μA
OFF	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.75	μΑ
∆I _{OFF}	additional power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.75	μΑ
lcc	supply current	$\label{eq:VI} \begin{array}{l} V_{I} = GND \text{ or } V_{CC}; \ I_{O} = 0 \ A; \\ V_{CC} = 0.8 \ V \text{ to } 3.6 \ V \end{array}$	-	-	1.4	μΑ
∆I _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	-	-	75	μΑ

Table 7. Static characteristics ...continued

Low-power inverter

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 9

Symbol	Parameter	Conditions		Min	Typ 🚹	Max	Unit
T _{amb} = 25	°C; C _L = 5 pF						
t _{pd}	propagation delay	A to Y; see Figure 8	[2]				
		$V_{CC} = 0.8 V$		-	16.0	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		2.4	5.0	10.3	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		1.8	3.6	6.4	ns
		V_{CC} = 1.65 V to 1.95 V		1.5	2.9	5.0	ns
		V_{CC} = 2.3 V to 2.7 V		1.2	2.4	3.9	ns
		V_{CC} = 3.0 V to 3.6 V		1.1	2.1	3.2	ns
T _{amb} = 25	°C; C _L = 10 pF						
t _{pd}	propagation delay	A to Y; see Figure 8	[2]				
		$V_{CC} = 0.8 V$		-	19.8	-	ns
		V_{CC} = 1.1 V to 1.3 V		2.8	5.9	12.2	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		2.3	4.2	7.5	ns
		V_{CC} = 1.65 V to 1.95 V		2.0	3.5	5.9	ns
		V_{CC} = 2.3 V to 2.7 V		1.7	2.9	4.6	ns
		V_{CC} = 3.0 V to 3.6 V		1.6	2.7	3.8	ns
T _{amb} = 25	°C; C _L = 15 pF						
t _{pd}	propagation delay	A to Y; see Figure 8	[2]				
		$V_{CC} = 0.8 V$		-	23.3	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		3.2	6.7	13.0	ns
		$V_{CC} = 1.4 \text{ V} \text{ to } 1.6 \text{ V}$		2.6	4.7	8.6	ns
		V_{CC} = 1.65 V to 1.95 V		2.3	4.0	6.7	ns
		V_{CC} = 2.3 V to 2.7 V		2.1	3.3	5.1	ns
		V_{CC} = 3.0 V to 3.6 V		2.0	3.1	4.2	ns
T _{amb} = 25	°C; C _L = 30 pF						
t _{pd}	propagation delay	A to Y; see Figure 8	[2]				
		$V_{CC} = 0.8 V$		-	33.6	-	ns
		V_{CC} = 1.1 V to 1.3 V		4.4	8.9	16.0	ns
		V_{CC} = 1.4 V to 1.6 V		3.6	6.3	10.8	ns
		V_{CC} = 1.65 V to 1.95 V		3.2	5.3	9.0	ns
		V_{CC} = 2.3 V to 2.7 V		2.9	4.5	6.5	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$		2.9	4.2	5.4	ns

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Symbol	Parameter	Conditions	Min	Typ 🚺	Мах	Unit
T _{amb} = 25	°C					
C _{PD}	power dissipation capacitance	$f = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$ [3]				
		V _{CC} = 0.8 V	-	2.5	-	pF
		$V_{CC} = 1.1 \text{ V}$ to 1.3 V	-	2.7	-	pF
		$V_{CC} = 1.4 \text{ V}$ to 1.6 V	-	2.8	-	pF
		$V_{CC} = 1.65 \text{ V} \text{ to } 1.95 \text{ V}$	-	3.0	-	pF
		V_{CC} = 2.3 V to 2.7 V	-	3.5	-	pF
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	-	4.0	-	pF

Table 8. Dynamic characteristics ... continued

[1] All typical values are measured at nominal V_{CC} .

[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

- [3] C_{PD} is used to determine the dynamic power dissipation (P_D in μ W).
 - $P_{D} = C_{PD} \times V_{CC}{}^{2} \times f_{i} \times N + \Sigma (C_{L} \times V_{CC}{}^{2} \times f_{o}) \text{ where:}$

 f_i = input frequency in MHz;

 $f_o = output frequency in MHz;$

C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

Table 9. **Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 9

Symbol	Parameter	Conditions		–40 °C t	o +85 °C	-40 °C to	o +125 °C	Unit
				Min	Max	Min	Max	
C _L = 5 pF					1	1	1	
t _{pd}	propagation delay	A to Y; see Figure 8	<u>[1]</u>					
		$V_{CC} = 1.1 \text{ V}$ to 1.3 V		2.1	11.4	2.1	12.6	ns
		$V_{CC} = 1.4 \text{ V} \text{ to } 1.6 \text{ V}$		1.6	7.4	1.6	8.2	ns
		V_{CC} = 1.65 V to 1.95 V		1.4	5.9	1.4	6.5	ns
		V_{CC} = 2.3 V to 2.7 V		1.1	4.5	1.1	5.0	ns
		V_{CC} = 3.0 V to 3.6 V		1.0	3.9	1.0	4.3	ns
C _L = 10 p	F							
t _{pd}	propagation delay	A to Y; see Figure 8	<u>[1]</u>					
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		2.6	13.7	2.6	15.1	ns
		$V_{CC} = 1.4 \text{ V} \text{ to } 1.6 \text{ V}$		2.1	8.7	2.1	9.6	ns
		V_{CC} = 1.65 V to 1.95 V		1.8	7.0	1.8	7.7	ns
		V_{CC} = 2.3 V to 2.7 V		1.5	5.4	1.5	6.0	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$		1.4	4.5	1.4	5.0	ns

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Symbol	Parameter	Conditions		–40 °C to +85 °C		–40 °C to +125 °C		Unit
				Min	Max	Min	Max	
C _L = 15 pl	=							
t _{pd}	propagation delay	A to Y; see Figure 8	<u>[1]</u>					
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		3.0	15.8	3.0	17.4	ns
		$V_{CC} = 1.4 \text{ V}$ to 1.6 V		2.4	10.0	2.4	11.0	ns
		V_{CC} = 1.65 V to 1.95 V		2.1	8.0	2.1	8.8	ns
		V_{CC} = 2.3 V to 2.7 V		1.8	6.1	1.8	6.8	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$		1.8	5.0	1.8	5.5	ns
C _L = 30 pl	-							
t _{pd}	propagation delay	A to Y; see Figure 8	<u>[1]</u>					
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		4.0	19.0	4.0	20.9	ns
		$V_{CC} = 1.4 \text{ V} \text{ to } 1.6 \text{ V}$		3.2	12.9	3.2	14.2	ns
		V_{CC} = 1.65 V to 1.95 V		2.9	10.5	2.9	11.6	ns
		V_{CC} = 2.3 V to 2.7 V		2.6	7.6	2.6	8.4	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$		2.6	6.2	2.6	6.9	ns

Table 9. Dynamic characteristics ... continued

[1] t_{pd} is the same as t_{PLH} and t_{PHL} .

12. Waveforms

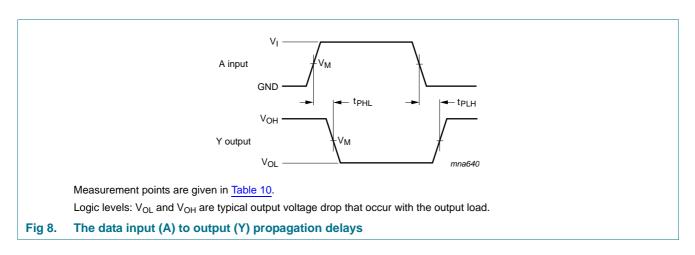


Table 10. Measurement points

Supply voltage	Output	Input		
V _{CC}	V _M	V _M	VI	$t_r = t_f$
0.8 V to 3.6 V	$0.5 \times V_{CC}$	$0.5 imes V_{CC}$	V _{CC}	≤ 3.0 ns

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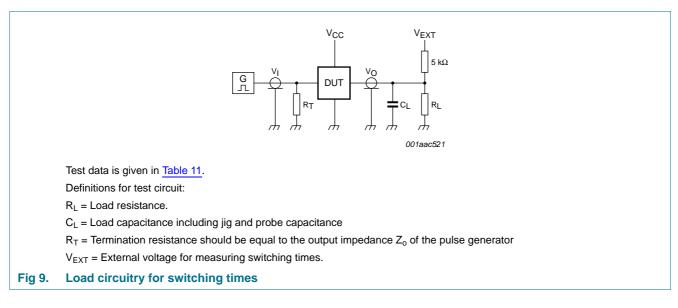


Table 11. Test data

Supply voltage	Load	V _{EXT}			
V _{CC}	CL	R _L [1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 k Ω or 1 M Ω	open	GND	$2 \times V_{CC}$

[1] For measuring enable and disable times $R_L = 5 k\Omega$, for measuring propagation delays, setup and hold times and pulse width $R_L = 1 M\Omega$.

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13. Package outline

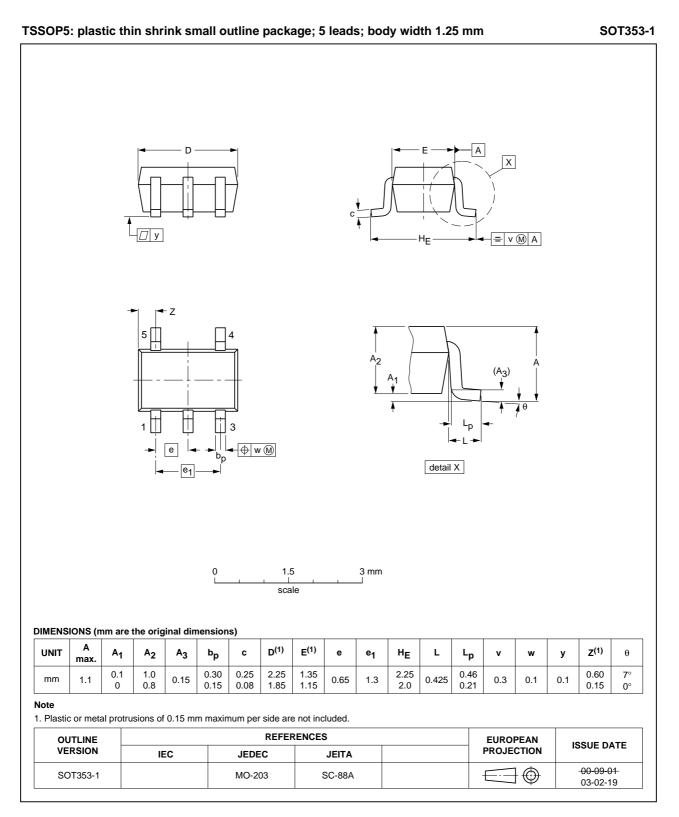
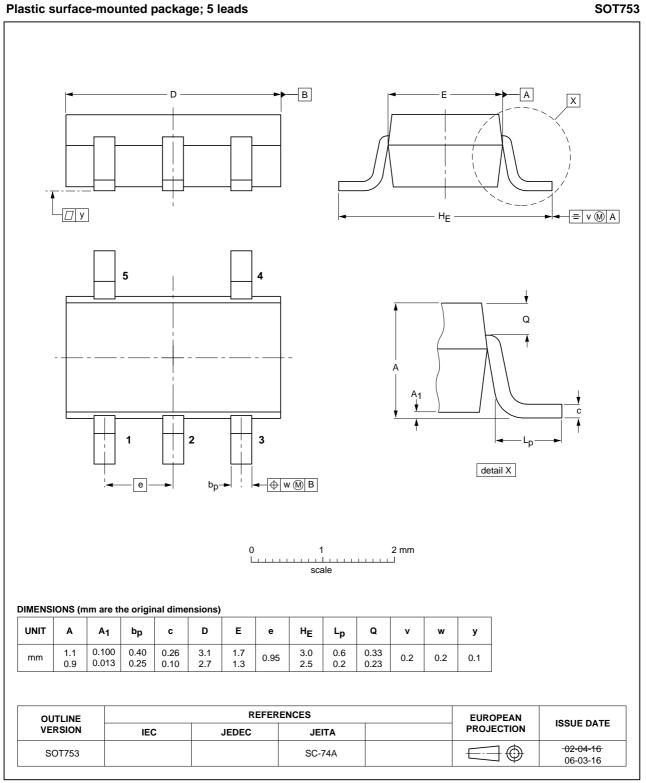


Fig 10. Package outline SOT353-1 (TSSOP5)

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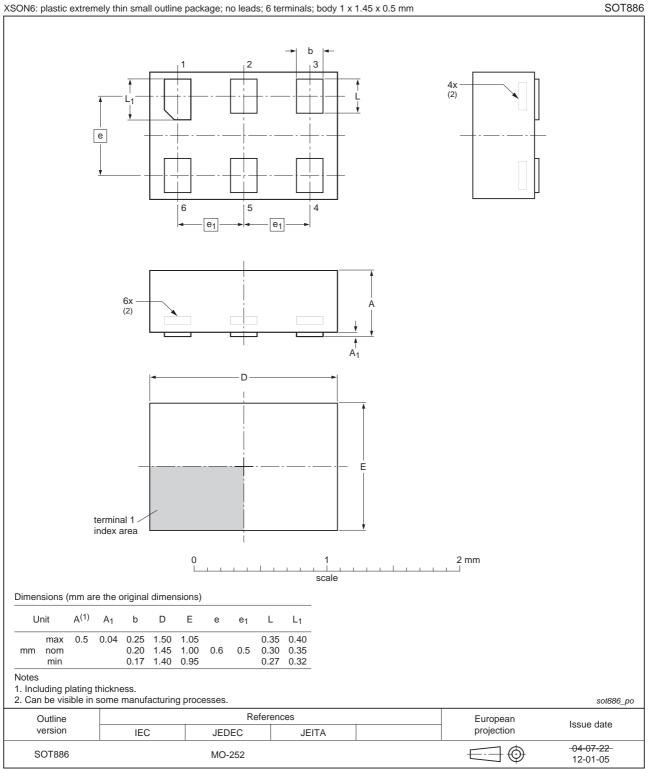
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Plastic surface-mounted package; 5 leads

Fig 11. Package outline SOT753 (SC-74A)

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XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm

Fig 12. Package outline SOT886 (XSON6)

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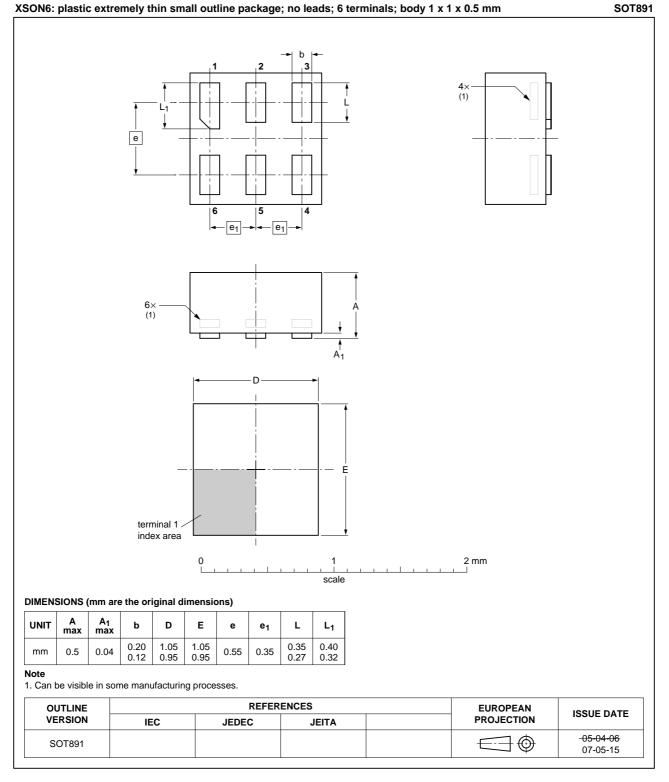
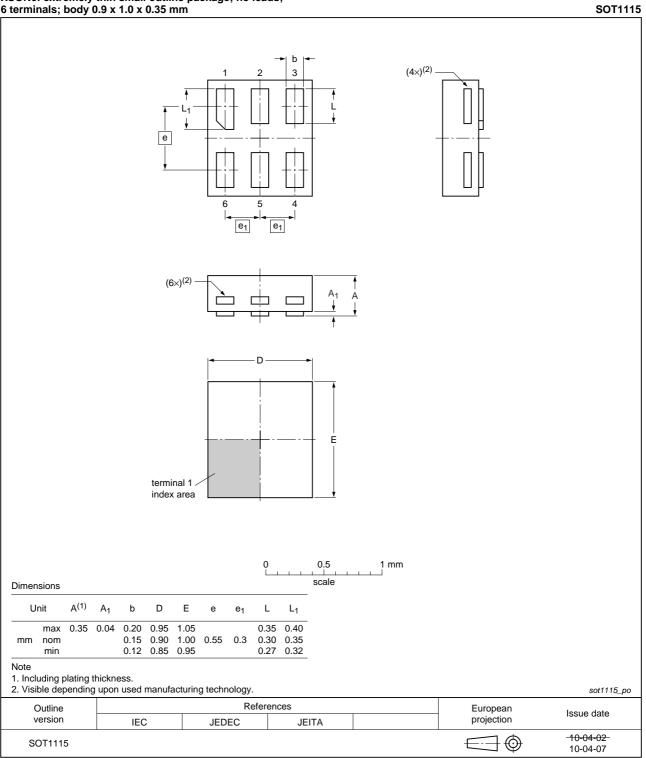


Fig 13. Package outline SOT891 (XSON6)

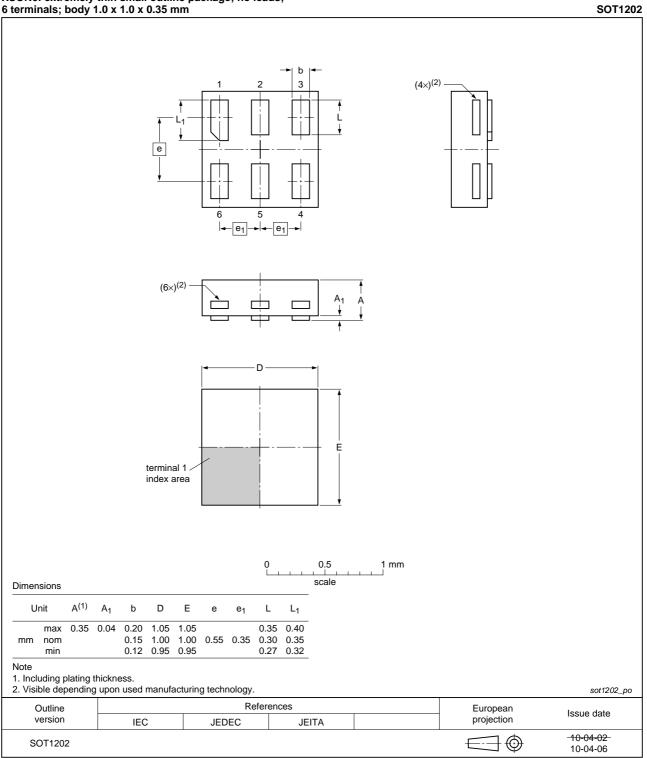
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XSON6: extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm

Fig 14. Package outline SOT1115 (XSON6)

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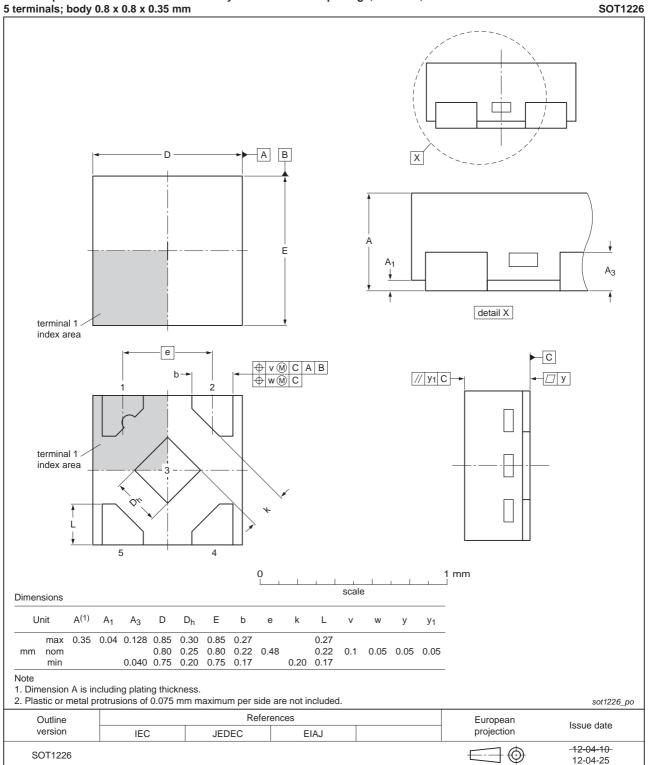


XSON6: extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm

Fig 15. Package outline SOT1202 (XSON6)

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X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 x 0.8 x 0.35 mm

Fig 16. Package outline SOT1226 (X2SON5)

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14. Abbreviations

Table 12. Abbreviations				
Acronym	Description			
CDM	Charged Device Model			
DUT	Device Under Test			
ESD	ElectroStatic Discharge			
HBM	Human Body Model			
MM	Machine Model			

15. Revision history

Table 13.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1G04 v.7	20120627	Product data sheet	-	74AUP1G04 v.6
Modifications:	 Added type n 	umber 74AUP1G04GX (SOT1	226)	
74AUP1G04 v.6	20120214	Product data sheet	-	74AUP1G04 v.5
Modifications:	 Package outli 	ne drawing of SOT886 (Figure	e 12) modified.	
74AUP1G04 v.5	20111205	Product data sheet	-	74AUP1G04 v.4
Modifications:	 Legal pages u 	updated.		
74AUP1G04 v.4	20100630	Product data sheet	-	74AUP1G04 v.3
74AUP1G04 v.3	20091105	Product data sheet	-	74AUP1G04 v.2
74AUP1G04 v.2	20060628	Product data sheet	-	74AUP1G04 v.1
74AUP1G04 v.1	20050718	Product data sheet	-	-



16. Legal information

16.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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