



60V P-Channel Enhancement Mode MOSFET

Voltage

-60 V

Current

-3 A

Features

- $R_{DS(ON)}$, $V_{GS}@-10V$, $I_D@-2A<190m\Omega$
- $R_{DS(ON)}$, $V_{GS}@-4.5V$, $I_D@-1.5A<240m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

• Case: SOT-223 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.043 ounces, 0.123 grams

SOT-223 Drain Gate Source

Maximum Ratings and Thermal Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-60		
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V	
Continuous Drain Current (Note 4)	T _A =25°C	- I _D	-3	A	
	T _A =70°C		-2.4		
Pulsed Drain Current (Note 1)		I _{DM}	-12		
Power Dissipation	T _A =25°C	P _D	3.1		
	T _A =70°C		2	W	
Single Pulse Avalanche Energy (Note 6)		E _{AS}	32	mJ	
Operating Junction and Storage Temperature Range		T_{J} , T_{STG}	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient (Note 4,5)		$R_{\theta JA}$	40.3	°C/W	

• Limited only By Maximum Junction Temperature

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Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS		
Static								
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-60	-	-	V		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-1	-1.88	-2.5			
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-2A	-	140	190	mΩ		
		V _{GS} =-4.5V, I _D =-1.5A	-	190	240			
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-60V, V _{GS} =0V	-	-	-1	uA		
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA		
Dynamic (Note 7)								
Total Gate Charge	Qg	V_{DS} =-30V, I_{D} =-2A, V_{GS} =-10V (Note 2,3)	-	8.3	-	nC		
Gate-Source Charge	Q _{gs}		-	1.8	-			
Gate-Drain Charge	Q_{gd}		-	1.6	-			
Input Capacitance	Ciss	V _{DS} =-30V, V _{GS} =0V, f=1MHZ	-	430	-	pF		
Output Capacitance	Coss		-	33	-			
Reverse Transfer Capacitance	Crss	I= IIVIIIZ	-	29	-			
Turn-On Delay Time	td _(on)	\/ 20\/ I 4A	-	5.1	-			
Turn-On Rise Time	t _r	V_{DD} =-30V, I_{D} =-1A, V_{GS} =-10V, R_{G} =6 Ω (Note 2,3)	-	20	-	ns		
Turn-Off Delay Time	td _(off)		-	36	-			
Turn-Off Fall Time	t _f	K _G =012	-	11	-			
Drain-Source Diode								
Maximum Continuous Drain-Source	,				-2	Α		
Diode Forward Current	I _S		-	-	-2	A		
Diode Forward Voltage	V_{SD}	I _S =-1A, V _{GS} =0V	-	-0.78	-1	V		

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 4. The maximum current rating is package limited.
- 5. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. The test condition is L=1mH, I_{AS} =-8A, V_{DD} =-25V, V_{GS} =-10V
- 7. Guaranteed by design, not subject to production testing.



300

250

200

150

100

0

V_{GS}=-4.5V

 V_{GS} =-10V

 $R_{DS}(on)$ - On-Resistance(m Ω)



PJW3P06A

TYPICAL CHARACTERISTIC CURVES

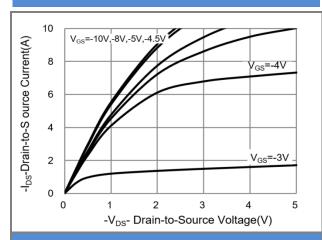
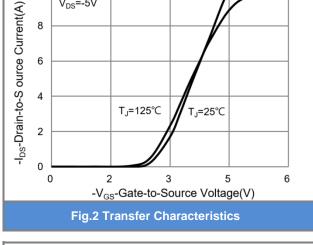
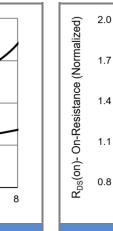
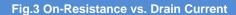


Fig.1 Output Characteristics



 V_{DS} =-5V





4

-I_{DS}-Drain-to-Source Current(A)

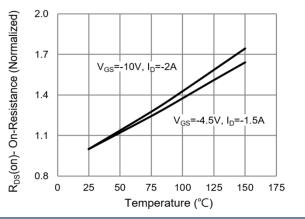


Fig.4 On-Resistance vs. Junction temperature

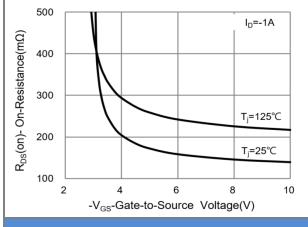


Fig.5 On-Resistance Variation with V_{GS}

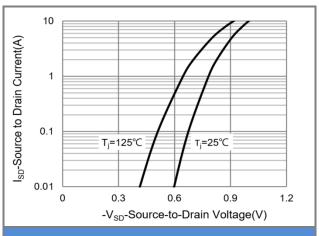


Fig.6 Source-Drain Diode Forward Voltage

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TYPICAL CHARACTERISTIC CURVES

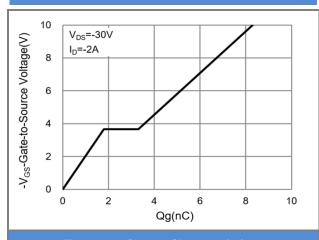


Fig.7 Gate-Charge Characteristics

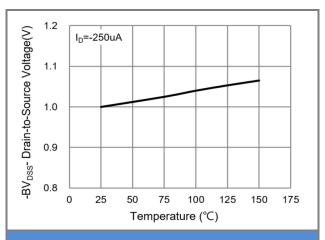


Fig.8 Breakdown Voltage Variation vs. Temperature

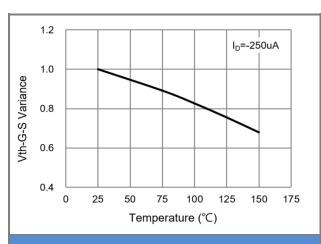


Fig.9 Threshold Voltage Variation with Temperature

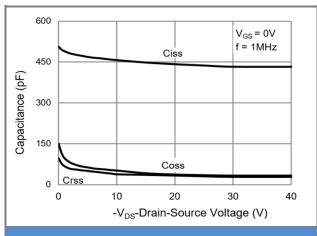


Fig.10 Capacitance vs. Drain-Source Voltage

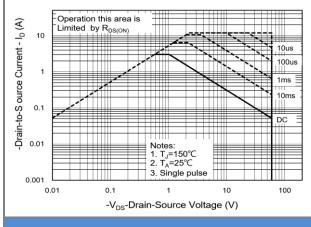


Fig.11 Maximum Safe Operating Area

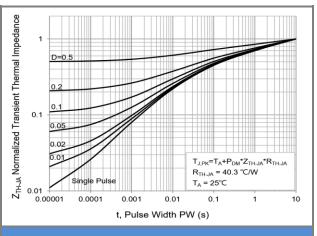


Fig.12 Normalized Transient Thermal Impedance

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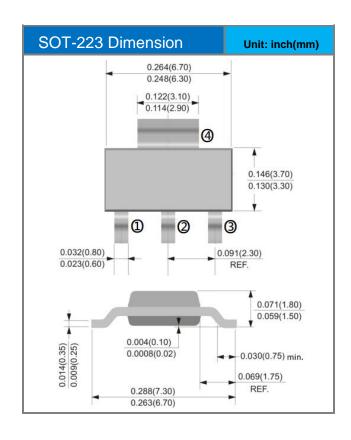


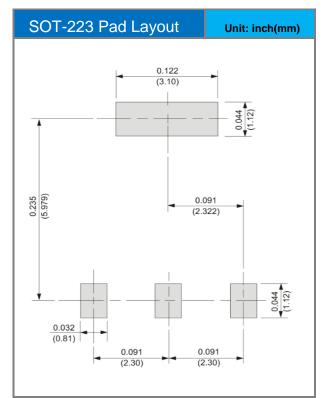


Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version	
PJW3P06A_R2_00001	SOT-223	2,500pcs / 13" reel	W3P06A	Halogen free	

Packaging Information & Mounting Pad Layout









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