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Surface Mount Trench MOS Barrier Schottky Rectifier



PRIMARY CHARACTERISTICS			
Package	DO-221AC		
I _{F(AV)}	5.0 A		
V _{RRM}	45 V		
I _{FSM}	100 A		
V_F at $I_F = 5.0$ A	0.39 V		
T _J max.	150 °C		
Diode variations	Single		

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

FEATURES

- Very low profile typical height of 0.95 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

MECHANICAL DATA

Case: DO-221AC (SlimSMA)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD22-B102

M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VSSAF5L45	UNIT	
Device marking code		5L45		
Maximum repetitive peak reverse voltage	V _{RRM}	45	V	
Maximum DC forward current	I _F ⁽¹⁾	5.0	— A	
	I _F ⁽²⁾	3.0		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	100	А	
Operating junction and storage temperature range	T _J , T _{STG}	- 40 to + 150	°C	

Notes

(1) Mounted on 10 mm x 10 mm pad areas, 2 oz. FR4 PCB

⁽²⁾ Free air, mounted on recommended copper pad area

1

COMPLIANT

HALOGEN

FREE

VSSAF5L45



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ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 2.5 A	T 25 °C	V _F ⁽¹⁾	0.42	-	v
	$I_F = 5.0 \text{ A}$			0.47	0.56	
	I _F = 2.5 A	T _A = 125 °C		0.31	-	
	I _F = 5.0 A			0.39	0.47	
Reverse current	V _B = 45 V	T _A = 25 °C	I _R ⁽²⁾	-	650	μA
	v _R = 43 v	T _A = 125 °C		8	45	mA
Typical junction capacitance	4.0 V, 1 MH	4.0 V, 1 MHz		740	-	pF

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise specified)				
PARAMETER	SYMBOL VSSAF5L45		UNIT	
Typical thermal resistance	R _{0JA} ⁽¹⁾	115	°C/W	
	R _{0JM} ⁽²⁾	12		

Notes

 $^{(1)}$ Free air, mounted on recommended PCB, 1 oz. pad area; thermal resistance $R_{\theta JA}$ - junction to ambient

⁽²⁾ Mounted on 10 mm x 10 mm pad areas, 2 oz. FR4 PCB; $R_{\theta JM}$ - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
VSSAF5L45-M3/6A	0.032	6A	3500	7" diameter plastic tape and reel	
VSSAF5L45-M3/6B	0.032	6B	14 000	13" diameter plastic tape and reel	
VSSAF5L45HM3/6A (1)	0.032	6A	3500	7" diameter plastic tape and reel	
VSSAF5L45HM3/6B ⁽¹⁾	0.032	6B	14 000	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

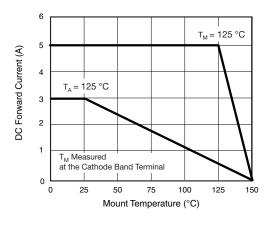


Fig. 1 - Maximum Forward Current Derating Curve

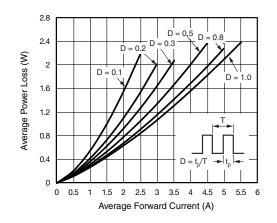


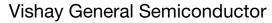
Fig. 2 - Average Power Loss Characteristics

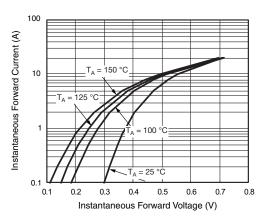
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2

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Fig. 3 - Typical Instantaneous Forward Characteristics

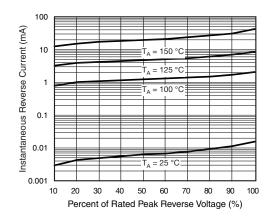


Fig. 4 - Typcial Reverse Leakage Characteristics

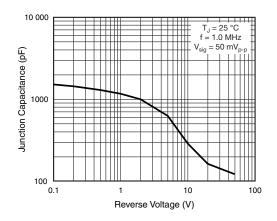


Fig. 5 - Typical Junction Capacitance

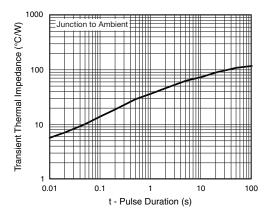


Fig. 6 - Typcial Transient Thermal Impedance

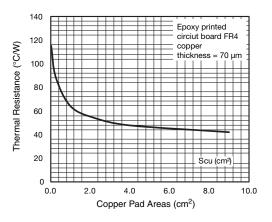


Fig. 7 - Thermal Resistance Junction to Ambient vs. Copper Pad Areas

Revision: 06-Jun-13

3

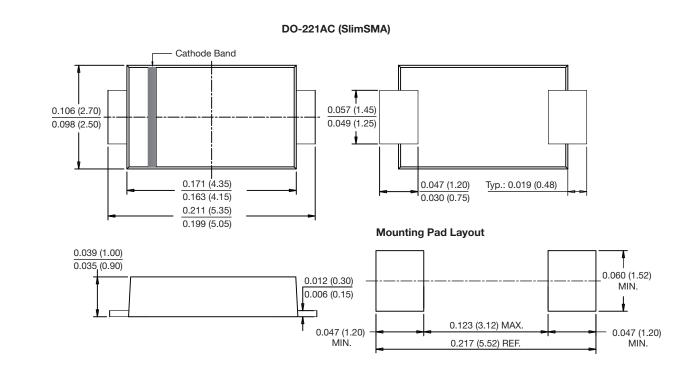
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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