Product data sheet

1. General description

High power density, hyperfast switching time recovery rectifier with high-efficiency planar technology, encapsulated in a CFP15B (SOT1289B) power and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

Reverse voltage: V_R ≤ 200 V

Forward current: I_F ≤ 10 A

Switching time: $t_{rr} \le 30 \text{ ns}$

Pt doped life time control

Low inductance

Power and flat lead SMD plastic package

Package height typical 0.95 mm

High power capability due to clip-bond technology

Planar die design

Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- General-purpose rectification
- Reverse polarity protection
- Hyperfast switching
- Freewheeling applications
- Engine Control Unit (ECU)

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 171 °C		-	-	10	А
V _R	reverse voltage	T _j = 25 °C		-	-	200	V
V_{RRM}	repetitive peak reverse voltage			-	-	200	V
V _F	forward voltage	I _F = 10 A; T _j = 25 °C	[1]	-	890	960	mV
		I _F = 10 A; T _j = 125 °C	[1]	-	750	820	mV
I _R	reverse current	V _R = 200 V; T _j = 25 °C	[1]	-	-	1	μA
		V _R = 200 V; T _j = 125 °C	[1]	-	3	25	μΑ

^[1] Very short pulse, in order to maintain a stable junction temperature.



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A1	anode 1	5.	
2	A2	anode 2		K A1
3	K	cathode]2	A2 aaa-033688
			CFP15B (SOT1289B)	

6. Ordering information

Table 3. Ordering information

Type number	Package						
	Name	Description	Version				
PNE200100EPE-Q		plastic, thermal enhanced ultra thin SMD package; 3 leads; 2.13 mm pitch; 5.8 x 4.3 x 0.95 mm body	<u>SOT1289B</u>				

7. Marking

Table 4. Marking codes

Type number	Marking code
PNE200100EPE-Q	200E
	110E

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC60134)

Symbol	Parameter	Conditions		Min	Max	Unit
V_R	reverse voltage	T _j = 25 °C		-	200	V
V_{RRM}	repetitive peak reverse voltage			-	200	V
V _{R(RMS)lim}	limiting RMS reverse voltage			-	140	V
I _F	forward current	δ = 1; $T_{sp} \le 150 ^{\circ}\text{C}$		-	14.1	А
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 171 °C		-	10	Α
I _{FSM}	non-repetitive peak forward current	t_p = 8.3 ms; single half sine wave (applied at rated load condition); $T_{j(init)}$ = 25 °C		-	190	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	1.75	W
			[2]	-	2.15	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

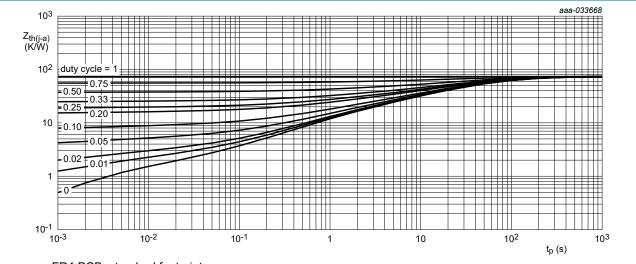
Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint. Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 $\rm cm^2$.

9. Thermal characteristics

Table 6. Thermal characteristics

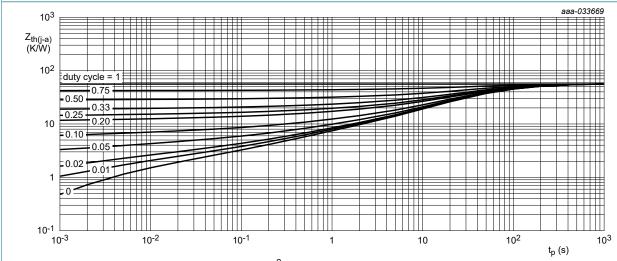
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from	in free air	[1]	-	-	85	K/W
junction to ambient		[2]	-	-	70	K/W	
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[3]	-	-	1.2	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [3] Soldering point of cathode tab.



FR4 PCB, standard footprint

Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm²

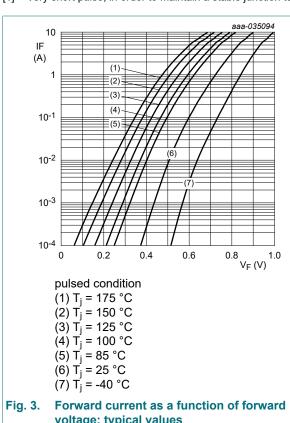
Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	I _R = 100 μA; T _j = 25 °C	[1]	200	-	-	V
V _F	forward voltage	I _F = 10 A; T _j = 25 °C	[1]	-	890	960	mV
		I _F = 10 A; T _j = 125 °C	[1]	-	750	820	mV
I _R	reverse current	V _R = 200 V; T _j = 25 °C	[1]	-	-	1	μΑ
		V _R = 200 V; T _j = 125 °C	[1]	-	3	25	μΑ
C _d	diode capacitance	V _R = 4 V; f = 1 MHz; T _j = 25 °C		-	99	-	pF
t _{rr}	reverse recovery time step recovery	$I_F = 0.5 \text{ A}; I_R = 1 \text{ A}; I_{R(meas)} = 0.25 \text{ A};$ $T_j = 25 \text{ °C}$		-	16	30	ns
	reverse recovery time ramp recovery	$dI_F/dt = 50 \text{ A/}\mu\text{s}; I_F = 1 \text{ A}; V_R = 30 \text{ V};$ $T_j = 25 ^{\circ}\text{C}$		-	21	-	ns
V_{FRM}	peak forward recovery voltage	$I_F = 1 \text{ A}; \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}; T_j = 25 ^{\circ}\text{C}$		-	780	-	mV

[1] Very short pulse, in order to maintain a stable junction temperature.



voltage; typical values

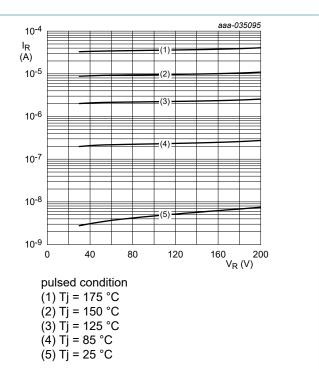


Fig. 4. Reverse current as a function of reverse voltage; typical values

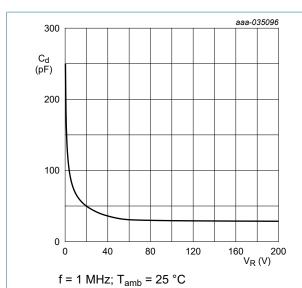
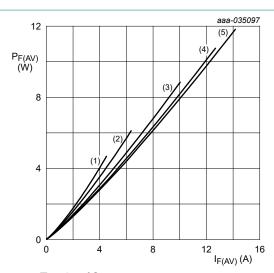


Fig. 5. Diode capacitance as a function of reverse voltage; typical values



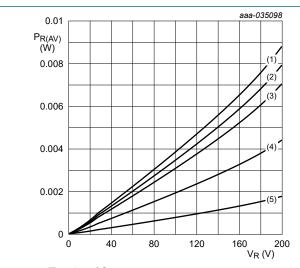
 $T_j = 175 \,^{\circ}\text{C}$ (1) $\delta = 0.1$

(2) $\delta = 0.2$ (3) $\delta = 0.5$

 $(4) \delta = 0.8$

(5) $\delta = 1$; DC

Fig. 6. Average forward power dissipation as a function of average forward current; typical values



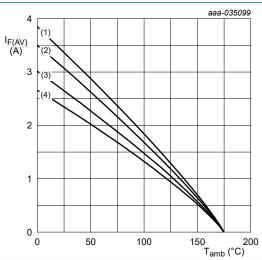
 $T_j = 175 \,^{\circ}\text{C}$ (1) $\delta = 1$; DC (2) $\delta = 0.9$

 $(3) \delta = 0.8$

 $(4) \delta = 0.5$

 $(5) \delta = 0.2$





FR4 PCB, standard footprint

 $T_i = 175 \,{}^{\circ}\text{C}$

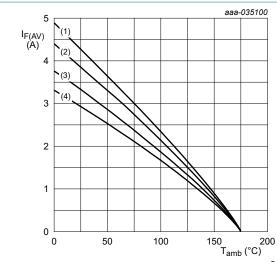
 $(1) \delta = 1; DC$

(2) δ = 0.5; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

 $(4) \delta = 0.1$; f = 20 kHz

Fig. 8. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1 cm²

 $T_i = 175 \,{}^{\circ}\text{C}$

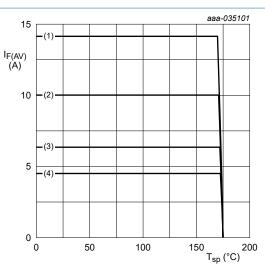
 $(1) \delta = 1$; DC

 $(2) \delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

 $(4) \delta = 0.1$; f = 20 kHz

Fig. 9. Average forward current as a function of ambient temperature; typical values



T_i = 175 °C

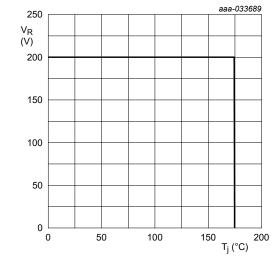
 $(1) \delta = 1; DC$

(2) δ = 0.5; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

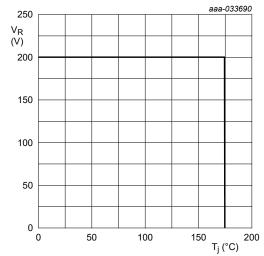
 $(4) \delta = 0.1$; f = 20 kHz

Fig. 10. Average forward current as a function of solder point temperature; typical values



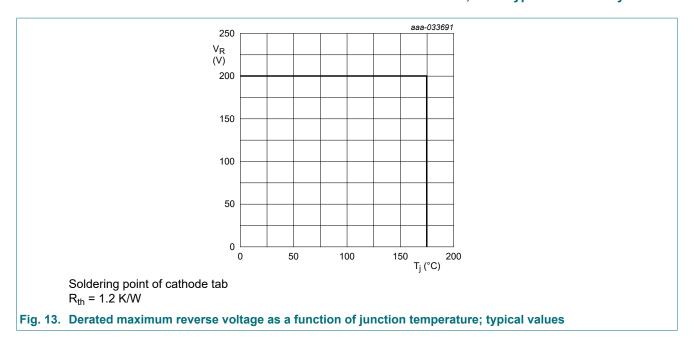
FR4 PCB, standard footprint $R_{th} = 85 \text{ K/W}$

of junction temperature; typical values

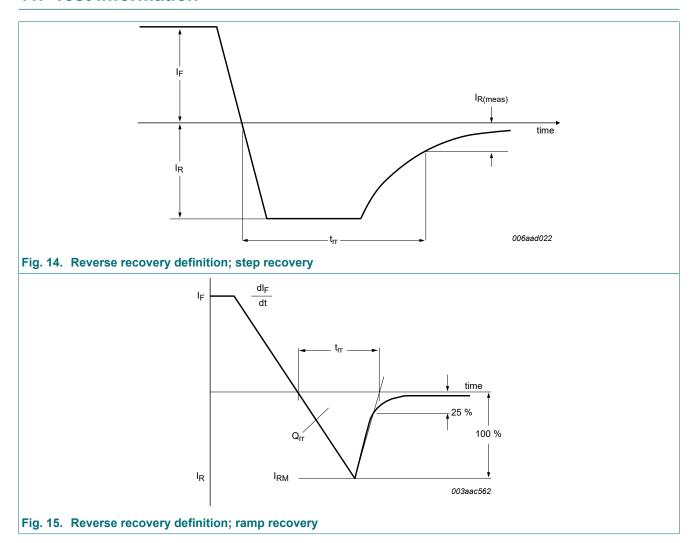


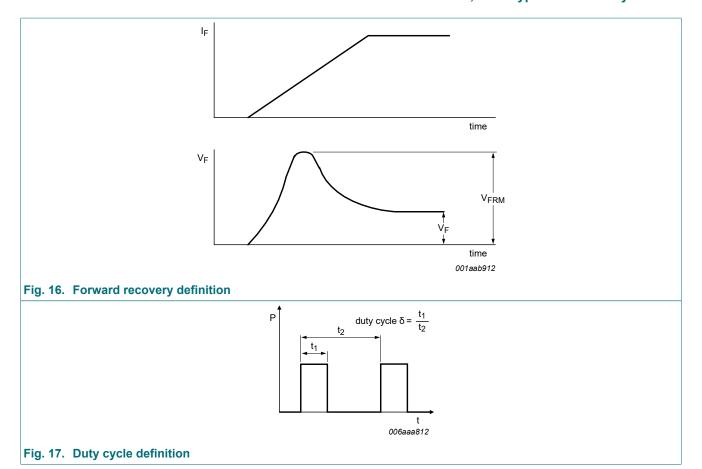
FR4 PCB, mounting pad for cathode 1 cm² $R_{th} = 70 \text{ K/W}$

Fig. 11. Derated maximum reverse voltage as a function | Fig. 12. Derated maximum reverse voltage as a function of junction temperature; typical values



11. Test information





The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current

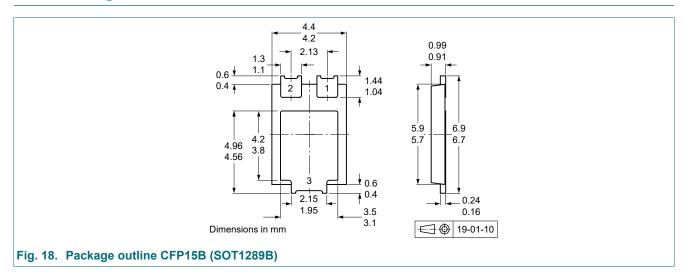
 $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_{M} \times \sqrt{\delta}$

with $I_{\mbox{\scriptsize RMS}}$ defined as RMS current.

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering

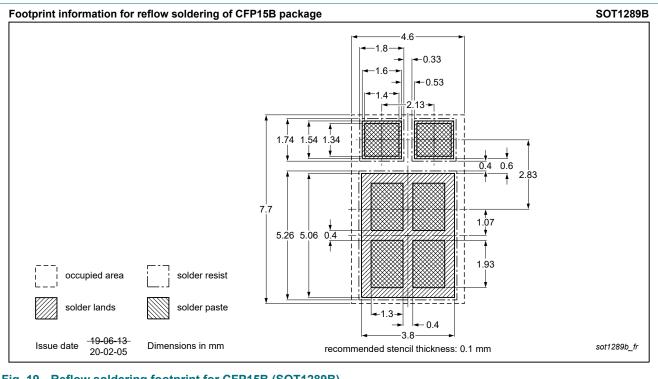


Fig. 19. Reflow soldering footprint for CFP15B (SOT1289B)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PNE200100EPE-Q v.1	20220616	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Product data sheet

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