PNP -100mA -50V Digital Transistor (Bias Resistor Built-in Transistor)

Datasheet

AEC-Q101 Qualified

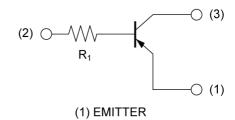
Parameter	Value
V _{CEO}	-50V
I _C	-100mA
R ₁	10kΩ

● Outline SOT-323 SC-70 (2) (UMT3)

Features

- 1) Built-In Biasing Resistor
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary NPN Types: DTC114TU3 HZG

•Inner circuit



(2) BASE (3) COLLECTOR

Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTA114TU3 HZG	SOT-323 (UMT3)	2021	T106	180	8	3000	94

● Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Values	Unit
Collector-base voltage	V_{CBO}	-50	V
Collector-emitter voltage	V_{CEO}	-50	V
Emitter-base voltage	V _{EBO}	-5	V
Collector current	I _C	-100	mA
Power dissipation	P _D *1	200	mW
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

● Electrical characteristics (T_a = 25°C)

Darameter	Cymah al	Conditions	Values			1.124	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Collector-base breakdown voltage	BV _{CBO}	BO I _C = -50μA		-	-	V	
Collector-emitter breakdown voltage	BV _{CEO}	CEO I _C = -1mA		-	-	V	
Emitter-base breakdown voltage	BV _{EBO}	I _E = -50μA	-5	-	-	V	
Collector cut-off current	I _{CBO}	V _{CB} = -50V	-	-	-500	nA	
Emitter cut-off current	I _{EBO}	V _{EB} = -4V	-	-	-500	nA	
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = -10mA, I _B = -1mA	-	-	-300	mV	
DC current gain	h _{FE}	$V_{CE} = -5V, I_{C} = -1mA$	100	250	600	-	
Input resistance	R ₁	-	7	10	13	kΩ	
Transition frequency	f _T *2	V _{CE} = -10V, I _E = 5mA, f = 100MHz	-	250	-	MHz	

^{*1} Each terminal mounted on a reference land.

^{*2} Characteristics of built-in transistor

● Electrical characteristic curves (T_a =25°C)

Fig.1 Grounded emitter propagation characteristics

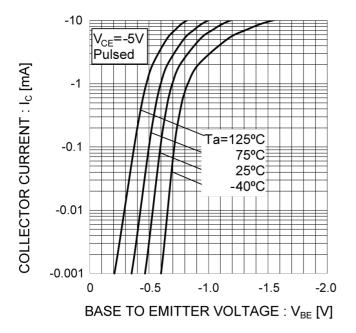
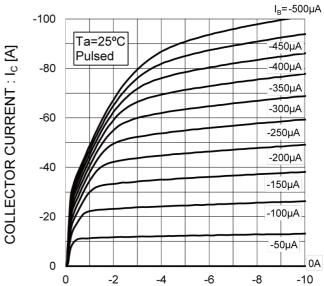


Fig.2 Typical Output Characteristics



COLLECTOR TO EMITTER VOLTAGE: V_{CE} [V]

Fig.3 DC Current Gain vs. Collector Current

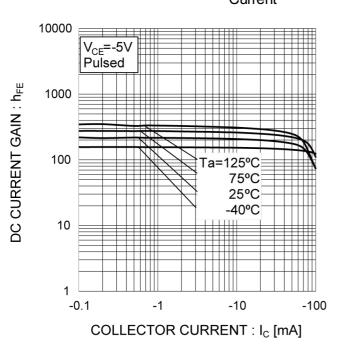
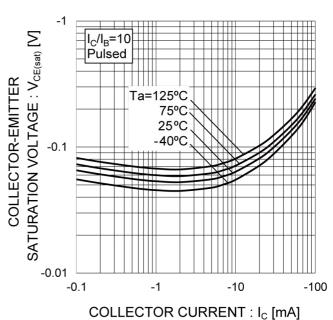
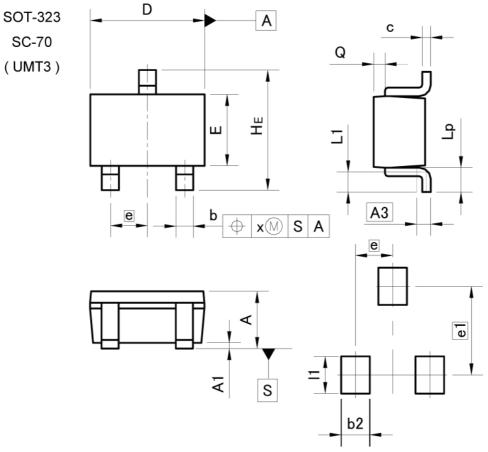


Fig.4 Collector-Emitter Saturation
Voltage vs. Collector Current



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	0.80	1.00	0.031	0.039	
A1	0.00	0.10	0.000	0.004	
A3	0.5	25	0.0	.010	
b	0.25	0.40	0.010	0.016	
С	0.10	0.20	0.004	0.008	
D	1.90	2.10	0.075	0.083	
E	1.15	1.35	0.045	0.053	
е	0.	65	0.0	26	
HE	2.00	2.20	0.079	0.087	
L1	0.10	0.40	0.004	0.016	
Lp	0.25	0.55	0.010	0.022	
Q	0.10	0.30	0.004	0.012	
х	_	0.10	_	0.004	

DIM	MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	_	0.50	_	0.020	
e1	1.55		0.0	61	
- 11	-	0.65	-	0.026	

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
CLASSⅢ	OL ACOM	CLASS II b	ОГУООШ
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - If Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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