General Description

The MAX3522B evaluation kit (EV kit) simplifies the testing and evaluation of the MAX3522B DOCSIS 3.1 upstream amplifier. The EV kit is fully assembled and tested at the factory. Standard 50 Ω SMA connectors are included on the EV kit for the inputs and outputs to allow quick and easy evaluation on the test bench.

This document provides a list of equipment required to evaluate the device, a straightforward test procedure to verify functionality, a description of the EV kit circuit, the circuit schematic, a list of components for the EV kit, and artwork for each layer of the PCB.

Features

- Easy Evaluation of the MAX3522B
- 50Ω SMA Connectors
- All Critical Peripheral Components Included
- Reference Design Ready Layout
- Fully Assembled and Tested
- PC Control Software (Available at www.maximintegrated.com)

Ordering Information appears at end of data sheet.

Quick Start

Required Equipment

- MAX3522B EV Kit Board (included)
- One power supply capable of supplying at least 2000mA at +5V
- One RF signal generator capable of delivering at least -10dBm of output power at up to 204MHz frequency (HP8482A or equivalent)
- One RF spectrum analyzer capable of covering the operating frequency range of the device
- One Windows PC with available USB port
- Two 50Ω SMA cables
- Mini-USB type A-to-type B cable
- (Optional) One network analyzer to measure return loss
- (Optional) One ammeter to measure supply current

Procedure

This section provides a step-by-step guide to testing the basic functionality of the EV kit.

- With its output disabled, set the DC power supply to +5V. If available, set the power supply's current limit to 2000mA. Connect the power supply to the +5V (through an ammeter if desired) and GND terminals on the EV kit, as shown in <u>Figure 1</u>.
- With its output disabled, set the RF signal generator to 85MHz and a power level of -20dBm. Connect the output of the RF signal generator to the SMA connector labeled J1 RF IN on the evaluation board, as shown in Figure 1.
- Connect the SMA connector labeled J2 RFOUT1 or J3 RFOUT2 on the evaluation board to a spectrum analyzer, as shown in <u>Figure 1</u>.
- 4) Connect the USB cable from FT4232 module's USB mini-B port to PC USB port, as shown in Figure 1.
- 5) Download the MAX3522B control software (MAX3522_SetupXXXX.zip), at <u>https://www.maximintegrated.com/en/design/tools/applications/</u> <u>evkit-software</u>
- 6) Extract the zip file and run the installation file.



Evaluates: MAX3522B

- 7) Turn on the +5V power supply.
- 8) Run MAX3522.exe. MAX3522B GUI should be launched as shown in <u>Figure 2</u>. Please notice that the sequence of first turning on the power supply then starting the GUI is very important. This sequence ensures proper initialization of the MAX3522B. If the setup is successful, GUI should indicate "interface connected", in the lower right corner of the GUI (see <u>Figure 2</u>).
- 9) On the MAX3522B GUI, hit Refresh ROM button, located in the upper portion of the GUI (see Figure 2).
- On the MAX3522B GUI, set Power Code = 3, Gain Code = 63, (located in the upper-left section of the GUI) by using the up/down arrow buttons or typing the value (see <u>Figure 2</u>).
- 11) On the MAX3522B GUI, Turn on the TX_ENABLE switch (see Figure 2). The supply current from the +5V supply should read approximately 1100 mA. Be sure to adjust the power supply to account for any voltage drop across the ammeter.
- 12) Depending on which output SMA is connected, user needs to set PASELECT bit accordingly. On the MAX3522B GUI, click 01 CTRL and then set PASELECT (see Figure 3) by clicking the register value (default = 0b). PASLECT = 0 enables OUTPUT1 (J2), PASLECT = 1 enables OUTPUT2 (J3).
- 13) Enable the RF signal generator's output.
- 14) Check the output level on the spectrum analyzer. Expected power level is about -7dBm at 85MHz.

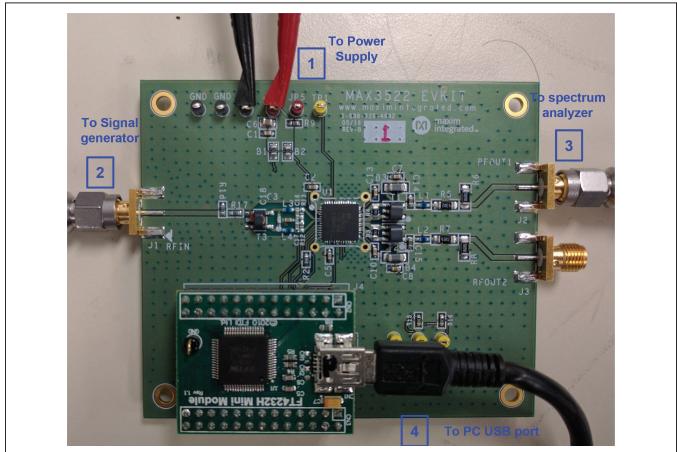


Figure 1. EV Kit Hardware Setup

Evaluates: MAX3522B

File Options Settings H	reip				[1]	
Power Code Gain Code 3 • 63 •		Regs. 1	To Clipboard Clipboa	d To Regs. He	Transmitting	9 Refresh ROM
Write All Registers	Value: FF	🕂 🖲 Hex	C Dec		Read Register 0	Write Register 0
00 GAIN 01 CTRL	GAIN				Index: 0d (
	7	6	5	4	3 2 1	0
	PC<1:	0>			GC<5:0>	
	116				111111b	
	Index	Туре	Name	Reset	Description	
	Bit 7:6		PC<1:0>	0x00	PowerControl, controls OUTP/OUTN bias current 00 Minimum power output 01 284min 11 489min 11 Maximum powercutput, 6.6d8+min	
Find: Bit Fields					Grin Control	

Figure 2. EV Kit GUI Register00

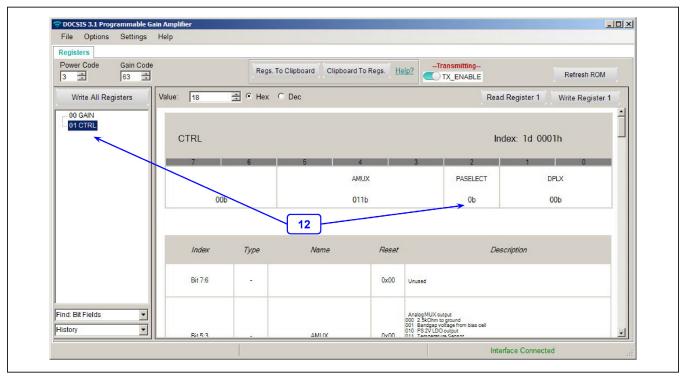


Figure 3. EV Kit GUI Register01

Detailed Description of Hardware (or Software)

Gain Calculation

A minimum loss pad (MLP R18/R17) after the input port (J1) transforms the 50 Ω test equipment impedance to 100 Ω input transformer (T3) impedance, with a voltage loss of 4.7dB. T3 converts the unbalanced 100 Ω impedance to a balanced 100 Ω , matching the overall impedance of input of the MAX3522B (200 Ω) paralleled with 200 Ω resistor (R12 + R13). The power loss of T3 is approximately 0.5dB across the operating frequency range of the MAX3522B. So the total voltage loss due to T3 and MLP (R17/R18) is 5.2 dB.

T3 and MLP (R17/R18) are included to assist in evaluation of the IC using standard 50Ω lab equipment. They are not required for most applications.

The 1:4 impedance output transformer (T1 or T2) provides 6dB voltage gain, which is included in overall MAX3522B voltage gain specified in the IC data sheet.

A minimum loss pad (MLP R5/R6 or R7/R8) after the output of T1 or T2 transforms the 75 Ω output impedance of the T1/T2 to 50 Ω test equipment impedance, with a voltage loss of 7.5dB. To calculate the voltage gain of the MAX3522B between J1 and J2 or between J1 and J3:

Voltage Gain = POUT(dBm) + 7.5dB (MLP) - [PIN(dBm) - 5.2dB (T3/MLP)]

= POUT(dBm) - PIN(dBm) + 12.7dB

The voltage gain should be between 24.2dB and 26.2dB under gain code 63.

Layout Considerations

The MAX3522B is a high-performance RF amplifier. PCB layout and external component selection is critical to achieve specified performance. We strongly recommend the user to follow the EV kit PCB layout, including number and placement of ground throughputs, internal layer geometry, and component size, as closely as possible. Keep RF signal lines as short as possible to minimize losses and radiation. This is especially true for the output traces between the MAX3522B and transformers T1/T2.

To minimize second-order distortion, traces in the balanced input and output circuitry should be as symmetric as possible. The exposed pad must be soldered evenly to the board's ground plane for proper operation. Use abundant throughputs beneath the exposed pad, and surrounding area for maximum heat dissipation. Use abundant ground throughputs between RF traces to minimize undesired coupling.

To minimize coupling between different sections of the device, the ideal power-supply layout is a star configuration, which has a large decoupling capacitor at the central V_{CC} node. The V_{CC} traces branch out from this node, with each trace going to separate V_{CC} pins of the device. Each V_{CC} pin must have a bypass capacitor with low impedance to ground at the frequency of interest. Do not share ground vias among multiple connections to the PCB ground plane.

Component Suppliers

SUPPLIER	WEBSITE
Emerson Network Power	www.emersonnetworkpower.com
Keystone Electronics Corp.	www.keyelco.com
Murata Americas	www.murata.com
MACOM	www.macom.com

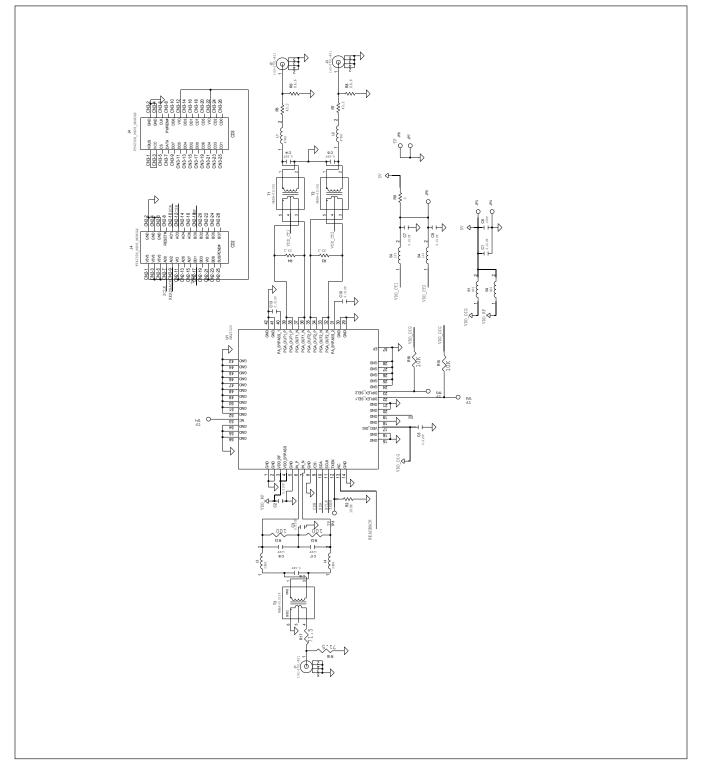
Evaluates: MAX3522B

B1.B2 2 FERRITE-BEAD (0805) MUFAT ABUATAGE01SN1D C1-05, G7, C8, C10, C13 8 0. FRANTE-BEAD/UTAT AGRAIDBATA GRAIDBATA GRAIDBATA C1-05, G7, C8, C10, C13 8 0. FRANTE-BEAD/UTATA GRAIDBATA GRAIDBATA C1-05, G7, C8, C10, C13 8 0. FRANTE-BEAD/UTATA GRAIDBATA GRAIDBATA C16 1 1. 10µF ±10% ceramic capacitors (0605) MURATA GRAIDBATA GRAIDBACD50 C16 2 8. 68F ± 0.25FF ceramic capacitors (0603) MURATA GRAIDBAGD50 C16 1 3 5. 68F ± 0.25FF ceramic capacitors (0603) MURATA GRAIDBAGD50 C16 1 3 5. 68F ± 0.25FF ceramic capacitors (0603) MURATA GRAIDBASC1H5R6CA01 Ju-J3 3 S. FLA ± 0.25FF ceramic capacitors (0603) MURATA GRAIDBSC1H5R6CA01 Ju-J3 3 S. FLA ± 0.25FF ceramic capacitors (0603) MURATA GRAIDBSC1H5R6CA01 Ju-J3 3 S. FLA ± 0.25FF ceramic capacitors (0603) MURATA GRAIDBSC1H5R6CA01 Ju-J3 3 S. FLA ± 0.25FF ceramic capacitors (0603) MURATA GRAIDBSC1H5R6CA01 Ju-J3 3 S. FLA ± 0.25FF ceramic capacitors (0603) MURATA GRAIDBSC1H5R6CA01 Ju-J3 3 S. FLA ± 0.25FF ceramic capacitors (0603) MURATA GRAIDBSC1H5R6CA01 Ju-J3	Designation	Qty	Description
C5, C7, C8, C10, C13 2 5 2 7 2 7 2 7 2 7 2 6 3 3, TP8 3 3 2 6 2 7 2 8 2 9 2 11 1 12 2 14 2 15 2 16 2 17 2 18 2 19 2 20 2 11 2 12 2 13 2 14 1	B1, B2	2	FERRITE-BEAD (0805) MURATA BLM21AG601SN1D
C5, C7, C8, C10, C13 8 5 7 7 2 7 7 2 7 7 2 7 7 2 7 7 2 8 5 7 2 8 7 9	B3, B4	2	FERRITE-BEAD(1206) MURATA BLM31PG121SN1
5 7 7 5 6 9 9 1 1 1 1 2 2 2 3 7 1 1 1 2 3 4 1 1 2 2 3 7 2 3 7 2 2 2 2 2 2 2 2 2 2 2 2 2	C1-C3, C5, C7, C8, C10, C13	∞	0.01µF ±10% ceramic capacitors (0603) MURATA GRM188R71C103KA01
5 7 5 6 9 7 1 1 1 2 8 9 1 2 2 2 2 2 2 2 2 2 2 2 2 2	C6	-	10µF ±10% ceramic capacitors (0805) MURATA GRM21BR71A106KE51
7 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	C14, C15	2	6.8pF ±0.25PF ceramic capacitors (0603) MURATA GRM39C0G6R8C050
5 3 5 3 6, JP7 3 3, TP8 3 <t< th=""><td>C16, C17</td><td>2</td><td>12pF ±0.25PF ceramic capacitors (0603) MURATA GRM1885C1H120FZ01</td></t<>	C16, C17	2	12pF ±0.25PF ceramic capacitors (0603) MURATA GRM1885C1H120FZ01
5 6. JP7 6. JP7 3. TP8 3. TP8 3. TP8 3. тр8 3. тр8 3 3. тр8 3 3. тр8 3 3 3. тр8 3 3 3 5 6 6 5 8 8 8 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	C18	-	5.6PF ±0.25PF ceramic capacitors (0603) MURATA GRM1885C1H5R6CA01
5 6, JP7 3, TP8 3, TP8 3, TP8 3, TP8 3 3 3 4 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	J1-J3	ę	SMA end-launch jack receptacles Emerson JOHNSON COMPONENTS 142-0701-801
5 6, JP7 3, TP8 3, TP8 3 3, TP8 3 3, TP8 3 3 4 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	J4	-	2X 13 FEMALE HEADERS and FUTURE TECHNOLOGY DEVICES INTL LTD, FT4232H_MINI_MODULE
6, JP7 3 3, TP8 3 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	JP3, JP5	2	PC mini red test point KEYSTONE 5000
3, TP8 3 2 2 2 7 2 7 2 7 2 7 2 7 2 7 2 7	JP4, JP6, JP7	ო	PC mini black test point KEYSTONE 5001
2 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	TP1-TP3, TP8	ო	PC mini yellow test point KEYSTONE 5004
2 3 3 3 3 4 4 5 5 5 5 5 5 5 5 5 5 5 5 5 5	L1, L2	2	47nH ±5% ceramic chip inductor (0603)
	L3, L4	2	33nH ±2% wire wound chip inductor (0603)
00 03 03 04 05 05 05 05 05 05 05 05 05 05 05 05 05	R2	1	100K Ω ±5% resistor (0603)
00 00 00 00 00 00 00 00 00 00 00 00 00	R3, R4	2	33.2Ω ±1% resistor (0805)
00 00 00 00 00 00 00 00 00 00 00 00 00	R5, R7	2	43.2Ω ±1% resistor(1206)
	R6, R8	2	86.6Ω ±1% resistor(1206)
	R9	1	00 resistor (0603)
0 0 0	R12, R13	2	1000 ±1% resistor (0603)
	R15, R16	2	10KΩ ±1% resistor (0603)
T2 2 1 1 1 1	R17, R18	2	[71.5Ω ±1% resistor (0603)
	T1, T2	2	1:4 transformer MACOM MABA-011061
	Т3	1	1:1 transformer MACOM MABA-011026
	U1	1	DOCSIS 3.1 upstream amplifier (56L TQFN) MAXIM MAX3522BCTN+
	1	-	PCB: MAX3522B EVALUATION KIT#
	I	-	Mini-USB type A-to-type B cable

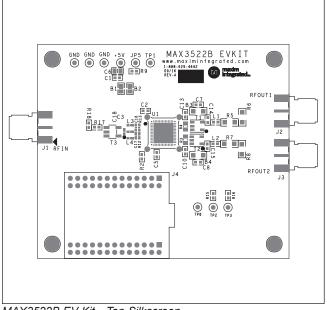
MAX3522B EV Kit Bill of Materials

Evaluates: MAX3522B

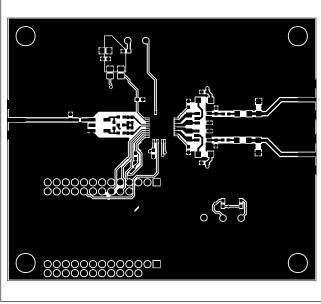
MAX3522B EV Kit Schematic



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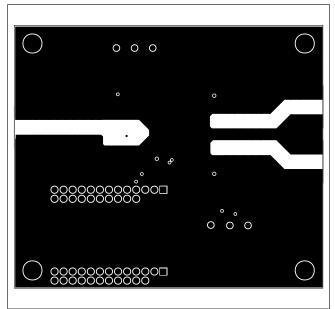


MAX3522B EV Kit PCB Layout Diagrams

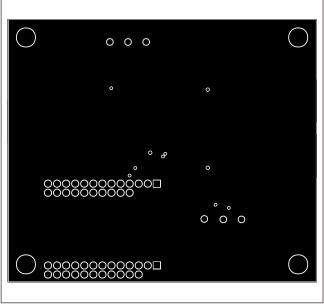


MAX3522B EV Kit—Top Silkscreen



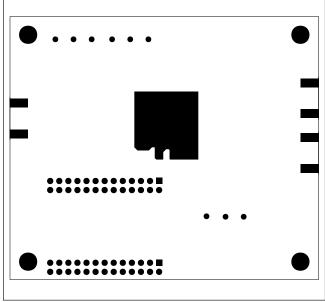


MAX3522B EV Kit—Internal 2

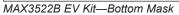


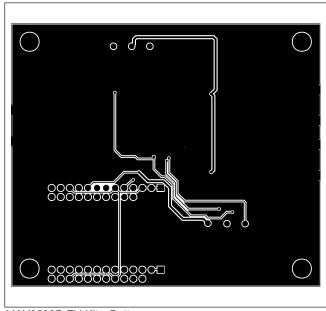
MAX3522B EV Kit—Internal 3

Evaluates: MAX3522B



MAX3522B EV Kit PCB Layout Diagrams





MAX3522B EV Kit—Bottom

Ordering Information

PART	TYPE	
MAX3522BEVKIT#	EV Kit	

#Denotes RoHS compliant.

Evaluates: MAX3522B

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	5/17	Initial release	_

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

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MAXESSENTIAL01+

Description

The Essential Analog toolkit contains a unique collection of Maxim's high-performance, analog building block products. This curated group of parts represent a selection of Maxim's vast product lines, specific to 20 product categories, from key performance areas including power efficiency, precise measurement, reliable connectivity, and robust protection.

The ICs in the toolkit offer the breadth of each product category: low power, low noise, multi-channel, high resolution, high accuracy, and high speed. All these features empower your designs and bring value to your systems.

At 6.4cm x 8.9cm x 1.3cm, the box itself is small, lightweight, and easy to carry. Products are guarded from ESD using a gel and ESD-protected box.

A guide that labels each of the part types inside the box supports the toolkit. Go to the Maxim website to find more information for the individual part numbers.

When planning your next design, pick up an Essential Analog toolkit to review Maxim's high-performance analog products.

Key Features

- Small, 6.4cm x 8.9cm x 1.3cm Package
- ESD Protection-Lined Package
- Accelerate Your Design with Quick Access



What's Included in the Box?

Efficient Power						
Buck	Boost	Buck-Boost	LDO	Continua		
MAX38640 MAX15026	MAX17225 MAX668	MAX77827 MAX77816	MAX38902B MAX1510	MAX38888		
Precision Measurement						
ADC	DAC	Audio Amp	Op Amp	CSA		
MAX11410 MAX11168	MAX5541 MAX5715	MAX98357A MAX98390	MAX40075 MAX40100	MAX44284 MAX40201		
Reliable Connectivity	Reliable Connectivity					
RS-485	RS-232	CAN	BTR Switch	Wireless		
MAX14780E MAX3485AE	MAX13235E MAX33250E	MAX13054A MAX33054E	MAX14778 MAX14763	MAX41460 MAX7034		
Robust Protection						
Supervisor	Temp Sensor	Isolator	Ideal Diode/V _{REF}	RTC		
MAX16150 MAX16140	MAX6680 MAX31875	MAX12930 MAX22445	MAX40203 MAX6078A	MAX31341B DS3231MZ		