



INCL CONTROLLER SSD1309 FOR SPI AND I<sup>2</sup>C



#### FEATURES

- 128X64 DOTS (RELATES TO 8x21 CHARACTER OR 4x16 LARGE CHARACTER)
- HIGH CONTRAST OLED DISPLAY
- INTEGRATED CONTROLLER SSD1309
- SPI INTERFACE: MOSI, CLK, CS, D/C
- I<sup>2</sup>C INTERFACE: SDA, SCL
- WIDE TEMPERATURE RANGE (T<sub>OP</sub> -40°C +80°C)
- NO MOUNTING REQUIRED: JUST PUT INTO PCB
- 3 VERSIONS (WITH / W.O. POLARISOR AND PROTECTION GLASS) IN VARIOUS COLORS)
- FAST RESPONSE TIME, NO AFTERGLOW

#### **ORDERING CODES**

- GRAPHIC 128x64, yellow, black background, incl. protection glass
- GRAPHIC 128x64, yellow, black background, w./o. protection glass
- GRAPHIC 128x64, white, black background, w./o. protection glass
- GRAPHIC 128x64, yellow, silver background, w./o. protection glass

WITH A MINIMUM ORDER QTY. OF 10,000 PCS.

• GRAPHIC 128x64, with Polarisor (Standard)

X: G = Yellow

B = Blue

#### ACCESSORIES

- TEST BOARD WITH USB-INTERFACE
- SOCKET 4.8MM HOCH (2 PCS. ARE REQUIRED)
- TOUCHPANEL, 4-WIRE ANALOGUE SELF-ADHESIVE
- ZIFF CONNECTOR FOR TOUCH, BOTTOM CONTACT <sup>1</sup> NUR IN KOMBINATION MIT EA OLEDL128-6GGA.

Fon: +49 (0)8105-7780 90 Fax: +49 (0)8105-7780 99 e-Mail: info@lcd-module.de Web: www.lcd-module.com EA 9781-1USB EA FL-20P EA TOUCH128-2<sup>\*)</sup> EA WF100-04S

EA OLEDL128-6GGA

EA OLEDL128-6LGA

EA OLEDL128-6LWA

EA OLEDL128-6NGA

EA OLEDL128-6LXA





#### **EA OLED SERIES**

With its EA OLED series ELECTRONIC ASSEMBLY launches worldwide the first display family with OLEDtechnology for direct mounting and soldering. In comparison to standard displays there's no FFC/FPC cable/connector that may loose contact, this OLED series will be soldered directly or put into a standard 2.54 mm precision socket.

It is designed for compact handheld equipment and provides a lot real advantages:

- Extreme compact (68x51mm) with a large viewing area (64x37mm)
- Super flat with 2.4 mm (without frontal protection glass)
- SPI and I<sup>2</sup>C interface
- Simple mounting with direct soldering
- Ex stock available from 1 pc. off
- Long life time (80,000 h for yellow)
- Extreme wide temperature range (-40..+80°C)
- Fast response time (10µs), no afterglow

#### VERSIONS

The EA OLEDL128-6 is available in 3 different versions:

#### EA OLEDL128-6GGA / Allround

This module is perfect for rough environment. An additional frontal glass protects the display against scratch, shock and UV light. Thanks to its integrated polariser there's no need for an additionally smoked glass.

#### EA OLEDL128-6LGA / Flat

This module is the standard module and does fit for the most applications. The flat design (2.4 mm) makes the display perfect for smallest equipment. The background is always deep black for best contrast.

#### EA OLEDL128-6NGA / Design solution

This version is to become favourite for assembly behind a smoked glass. There's no protection glass or polariser which makes it with 2.4mm very flat. The advantage is a doubled

brightness which makes it perfect in combination with the smoked glass. We recommend to put it behind a fully smoked glass front to make only the graphic visible, no edges, no print, no overhang and hygienic to clean.



#### **COLORS (CUSTOM MADE)**

The standard colors are yellow and white.

The flat version EA OLEDL128-6LGA is on customers request available in 3 more colors. The minimum order quantity is 10,000 pcs., lead time is about 20 weeks. Samples are available on request.

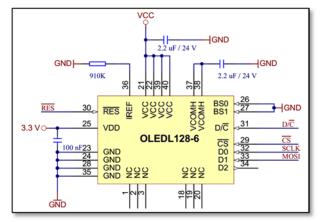
Interface and software are 100% compatible. The yellow color provides highest brightness and longest life time.



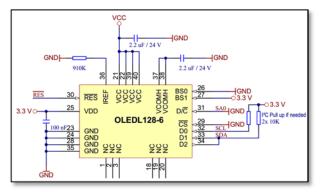




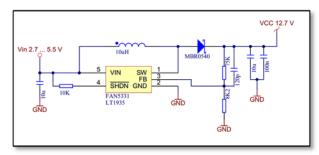
### **APPLICATION EXAMPLES**



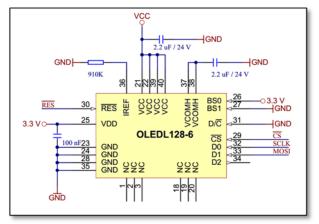
4-Wire SPI



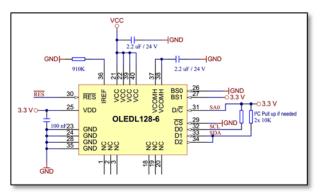
I<sup>2</sup>C Address 0x78



VCC - Generation: 12 V (FAN5331, LT1935)



3-Wire SPI



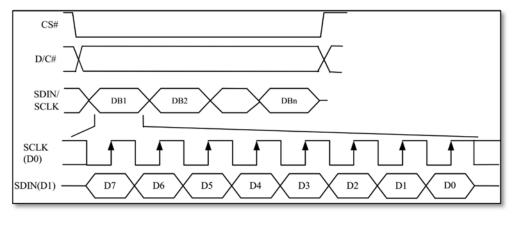
I<sup>2</sup>C Address 0x7A





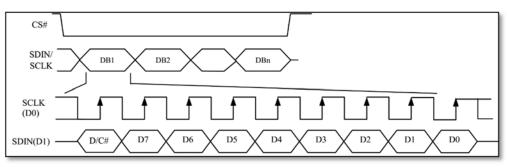
#### DATA TRANSFER 4-WIRE SPI (8 BIT)

Data transmission for SPI is unidirectional, that means that data can only be written, there's no data read option. Selection for writing data or command is done with the D/C line. A busy check is not necessary at all. Clock rate may be up to 10 MHz. Data transmission is based on SPI mode 3, MSB first. For more details please refer to the controllers data sheet SSD1309.



#### DATA TRANSFER 4-WIRE SPI (9 BIT)

Data transmission for SPI is unidirectional, that means that data can only be written, there's no data read option. Selection for writing data or command is done with the first bit of the 9 bit data transfer. A busy check is not necessary at all. Clock rate

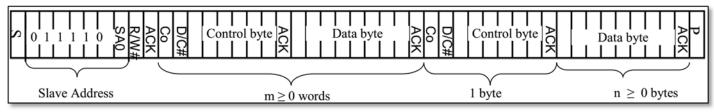


may be up to 10 MHz. Data transmission is based on SPI mode 3, MSB first (9 bit). For more details please refer to the controllers data sheet SSD1309.

#### DATA TRANSFER I<sup>2</sup>C

The I<sup>2</sup>C mode provides a bi-directional data transmission: That means that data can be written and read. With the pin SA0 the I<sup>2</sup>C address can be changed, so up to 2 displays may be driven on 1 bus. The clock rate may be up to 400 KHz. Please make sure when defining the pull-up resistors that the internal resistance of the display is 600..1000  $\Omega$ . This affects the low level when reading data and ACK bit.

**Attention:** When reading data, after the command for page- or column address there need to be a dummy read (discard the first byte).



Control byte: C<sub>o</sub> (Continuation bit) = 0  $\rightarrow$  Display data do follow; 1  $\rightarrow$  refer D/C bit t





#### **GRAPHIC RAM**

The EA OLEDL128-6 comes with an integrated display RAM. Each byte represents 8 dots. For more details please refer to the controllers data sheet SSD1309, available on our website at

http://www.lcd-module.de/fileadmin/eng/pdf/zubehoer/ssd1309.pdf.

# COMMAND TABLE (ABSTRACT)

This is a collection of the most important commands. The data sheet SSD1309 provides the full list plus a detailed description.

0	Column address 127
D0 1 D7	Page 0
D0 1 D7	Page 1
D0 <i>l</i> D7	Page 2
D0 <i>l</i> D7	Page 3
D0 / D7	Page 4
D0 / D7	Page 5
D0 1 D7	Page 6
D0 1 D7	Page 7
	0

Command	D/ C	Command Code							Description		
		Hex	D7	D6	D5	D4	D3	D2	D1	D0	Description
Contrast	0	81	1	0	0	0	0	0	0	1	Double byte command to select 1
Control	0	7F	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	<b>A</b> <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	<b>A</b> 1	A <sub>0</sub>	out of 256 contrast steps. Contrast increases as the value increases.
Display	0	AE/	1	0	1	0	1	1	1	X <sub>0</sub>	X <sub>0</sub> =0: Display OFF (sleep mode)
On / Off		AF									(RESET)
Set	0	21	0	0	1	0	0	0	0	1	X <sub>0</sub> =1: Display ON in normal mode Setup column start and end
Column	0	0	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	address
address	0	7F	B <sub>7</sub>	B <sub>6</sub>	B <sub>5</sub>	B <sub>4</sub>	B <sub>3</sub>	B <sub>2</sub>	B <sub>1</sub>	B <sub>0</sub>	A[7:0] : Column start address,
											range : 0-127d, (RESET = 0) B[7:0]: Column end address, range
											: 0-127d, RESET = 127)
											Note: This command is only for
											horizontal or vertical addressing mode.
Set Page	0	22	0	0	1	0	0	0	1	0	Setup page start and end address
address	0	0	X	X	Х	Х	X	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	A[2:0] : Page start Address, range : 0-7d, (RESET = 0)
	0	7	Х	Х	Х	Х	Х	B <sub>2</sub>	B <sub>1</sub>	B <sub>0</sub>	B[2:0] : Page end Address, range :
											0-7d, (RESET = 7)
											Note: This command is only for horizontal or vertical addressing
											mode.
Display	0	40	0	1	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	Set display RAM display start line
Start Line		– 7F									register from 0-63 using $X_5X_3X_2X_1X_0$ .
		/ Г									Display start line register is reset to
0	0	4.07		0			0	0	0	V	0 during RESET.
Segment remap	0	A0/ A1	1	0	1	0	0	0	0	X <sub>0</sub>	X <sub>0</sub> =0: column address 0 is mapped to SEG0 (RESET)
Теппар											$X_0=1$ : column address 127 is
		00/				<b>_</b>	X		_		mapped to SEG0
Com output	0	C0/ C8	1	1	0	0	<b>X</b> <sub>3</sub>	0	0	0	X <sub>3</sub> =0: normal mode (RESET) Scan from COM0 to COM[N −1]
scan		00									X <sub>3</sub> =1: remapped mode. Scan from
direction											COM[N-1] to COM0
RAM Data	1	XX	D <sub>7</sub>	D <sub>6</sub>	D <sub>5</sub>	D <sub>4</sub>	D <sub>3</sub>	D <sub>2</sub>	D <sub>1</sub>	D <sub>0</sub>	Where N is the Multiplex ratio D <sub>7</sub> -D <sub>0</sub> is written to RAM.
		$\Lambda\Lambda$	<b>D</b> 7	$D_6$	$D_5$	<b>D</b> 4	D3	$D_2$		$\mathbf{D}_0$	





#### INITIALISATION EXAMPLE

```
//Set Display start line
//Bottom View no Segment remap
//Bottom View COM scan direction normal
//Display normal (RAM)
//Set contrast to maximum
//Clock divider/Oscillator frequency
//Pre-charge Period
//Display on
```

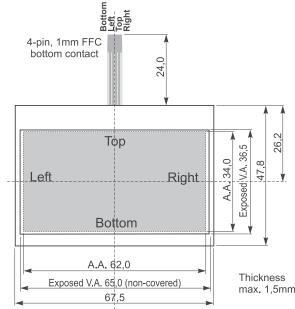
#### ACCESSORY: SOCKET EA FL-20P

Using a 20-pin socket makes the display replaceable and adapts the height. Those socket may also be soldered automatically by wave soldering or reflow process. Each display requires 2 pcs.

#### 25.4 2.54 2.54 2.54 2.54 2.54 2.54 2.54 00.50 0.50

### ACCESSORY: TOUCHPANEL EA TOUCH128-2

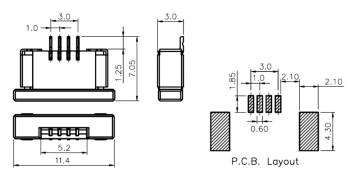
As an accessory there is an analogue touchpanel available. It comes with a self-adhesive glue on its rear side. Connection is done via FFC, pitch 1.0 mm. Any standard ZIFF connector can be used (e.g. EA WF100-04S). Bending radius is minimum 5 mm. Interfacing to a processor can be either done by an external touch panel controller or with a controller that is featured with analogue input. The touch panel is similar to a potentiometer: connecting a voltage of e.g. 3.3 V to the pins Top-Bottom makes it possible to read out a voltage on pin Left or Right which is linear to the Y-coordinate of the pressed point. The X-coordinate will result when the voltage will be supplied to Left-Right and measurement is done at Top or Bottom. The pinout of the cable the drawing. connecting is shown in Only in combination with the EA OLEDL128-6GGA.



#### ACCESSORY: ZIFF CONNECTOR EA WF100-04S

The ZIFF connector matches perfect to the touch panel EA TOUCH128-2. It provides 4 pins with 1.0 mm pitch. Connection is bottom contact.

Top contact version is called EA WF100-04T.







### **ELECTRICAL SPECIFICATION**

Unless otherwise specified, VSS = 0V , VDD = 1.8 - 3.3V (  $Ta = 25 \,$  C)

Value	Condition	min	typ	max	Unit
Operating Temperature		-40		+80	°C
Storage Temperature		-40		+80	°C
Storage Humidity	<40°C			90	%RH
Operating Voltage VDD logic supply		1.8	3.0	3.3	V
Operating Voltage VCC OLED supply			12.5	13.0	V
High Logic input level		0.8 x VDD			
Low Logic input level				0.2 x VDD	V
	All Pixel off		0,6		mA
Power Supply VCC <sup>1)</sup>	Demo picture <sup>2)</sup>		15		mA
	All Pixel on		36		mA

<sup>1)</sup> VCC= 12V, initialization 0x81,0xFF / 0xD5,0x40 / 0xD9,0x44 / 0xDB,0x34

<sup>2)</sup> Demo Picture:



#### **OPTICAL DATA**

Item	Symbol	Condition	min	typ	max	Unit
View Angle	(V)θ	CR≧2000	160			deg
	(H)φ	CR≧2000	160			deg
Contrast Ratio	CR	Dark Room	2000:1			
Response Time	T rise			10		μs
	T fall			10		μs
Luminance	L	All pixel on with polarizer	80	100		cd/m²
CIE 1931 x(Yellow)		Dark Room	0.45	0.47	0.49	
CIE 1931 y(Yellow)		Dark Room	0.48	0.50	0.52	





#### **DIMENSIONS EA OLEDL128-6**

