	1 A A A A A A A A A A A A A A A A A A A
ΡΛΝ	JIT
	SEMI
	CONDUCTOR





Current

1.9A

### Features

Voltage

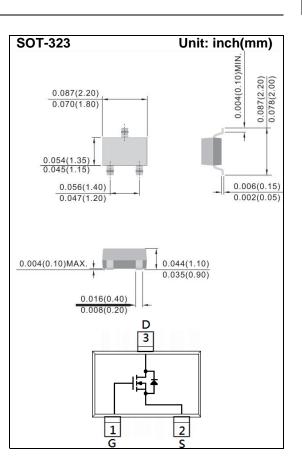
• RDS(ON) , VGS@10V, ID@1.9A<70mΩ

30 V

- RDS(ON), VGS@4.5V, ID@1.6A<75mΩ
- Rds(ON) , Vgs@2.5V, Id@1.2A<85mΩ
- Rds(on) , Vgs@1.8V, Id@0.7A<110m $\Omega$
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- Lead free in comply with EU RoHS 2011/65/EU directives.
- Green molding compound as per IEC61249 Std. (Halogen Free)

## **Mechanical Data**

- Case: SOT-323 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.00018 ounces, 0.005 grams
- Marking: C00



## **Maximum Ratings and Thermal Characteristics** ( $T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	30	V
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 12	V
Continuous Drain Current		I <sub>D</sub>	1.9	А
Pulsed Drain Current		I <sub>DM</sub>	7.6	А
Power Dissipation	T <sub>a</sub> =25°C	P <sub>D</sub>	350	mW
	Derate above 25°C		2.8	mW/°C
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~150	°C
Typical Thermal resistance - Junction to Ambient <sup>(Note 3)</sup>		R <sub>eja</sub>	357	°C/W



PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static		·	·			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250$ uA	0.4	0.72	1.2	V
Drain-Source On-State Resistance		V <sub>GS</sub> =10V, I <sub>D</sub> =1.9A	-	58	70	- mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =1.6A	-	61	75	
	R <sub>DS(on)</sub>	V <sub>GS</sub> =2.5V, I <sub>D</sub> =1.2A	-	69	85	
		V <sub>GS</sub> =1.8V, I <sub>D</sub> =0.7A	-	80	110	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	-	0.01	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 12V, V <sub>DS</sub> =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Qg	$V_{DS}$ =15V, I <sub>D</sub> =1.9A, $V_{GS}$ =10V <sup>(Note 1,2)</sup>	-	4.8	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	0.5	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	0.7	-	
Input Capacitance	Ciss	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V,	-	447	-	pF
Output Capacitance	Coss		-	34	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	22	-	
Switching						
Turn-On Delay Time	td <sub>(on)</sub>		-	2	-	
Turn-On Rise Time	tr	$V_{DD}=15V, I_{D}=1.9A,$		38	-	ns
Turn-Off Delay Time	td <sub>(off)</sub>	$V_{GS}=10V$ ,		812	-	
Turn-Off Fall Time	tf	$R_G=6\Omega^{(Note 1,2)}$	-	64	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	Is		_	_	0.5	А
Diode Forward Current	'S		_	_	0.0	
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =1.0A, V <sub>GS</sub> =0V		0.77	1.2	V

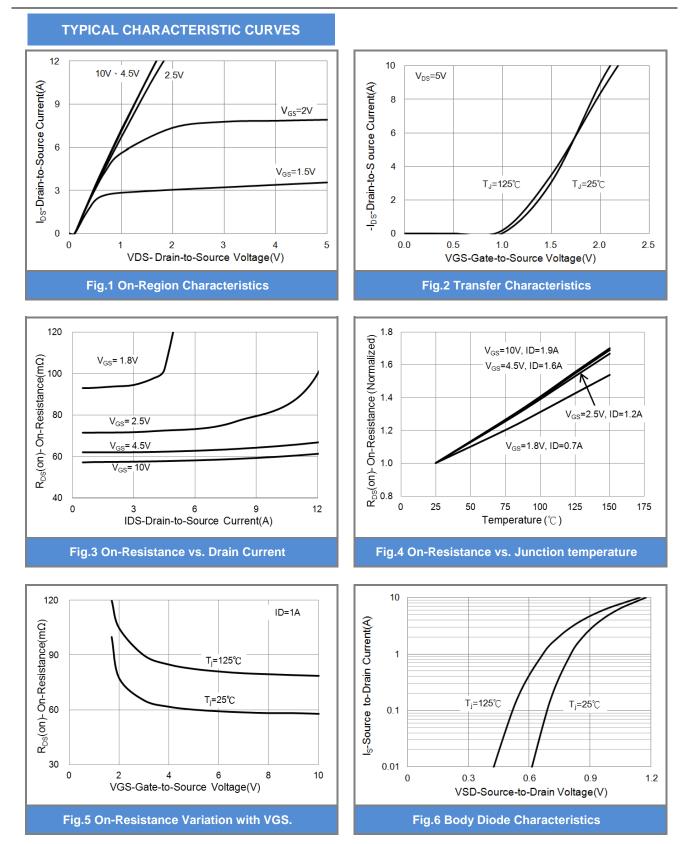
NOTES :

1. Pulse width</br>

2. Essentially independent of operating temperature typical characteristics.

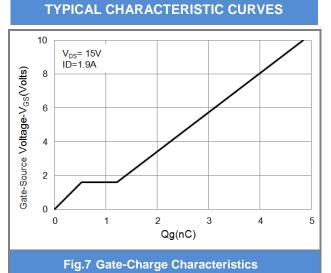
- 3. ReJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





4





# $\begin{bmatrix} 1.2 \\ 1.0 \\ 0.8 \\ 0.6 \\ 0.4 \\ 0.2 \\ 0.2 \\ 0 \\ 25 \\ 50 \\ 75 \\ 100 \\ 125 \\ 150 \\ 175 \\ Temperature (°C) \\ \end{bmatrix}$

Fig.8 Threshold Voltage Variation with Temperature.

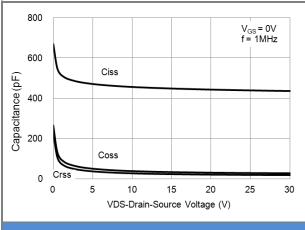


Fig.9 Capacitance vs. Drain-Source Voltage.

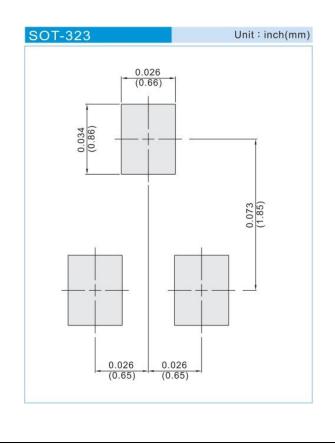




## PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJC7400_R1_00001	SOT-323	3K pcs / 7" reel	C00	Halogen free
PJC7400_R2_00001	SOT-323	12K pcs / 13" reel	C00	Halogen free

## **MOUNTING PAD LAYOUT**







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