

Vishay Siliconix

N-Channel 30-V (D-S) MOSFET

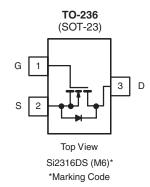
PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ)		
30	0.050 at V _{GS} = 10 V	4.5	3.16 nC		
30	0.080 at V _{GS} = 4.5 V	3.4	0.10110		

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- PWM Optimized
- 100 % R_g tested
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Battery Switch
- DC/DC Converter



Ordering Information: Si2316BDS-T1-E3 (Lead (Pb)-free) Si2316BDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T_A	_= 25 °C, unless oth	nerwise noted		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	30	v	
Gate-Source Voltage		V _{GS}	± 20	v
	T _C = 25 °C		4.5	
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 70 °C		3.6	
Continuous Drain Current $(T_j = 150 \text{ C})$	T _A = 25 °C	I _D	3.9 ^{b, c}	
	T _A = 70 °C		3.13 ^{b, c}	А
Pulsed Drain Current		I _{DM}	20	
Continuous Courses Drain Diada Current	T _C = 25 °C	1	1.39	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	1.04 ^{b, c}	
	T _C = 25 °C		1.66	
Maximum Dawar Disaination	T _C = 70 °C	Р	1.06	w
Maximum Power Dissipation	T _A = 25 °C	P _D	1.25 ^{b, c}	••
	T _A = 70 °C		0.8 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{sta}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	≤ 5 s	R _{thJA}	80	100	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	60	75	0/11

Notes: a. Based on $T_C = 25$ °C.

b. Surface mounted on 1" x 1" FR4 moard.

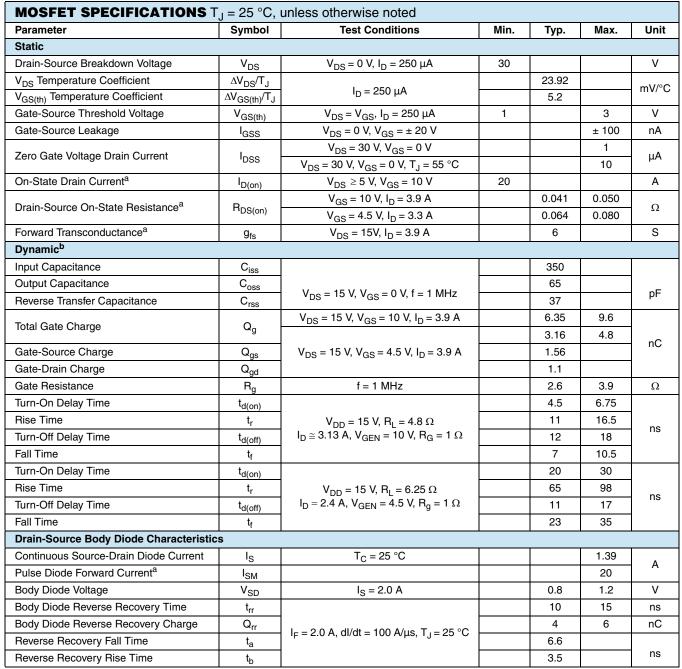
c. t = 5 s.

d. Maximum under Steady State conditions is 130 °C/W.



Si2316BDS

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Notes:

a. Pulse test: pulse width \leq 300 µs. duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

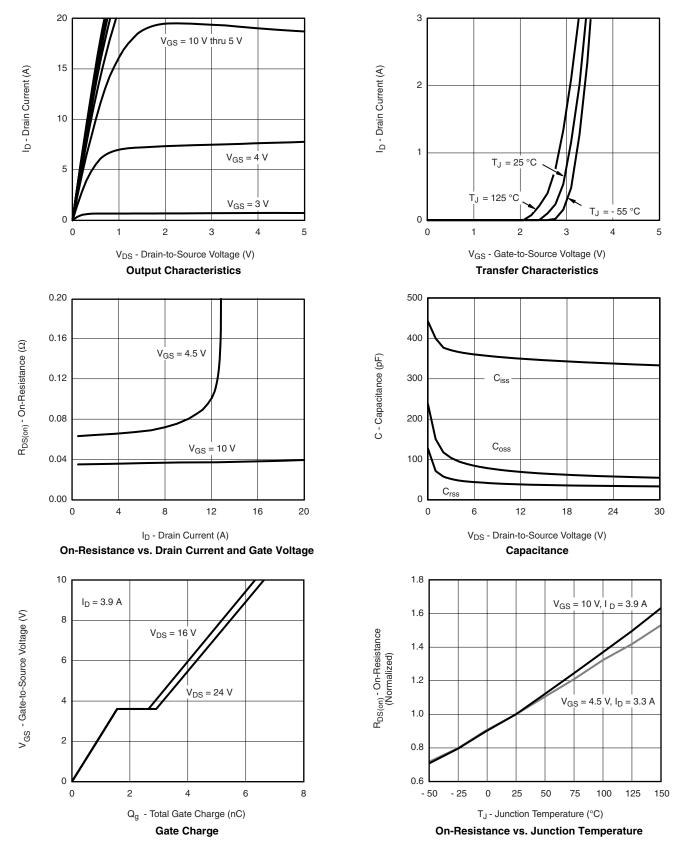




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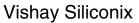
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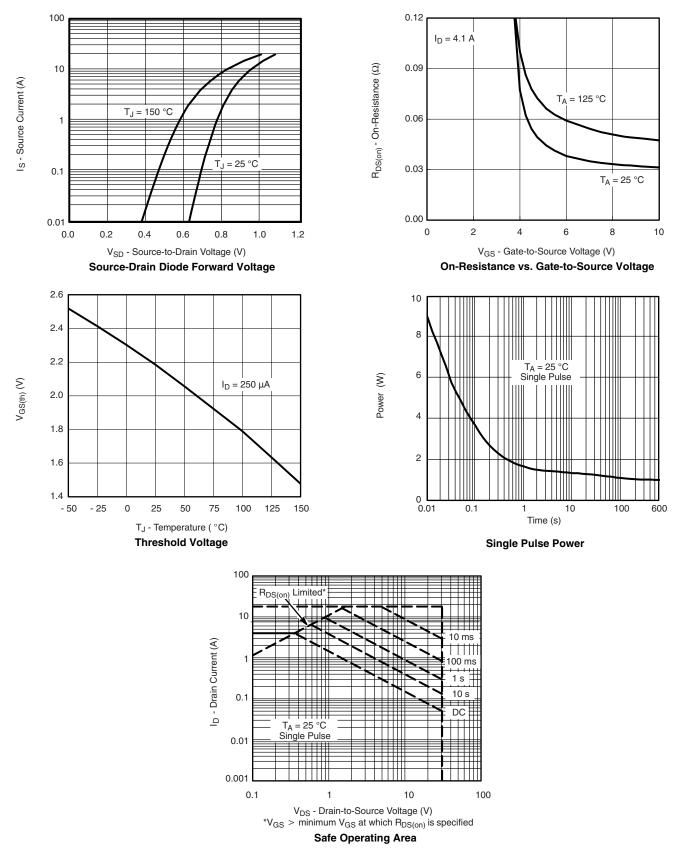
Document Number: 70445 S09-1503-Rev. B, 10-Aug-09

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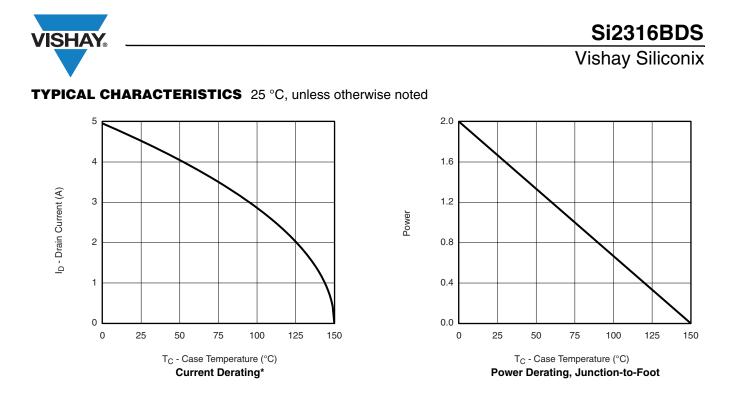




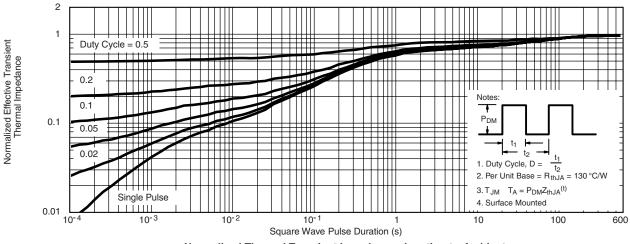
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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*The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg270445.

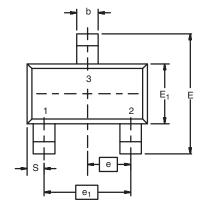
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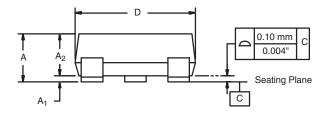


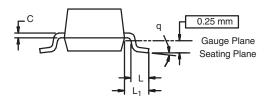
Package Information

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SOT-23 (TO-236): 3-LEAD







Dim	MILLIMETERS		INCHES		
	Min	Мах	Min	Мах	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
ECN: S-03946-Rev. K, 09- DWG: 5479	Jul-01				



Application Note 826

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RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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