**Product data sheet** 

## 1. General description

NPN/PNP general-purpose double transistors in an SOT457 (SC-74) plastic package.

### 2. Features and benefits

- · Reduces component count
- · Reduces pick and place costs
- AEC-Q101 qualified

## 3. Applications

· General purpose switching and amplification

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit		
Per transistor; for the PNP transistor with negative polarity									
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	45	V		
Ic	collector current			-	-	500	mA		
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-	1	Α		
Per transistor unless otherwise specified; for the PNP transistor with negative polarity									
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA	[1]	160	-	400			

<sup>[1]</sup> Pulsed test:  $t_p \le 300 \ \mu s; \ \delta \le 0.02$ 

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	□6 □5 □4	C1 B2 E2
2	B1	base TR1		
3	C2	collector TR2	0 □1 □2 □3	(TR1) TR2)
4	E2	emitter TR2	SC-74; TSOP6 (SOT457)	
5	B2	base TR2	, ( ,	E1 B1 C2
6	C1	collector TR1	]	sym019



#### NPN/PNP general purpose transistor

## 6. Ordering information

#### **Table 3. Ordering information**

Type number			
	Name	Description	Version
BC817DPN	SC-74; TSOP6	plastic, surface-mounted package (SC-74; TSOP6); 6 leads	SOT457

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code
BC817DPN	N4

## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or; for the PNP transistor wit	h negative polarity				
V <sub>CBO</sub>	collector-base voltage	open emitter		-	50	V
$V_{CEO}$	collector-emitter voltage	open base		-	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	1	Α
I <sub>BM</sub>	peak base current			-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	370	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Per device	1		1			
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	600	mW

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB); single-sided copper; tin plated; mounting pad for collector 1 cm<sup>2</sup>.

### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per device							
· ·ui(j-a)	thermal resistance from junction to ambient	in free air	[1]	-	-	208	K/W

<sup>[1]</sup> Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.

#### NPN/PNP general purpose transistor

### 10. Characteristics

#### **Table 7. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	tor unless otherwise spec	ified; for the PNP transistor with negativ	e polar	ity			
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 20 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
	current	V <sub>CB</sub> = 20 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	5	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA	[1]	160	-	400	
		V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C	[1]	40	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 500 \text{ mA}; I_B = 50 \text{ mA}; T_{amb} = 25 \text{ °C}$	[1]	-	-	700	mV
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA	[1] [2]	-	-	1.2	V
NPN transis	stor					'	
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$		-	5	-	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C		100	-	-	MHz
PNP transis	stor		•	•	'		
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C		-	9	-	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = -5 V; $I_{C}$ = -10 mA; f = 100 MHz; $T_{amb}$ = 25 °C		80	-	-	MHz

- [1] Pulsed test:  $t_p \le 300 \,\mu s$ ;  $\delta \le 0.02$
- [2]  $V_{BE}$  decreases by approximately -2 mV/k with increasing temperature.

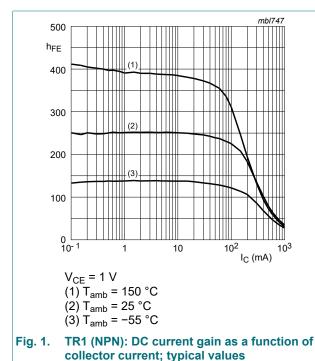
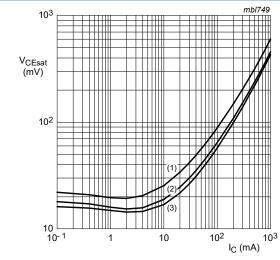


Fig. 2. TR1 (NPN): Collector current as a function of collector-emitter voltage; typical values

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#### NPN/PNP general purpose transistor

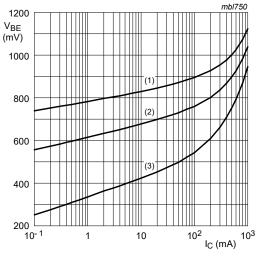


 $I_{\rm C}/I_{\rm B} = 10$ 

 $(1) T_{amb} = 150 ^{\circ} C$ 

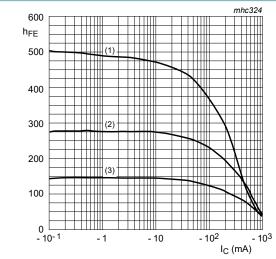
(2) T<sub>amb</sub> = 25 °C (3) T<sub>amb</sub> = -55 °C

Fig. 3. TR1 (NPN): Collector-emitter saturation voltage as a function of collector current; typical values



V<sub>CE</sub> = 1 V (1) T<sub>amb</sub> = -55 °C (2) T<sub>amb</sub> = 25 °C (3) T<sub>amb</sub> = 150 °C

Fig. 4. TR1 (NPN): Base-emitter voltage as a function of collector current; typical values



 $V_{CE}$  = -1 V

(1)  $T_{amb} = 150 \, ^{\circ}C$ 

(2)  $T_{amb} = 25 \, ^{\circ}C$ 

(3)  $T_{amb} = -55 \, ^{\circ}C$ 

Fig. 5. TR2 (PNP): DC current gain as a function of collector current; typical values

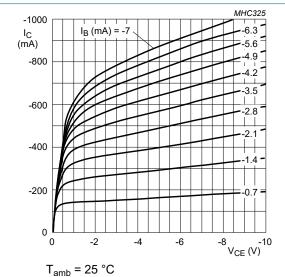


Fig. 6. TR2 (PNP): Collector current as a function of collector-emitter voltage; typical values

#### NPN/PNP general purpose transistor

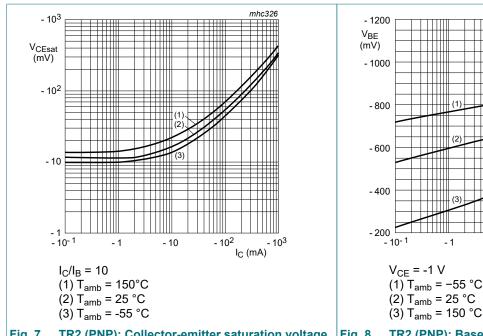


Fig. 7. TR2 (PNP): Collector-emitter saturation voltage as a function of collector current; typical values

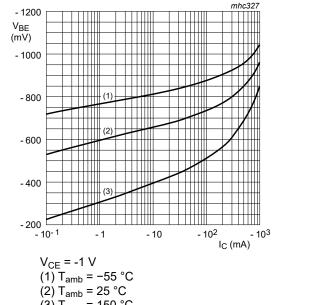


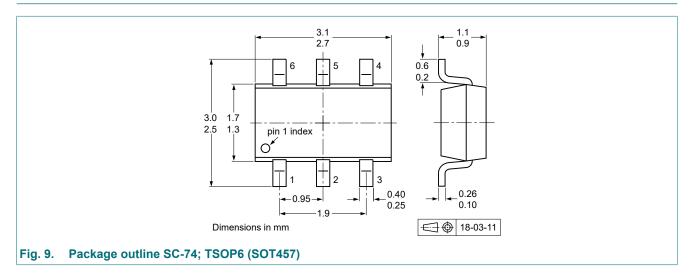
Fig. 8. TR2 (PNP): Base-emitter voltage as a function of collector current; typical values

### 11. Test information

### **Quality information**

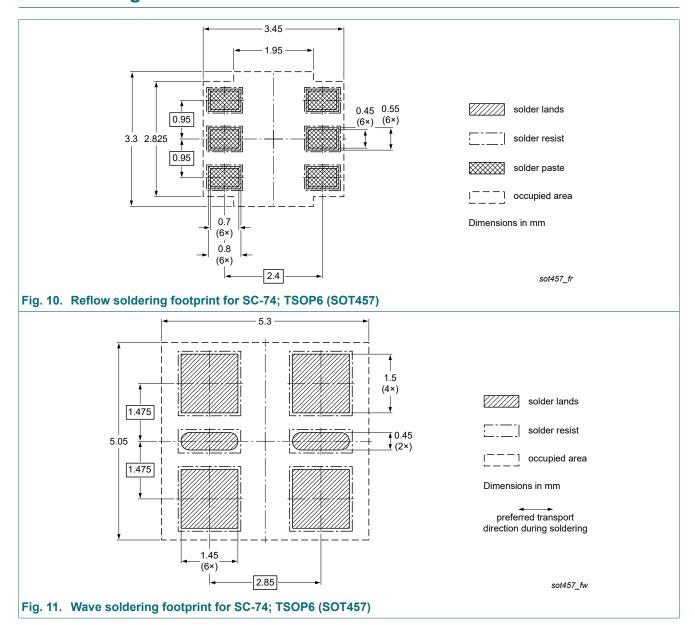
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

## 12. Package outline



### NPN/PNP general purpose transistor

## 13. Soldering



### NPN/PNP general purpose transistor

# 14. Revision history

#### **Table 8. Revision history**

Table of Revision metery								
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
BC817DPN v.3	20191127	Product data sheet	-	BC817DPN v.2				
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> </ul>							
BC817DPN v.2	20021122	Product data sheet	-	BC817DPN v.1				
BC817DPN v.1	20020809	Product data sheet	-	-				

### NPN/PNP general purpose transistor

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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#### NPN/PNP general purpose transistor

## **Contents**

1.	General description	. 1
2.	Features and benefits	. 1
3.	Applications	. 1
4.	Quick reference data	. 1
5.	Pinning information	.1
6.	Ordering information	. 2
7.	Marking	. 2
8.	Limiting values	2
9.	Thermal characteristics	2
10.	Characteristics	. 3
11.	Test information	. 5
12.	Package outline	. 5
	Soldering	
	Revision history	
	Legal information	

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