

DTD114GK

NPN 500mA 50V Digital Transistors (Bias Resistor Built-in Transistors)

Parameter	Value
V _{CEO}	50V
Ι _C	500mA
R	10kΩ

Features

- 1) Built-In Biasing Resistors
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Complementary PNP Types :DTB114GK
- 5) Lead Free/RoHS Compliant.

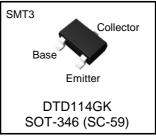
Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

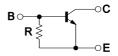
Packaging specifications

Part No.	Package	Package size (mm)	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit (pcs)	Marking
DTD114GK	SMT3	2928	T146	180	8	3,000	L24

Outline



Inner circuit



●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Values	Unit
Collector-base voltage	V _{CBO}	50	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	5	V
Collector current	Ι _C	500	mA
Power dissipation	P _d ^{*2}	200	mW
Junction temperature	Τ _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

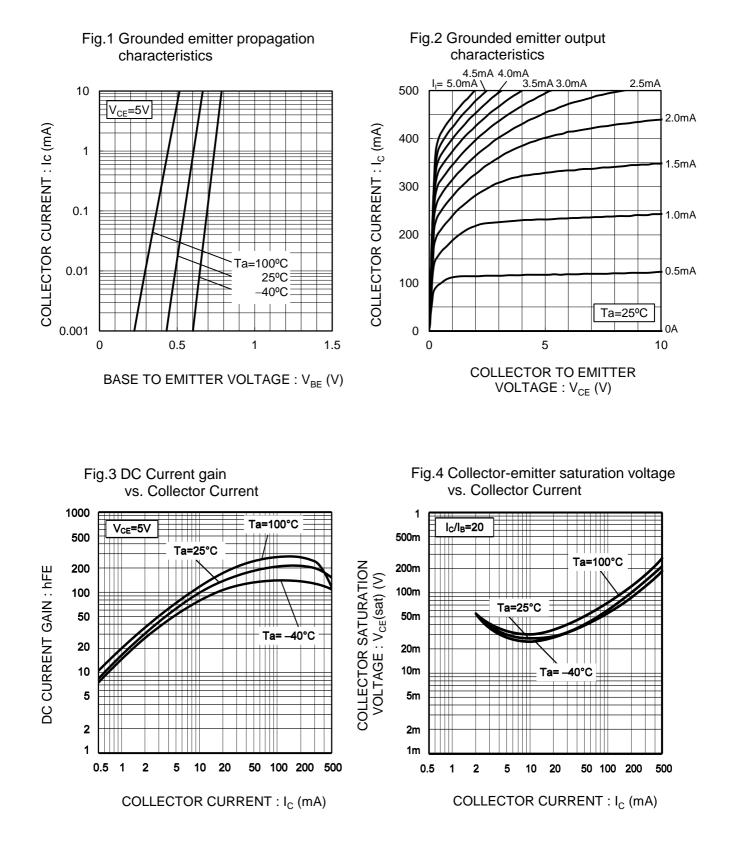
•Electrical characteristics(Ta = 25°C)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Collector-base breakdown voltage	BV_{CBO}	I _C = 50μA	50	-	-	V
Collector-emitter breakdown voltage	BV _{CEO}	I _C = 1mA	50	-	-	V
Emitter-base breakdown voltage	BV_{EBO}	I _E = 720μA	5	-	-	V
Collector cut-off current	I _{CBO}	V _{CB} = 50V	-	-	0.5	μA
Emitter cut-off current	I _{EBO}	$V_{EB} = 4V$	300	-	580	μA
Collector-emitter saturation voltage	V _{CE(sat)}	I _C / I _B = 50mA / 2.5mA	-	-	0.3	V
DC current gain	h _{FE}	V_{CE} = 5V , I _C = 50mA	56	-	-	-
Emitter-base resistance	R	-	7	10	13	kΩ
Transition frequency	f _T *1	V _{CE} = 10V, I _E = −5mA, f = 100MHz	-	200	-	MHz

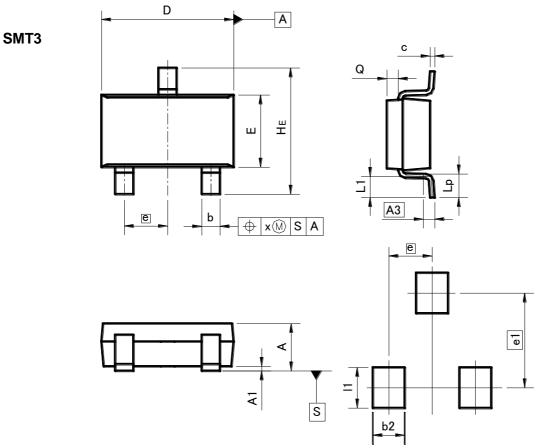
*1 Characteristics of built-in transistor

*2 Each terminal mounted on a reference footprint

•Electrical characteristic curves(Ta = 25°C)



•Dimensions (Unit : mm)



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
A	1.00	1.30	0.039	0.051	
A1	0.00	0.10	0.000	0.004	
A3	0.2	25	0.0	10	
b	0.35	0.50	0.014	0.020	
с	0.09	0.25	0.004	0.010	
D	2.80	3.00	0.110	0.118	
E	1.50	1.80	0.059	0.071	
е	0.9	95	0.037		
He	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.20	0.30	0.008	0.012	
x	_	0.10	_	0.004	
У	_	0.10	_	0.004	

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	-	0.60	-	0.024	
e1	2.10		0.0	83	
1	—	0.90	-	0.035	

Dimension in mm / inches

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dtd114gk - Web Page

Distribution Inventory

Part Number	dtd114gk
Package	SMT3
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes