



#### SURFACE-MOUNT SWITCHING DIODE ARRAY

### **Features**

- Fast Switching Speed
- High Reverse Breakdown Voltage
- Low Leakage Current
- Low Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The BAS16HTWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

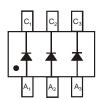
## **Mechanical Data**

- Package: SOT363
- Package Material: Molded Plastic, "Green" Molding Compound;
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe;
   (Lead-Free Plating). Solderable per MIL-STD-202, Method 208@3
- Polarity: See Diagram
- Weight: 0.006 grams (Approximate)

**SOT363** 



Top View



Top View Internal Schematic

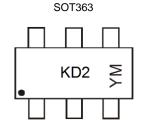
### Ordering Information (Notes 4 & 5)

Part Number	Package	Packing		
Fait Nullibei	Package	Qty.	Carrier	
BAS16HTWQ-13	SOT363	10,000	Tape & Reel	
BAS16HTWQ-13R	SOT363	10,000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.
- 5. The "-13R" suffix indicates that the devices are rotated 180° in the carrier tape as compared with the standard "-13" suffix devices.

### **Marking Information**



KD2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: K = 2023); A Bar on Top of the "Y" Denotes AT Site M = Month (ex: 9 = September)

Date Code Key

Year	2015	-	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032
Code	С	-	K	L	М	N	Р	R	S	Т	U	V
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	100	٧	
RMS Reverse Voltage	VR(RMS)	71	V	
Forward Continuous Current (Note 6)	Іғм	200	mA	
Repetitive Peak Forward Current	IFRM	500	mA	
	@ t = 1.0µs		4	
Non-Repetitive Peak Forward Surge Current	@ t = 1.0ms	IFSM	1.0	Α
@ t = 1.0			0.5	

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P <sub>D</sub>	250	mW
Thermal Resistance Junction to Ambient Air (Note 6)	Reja	500	°C/W
Thermal Resistance Junction to Solder Point (Note 7)	Rejsp	260	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

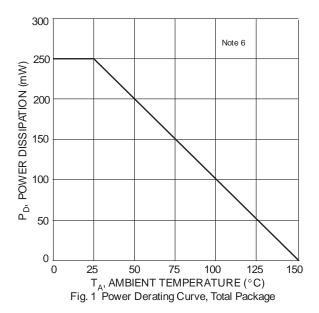
## **Electrical Characteristics** (@TA = +25°C, unless otherwise specified.)

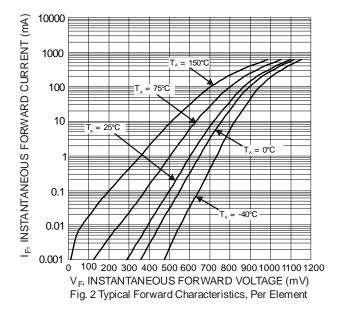
Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	V(BR)R	100	_	V	I <sub>R</sub> = 2.5µA
		_	0.715	V	$I_F = 1.0 \text{mA}$
Forward Voltage	VF	_	0.855		I <sub>F</sub> = 10mA
Forward voilage	VF	_	1.0		IF = 50mA
		_	1.25		I <sub>F</sub> = 150mA
		_	0.5	μΑ	V <sub>R</sub> = 80V
Reverse Current (Note 8)	l .	_	50		V <sub>R</sub> = 80V, T <sub>J</sub> = +150°C
Reverse Current (Note 8)	IR	_	30		V <sub>R</sub> = 25V, T <sub>J</sub> = +150°C
		_	30	nA	V <sub>R</sub> = 25V
Total Capacitance	Ст	_	1.5	pF	$V_R = 0$ , $f = 1.0MHz$
Reverse Recovery Time	top		4.0	ns	$I_F = I_R = 10 \text{mA}$
Theveloe Necovery Time	trr		4.0	115	$I_{RR} = 0.1 \times I_{R}, R_{L} = 100\Omega$
Forward Recovery Voltage	VfR	_	1.75	V	$I_F = 10 \text{mA}, t_R = 20 \text{ns}$

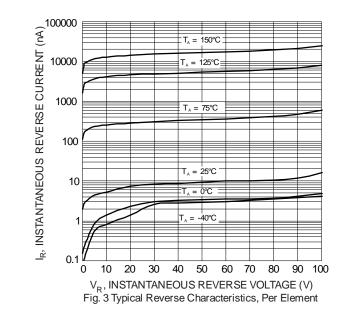
6. Part mounted on FR-4 PC board with recommended pad layout, please see http://www.diodes.com/package-outlines.html for the latest version. Notes:

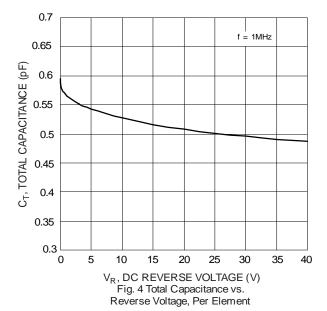
<sup>7.</sup> Soldering points at pins  $C_1$ ,  $C_2$  and  $C_3$ . 8. Short duration pulse test used to minimize self-heating effect.









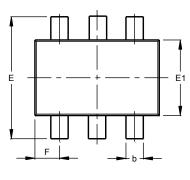


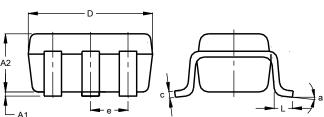


## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### **SOT363**



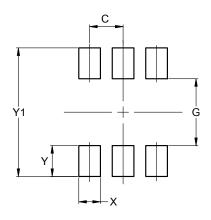


SOT363					
Dim	Min	Max	Тур		
<b>A</b> 1	0.00	0.10	0.05		
A2	0.90	1.00	0.95		
b	0.10	0.30	0.25		
C	0.10	0.22	0.11		
D	1.80	2.20	2.15		
Е	2.00	2.20	2.10		
E1	1.15	1.35	1.30		
e	0.650 BSC				
F	0.40	0.45	0.425		
L	0.25	0.40	0.30		
а	0°	8°			
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### **SOT363**



Dimensions	Value		
פווטופוושווט	(in mm)		
С	0.650		
G	1.300		
Х	0.420		
Y	0.600		
Y1	2.500		



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