

### PROTECTION PRODUCTS

#### Description

RClamp® TVS diodes are designed to protect sensitive electronics from damage or latch-up due to transient surge events. This device offers desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

RClamp®0504FB features extremely good ESD and EOS protection characteristics highlighted by low dynamic resistance and high surge capability. Data (I/O) lines are protected from EOS surge events as high as 14A ( $t_p = 8/20\mu s$ ). Power buses may be subjected to even harsher EOS events. The TVS connection at pin 5 allows protection of VBus lines to 28A ( $t_p = 8/20\mu s$ ).

RClamp0504FB is in a 6-pin, SC-70 6L package. The leads are finished with lead-free matte tin. The combination of small size, low capacitance, and high ESD and EOS surge capability makes them ideal for use in applications such as USB 2.0, multimedia cards, and video interfaces.

#### Features

- High ESD withstand Voltage: +/-24kV (Contact) & +/-30kV (Air) per IEC 61000-4-2
- High EOS Surge capability
- Low ESD clamping voltage
- Working voltage: 5V
- Low capacitance: 1.1 pF Typical (I/O to GND)
- Low dynamic resistance:  $0.25\Omega$
- Solid-state silicon-avalanche technology

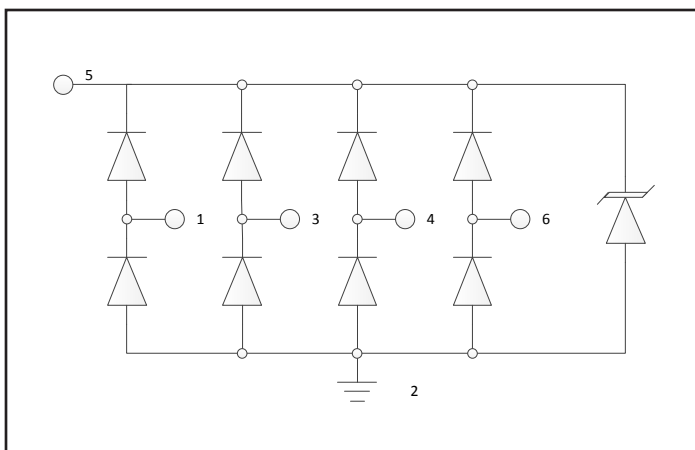
#### Mechanical Characteristics

- SC-70 6L package
- Pb-Free, Halogen Free, RoHS/WEEE compliant
- Nominal Dimensions: 2.1 x 2.0 x 1.0 mm
- Lead Finish: Matte Tin
- Marking: Marking code
- Packaging: Tape and Reel

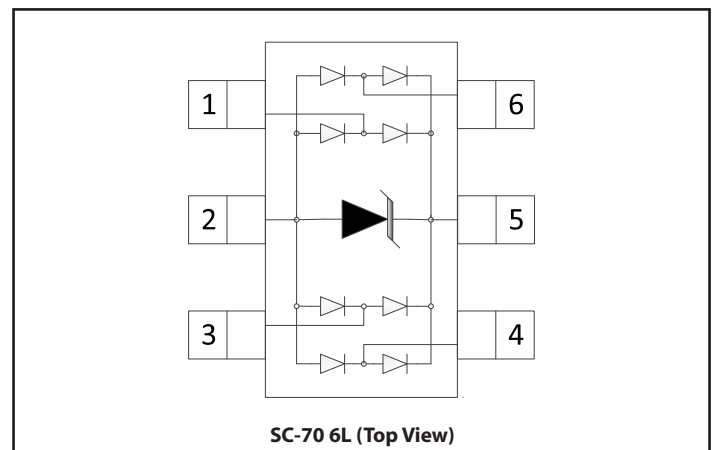
#### Applications

- Monitors and Flat Panel Displays
- Analog Video Lines
- LVDS Lines
- USB 2.0
- SIM Ports

#### Circuit Diagram



#### Schematic & Pin Configuration



## Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (I/O Pins, $t_p = 8/20\mu s$ )	$P_{PK}$	230	W
Peak Pulse Power (VBus Pin, $t_p = 8/20\mu s$ )	$P_{PK}$	500	W
Peak Pulse Current (I/O Pins, $t_p = 8/20\mu s$ )	$I_{PP}$	14	A
Peak Pulse Current (VBus Pin, $t_p = 8/20\mu s$ )	$I_{PP}$	28	A
ESD per IEC 61000-4-2 (Air) <sup>(1)</sup> ESD per IEC 61000-4-2 (Contact) <sup>(1)</sup>	$V_{ESD}$	$\pm 30$ $\pm 24$	kV
Operating Temperature	$T_J$	-55 to +125	°C
Storage Temperature	$T_{STG}$	-55 to +150	°C

## Electrical Characteristics (T=25°C unless otherwise specified)

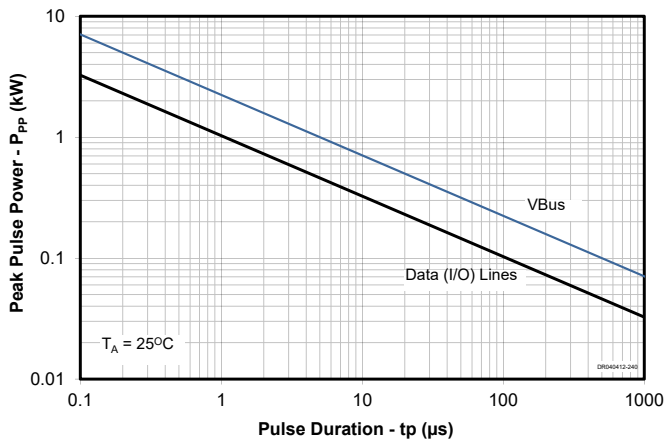
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Reverse Stand-Off Voltage	$V_{RWM}$	Any pin to Pin 2			5	V
Reverse Breakdown Voltage	$V_{BR}$	$I_{BR} = 1mA$ , any I/O Pin to Pin 2	6	8.5	10	V
Reverse Leakage Current	$I_R$	$V_{RWM} = 5V$ , any I/O Pin to Pin 2			0.1	$\mu A$
Clamping Voltage	$V_C$	$t_p = 8/20\mu s$ (Any I/O Pin to Pin 2)		13.7	16.5	V
		$t_p = 8/20\mu s$ (Pin 5 to Pin 2)	$I_{PP} = 14A$			
ESD Clamping Voltage <sup>2</sup>	$V_C$	$t_p = 0.2/100ns$ (Any I/O Pin to Pin 2)	$I_{PP} = 4A$	10.5		V
			$I_{PP} = 16A$	13.5		
Dynamic Resistance <sup>2,3</sup>	$R_{DYN}$	$t_p = 0.2/100ns$ , Any I/O Pin to Pin 2		0.25		$\Omega$
Junction Capacitance	$C_J$	$V_R = 0V$ , $f = 1MHz$	Any I/O Pin to Pin 2	1.1	2.5	pF
			Between I/O pins	0.55	1.5	

### Notes

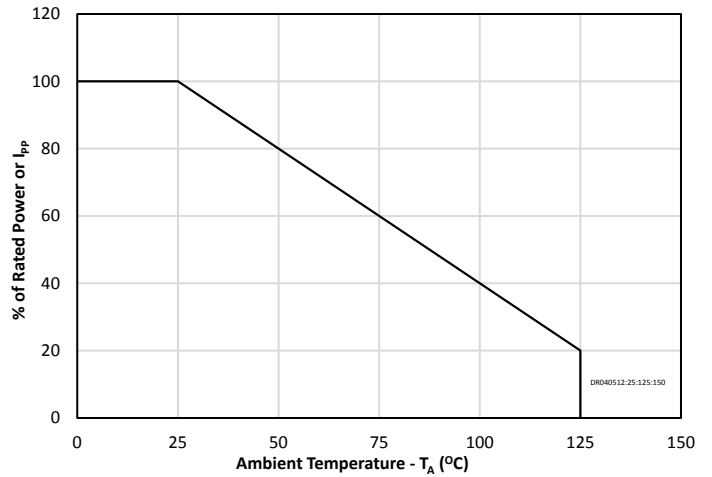
- 1) Measured with a 40dB attenuator, 50 Ohm scope input impedance, 2GHz bandwidth. ESD gun return path connected to ESD ground plane.
- 2) Transmission Line Pulse Test (TLP) Settings:  $t_p = 100ns$ ,  $t_r = 0.2ns$ ,  $I_{TLP}$  and  $V_{TLP}$  averaging window:  $t_1 = 70ns$  to  $t_2 = 90ns$ .
- 3) Dynamic resistance calculated from  $I_{TLP} = 4A$  to  $I_{TLP} = 16A$

# Typical Characteristics

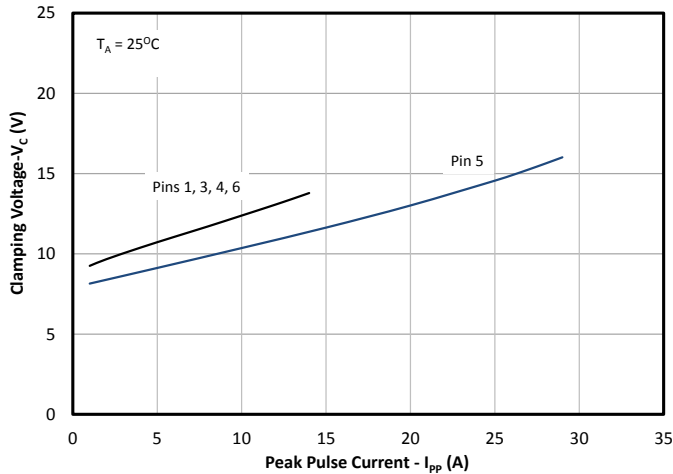
**Non Repetitive Peak Pulse Power vs. Pulse Time**



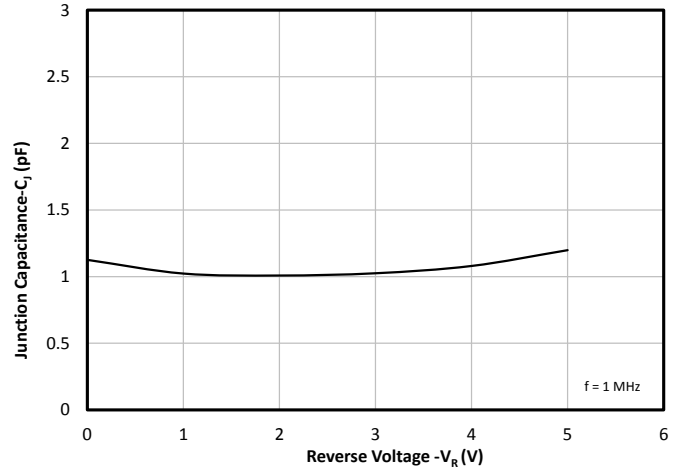
**Power Derating Curve**



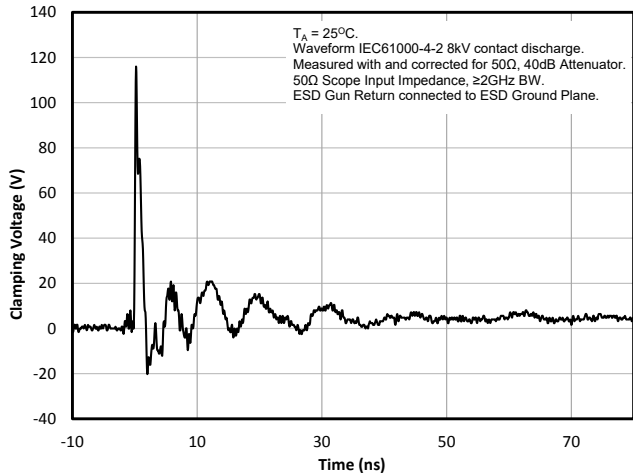
**Clamping Voltage vs. Peak Pulse Current ( $t_p=8/20\mu$ s)**



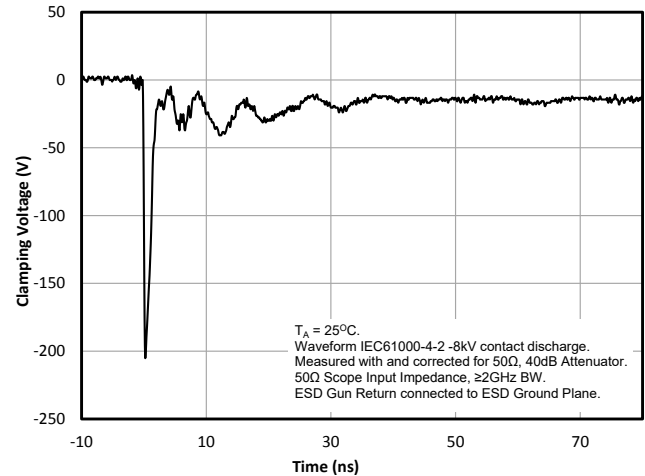
**Capacitance vs. Reverse Voltage (Pins 1, 3, 4, 6 to Pin 2)**



**ESD Clamping (8kV Contact per IEC 61000-4-2) (Pins 1, 3, 4, 6)**

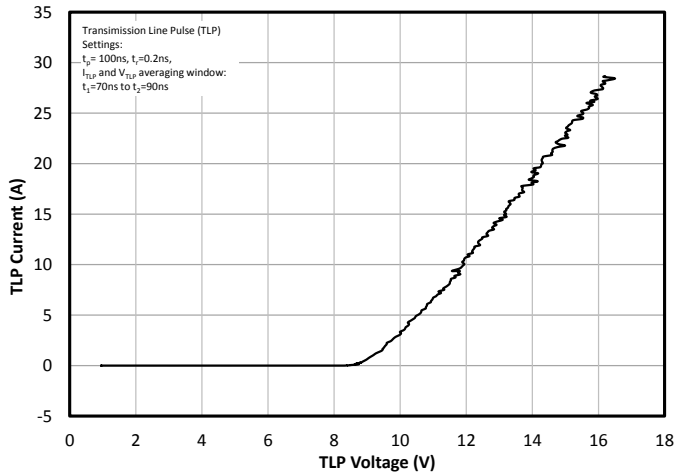


**ESD Clamping (-8kV Contact per IEC 61000-4-2) (Pins 1, 3, 4, 6)**

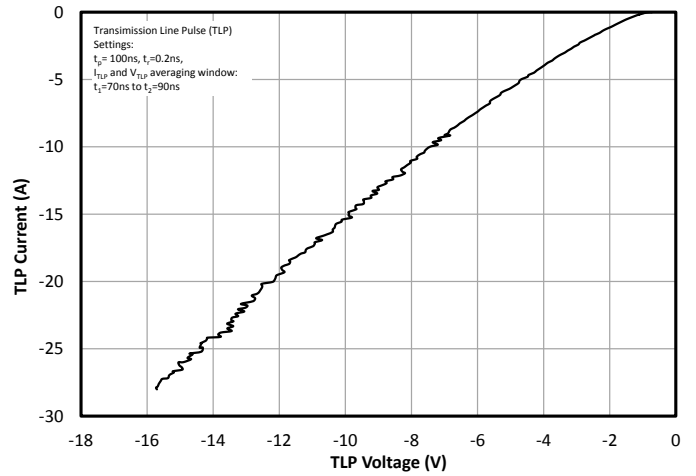


# Typical Characteristics (Continued)

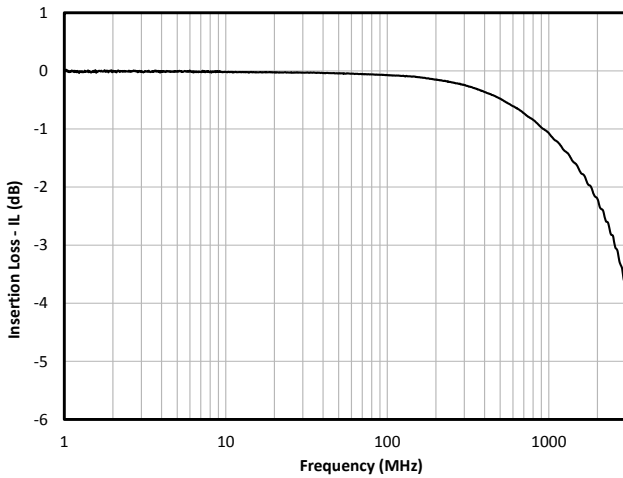
**Positive TLP Characteristic (Pins 1, 3, 4, 6)**



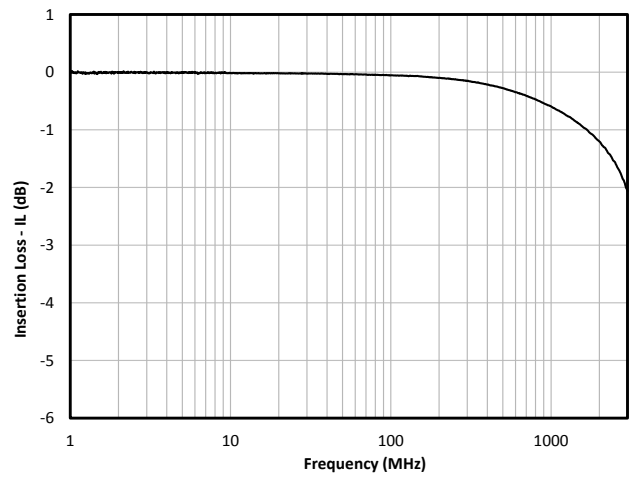
**Negative TLP Characteristic (Pins 1, 3, 4, 6)**



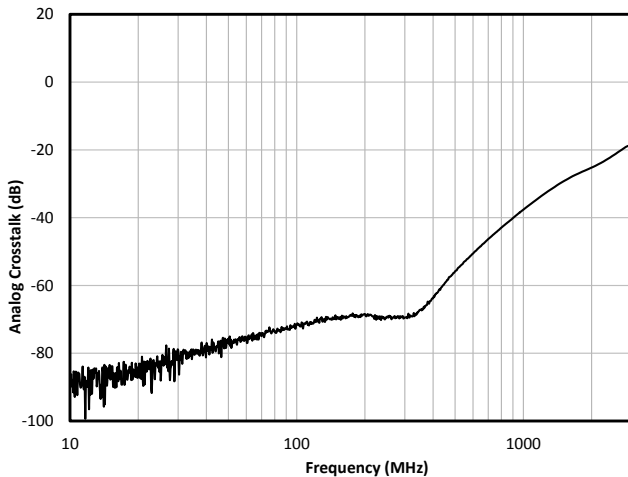
**Insertion Loss (I/O Pins 1, 3, 4, 6)**



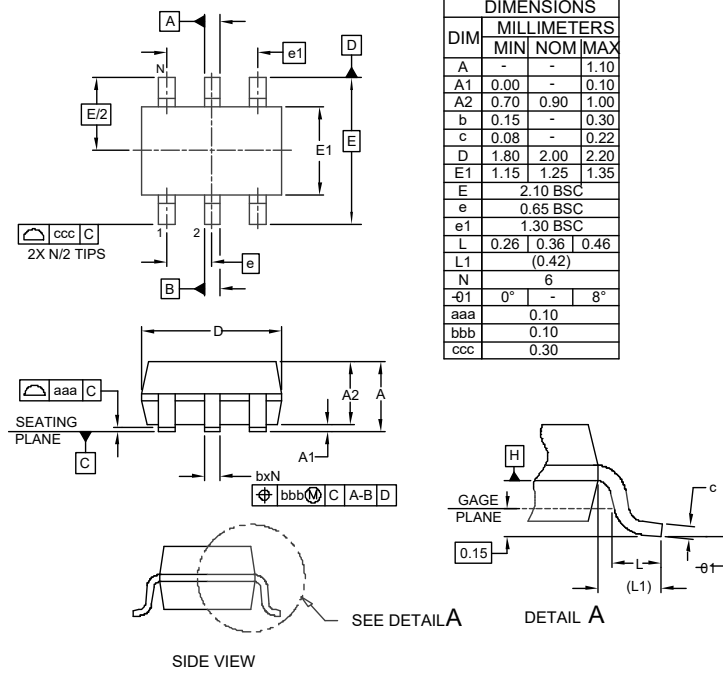
**Insertion Loss (I/O Pin to I/O Pin)**



**Analog Crosstalk**

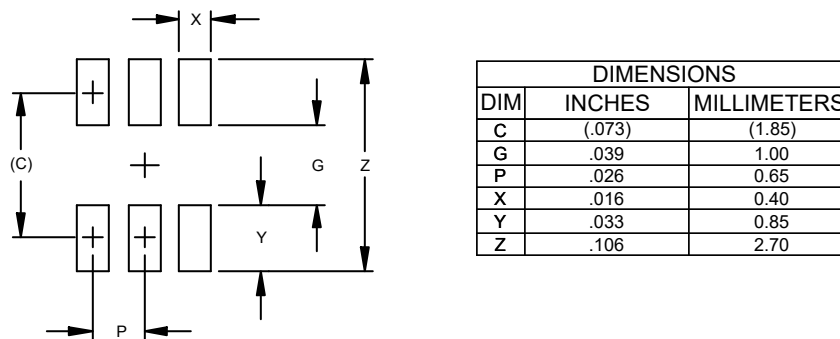


# Outline Drawing - SC70 6L



- NOTES:
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
  2. DATUMS  $\boxed{-A-}$  AND  $\boxed{-B-}$  TO BE DETERMINED AT DATUM PLANE  $\boxed{-H-}$
  3. DIMENSIONS "E1" AND "D" DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

# Land Pattern - SC70 6L



- NOTES:
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
  2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.





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