Note: This datasheet may be out of date.

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Co., Ltd.

http://www.murata.com/en-us/products/productdetail?partno=CSTCE20M0V53-R0

CSTCE20M0V53-R0



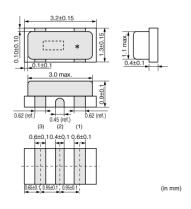






Appearance & Shape







Features

MURATA's package technology expertise has enabled the development of the Chip "CERALOCK" with built-in load capacitors. High-density mounting can be realized because of the small package and the elimination of the need for an external load capacitor.

Features

- 1. Oscillation circuits do not require external load capacitors.
- 2. The series is available in a wide frequency range.
- 3. The resonators are extremely small and have a low profile.
- 4. No adjustment is necessary for oscillation circuits.

Applications

Other Usage

Consumer/Industrial



Packaging Information

Packaging	Specifications	Minimum Order Quantity
-R0	180mm Embossed Tape	3000

1 of 3

Attention

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2. This datasheet has only typical specifications because there is no space for detailed specifications.

Therefore, please review our product specifications or consult the approval sheet for product specifications before ordering



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CSTCE20M0V53-R0



Shape	SMD
Frequency	20.000MHz
Frequency Tolerance	±0.5%
Operating Temperature Range	-20℃ to 80℃
Frequency Shift by Temperature	±0.3%
Frequency Aging	±0.2%
Wash	Not avai able
Resonant Impedance	40Ω
Built-in Load Capacitance	15pF

2 of 3

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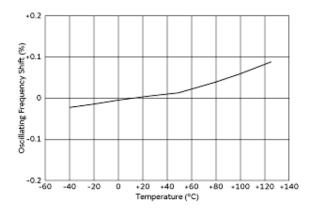
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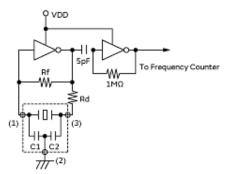
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CSTCE20M0V53-R0







Regarding CERALOCK® with built-in capacitors, the terminal (1) and (3) are interchangeable, but if connection is interchanged, the specification of initial tolerance frequency may not be guaranteed. *Please see dimensions about terminal numbers.

Temperature Characteristics

Measurement Circuit

3 of 3

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