

# Multilayer Ceramic Chip Capacitors

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## CGA3E1C0G2A562J080AC



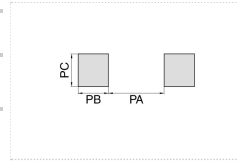
**TDK item description** [?](#) CGA3E1C0G2A562JT\*\*\*\*

**Applications** Automotive Grade

**Feature** Mid Mid Voltage (100 to 630V)  
AEC-Q200 AEC-Q200

**Series** CGA3(1608) [EIA 0603]

**Status** Production



Images are for reference only and show exemplary products.

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### Documents

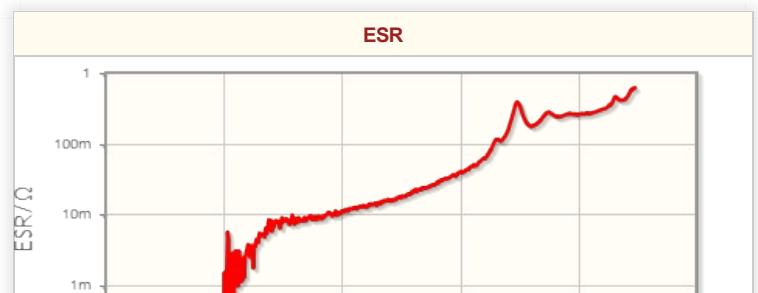
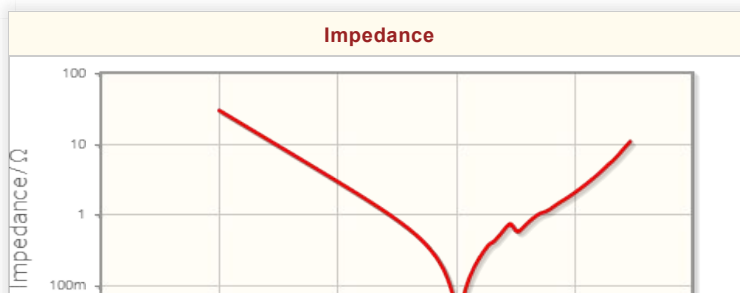
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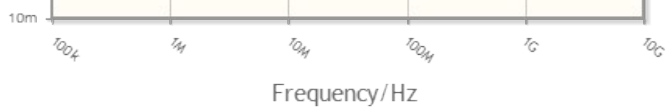
Size	
Length(L)	1.60mm ±0.10mm
Width(W)	0.80mm ±0.10mm
Thickness(T)	0.80mm ±0.10mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	0.30mm Min.
Recommended Land Pattern (PA)	0.70mm to 1.00mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)
Recommended Land Pattern (PB)	0.80mm to 1.00mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)
Recommended Land Pattern (PC)	0.60mm to 0.80mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)

Electrical Characteristics	
Capacitance	5.6nF ±5%
Rated Voltage	100VDC
Temperature Characteristic <a href="#">?</a>	C0G(0±30ppm/°C)
Q (Min.)	1000
Insulation Resistance (Min.)	10000MΩ

Other	
Soldering Method	Wave (Flow) Reflow
AEC-Q200	Yes
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	4000pcs

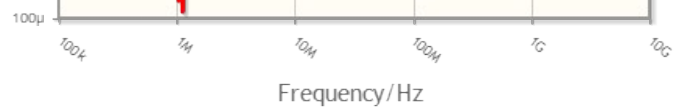
## Characteristic Graph (This is reference data, and does not guarantee the products characteristics.)





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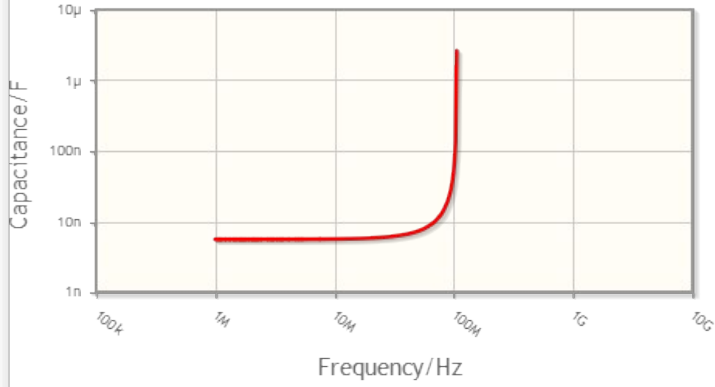
Change settings



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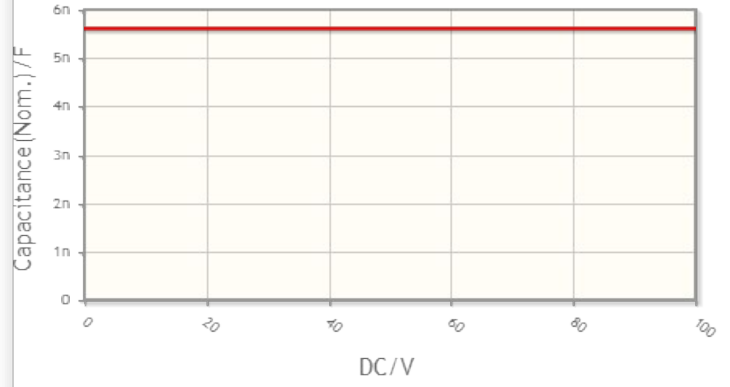
### Capacitance



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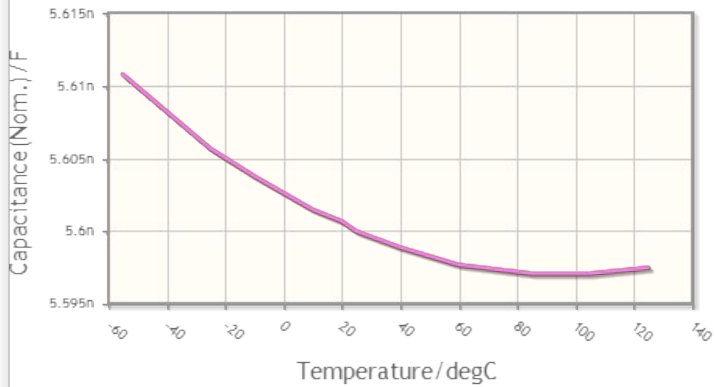
### DC Bias Characteristic



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### Temperature Characteristic



CGA3E1C0G2A562J080AC( No Bias) CGA3E1C0G2A562J080AC(DC Bias s = 50V )

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### Ripple Temperature Rising

no data available

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