

Multilayer Ceramic Chip Capacitors

▼ more

CGA3E1C0G2A103J080AC



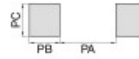
TDK item description ? CGA3E1C0G2A103JT****

Applications Automotive Grade

Feature Mid Mid Voltage (100 to 630V)
AEC-Q200 AEC-Q200

Series CGA3(1608) [EIA 0603]

Status Production



Images are for reference only and show exemplary products.

PDF file of this page

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- Documents**
- Catalog
 - Specification
 - RoHS Certificate
 - SVHC/REACH Certificate Update
 - Selection Guide for Automotive MLCC New
 - [Promotion Video] xEV Higher Withstand Voltage Applications Recommended
 - Sample Kits
 - Characterization Sheet

Size	
Length(L)	1.60mm ±0.10mm
Width(W)	0.80mm ±0.10mm
Thickness(T)	0.80mm ±0.10mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	0.30mm Min.
Recommended Land Pattern (PA)	0.70mm to 1.00mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)
Recommended Land Pattern (PB)	0.80mm to 1.00mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)
Recommended Land Pattern (PC)	0.60mm to 0.80mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)

Electrical Characteristics	
Capacitance	10nF ±5%
Rated Voltage	100VDC
Temperature Characteristic ?	C0G(0±30ppm/°C)
Q (Min.)	1000
Insulation Resistance (Min.)	10000MΩ

Other	
Soldering Method	Wave (Flow) Reflow
AEC-Q200	Yes
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	4000pcs

Characteristic Graph (This is reference data, and does not guarantee the products characteristics.)



CGA3E1C0G2A103J080AC

Configuration

CGA3E1C0G2A103J080AC

Configuration

Capacitance

CGA3E1C0G2A103J080AC

Configuration

DC Bias Characteristic

CGA3E1C0G2A103J080AC

Configuration

Temperature Characteristic

CGA3E1C0G2A103J080AC(No Bias) CGA3E1C0G2A103J080AC(DC Bias = 50V)

Configuration

Ripple Temperature Rising

no data available

Configuration



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- Voltage / Current / Temperature Protection Devices >
- Sensors and Sensor Systems >

- [Ceramic Switching / Heating, Piezo Components, Buzzers and Microphones](#) >
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