# **EDT**, +125°C



### **Overview**

The KEMET EDT aluminum electrolytic surface mount capacitors are designed for applications requiring high operating temperatures of up to 125°C and a low profile vertical chip.

### **Applications**

Typical applications include automatic mounting and reflow soldering.

### **Benefits**

- · Surface mount lead terminals
- · Low profile vertical chip
- Operating temperature of -40°C to +125°C
- 125°C/1,000 2,000 hours

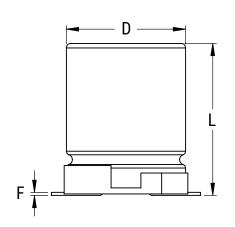


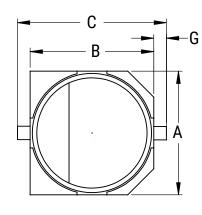
## **Part Number System**

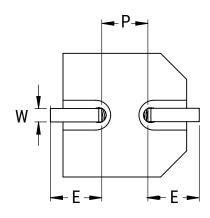
EDT	107	M	010	Α	9L	AA
Series	Capacitance Code (pF)	Tolerance	Rated Voltage (VDC)	Electrical Parameters	Size Code	Packaging
Surface Mount Aluminum Electrolytic	First two digits represent significant figures for capacitance values. Last digit specifies the number of zeros to be added.	M = ±20%	010 = 10 016 = 16 025 = 25 035 = 35 050 = 50	A = Standard S = AEC-Q200	See Dimension Table	AA = Tape & Reel (Paper) AP = Tape & Reel (Plastic)  *AP is only available for AEC-Q200 versions



### **Dimensions - Millimeters**







Cizo Codo	D		D L		A/B		C		E	
Size Code	Nominal	Tolerance								
9H	6.3	±0.5	7.7	±0.3	6.6	±0.2	7.8	Maximum	2.6	±0.2
9L	8.0	±0.5	6.2	±0.3	8.3	±0.2	9.5	Maximum	3.4	±0.2
9M	8.0	±0.5	10.2	±0.3	8.3	±0.2	10.0	Maximum	3.4	±0.2
9P	10.0	±0.5	10.2	±0.3	10.3	±0.2	13.0	Maximum	3.5	±0.2
9R	12.5	±0.5	13.5	±0.5	12.8	±0.2	15.2	Maximum	4.9	±0.2
98	12.5	±0.5	16.0	±0.5	12.8	±0.2	15.2	Maximum	4.9	±0.2

Ciro Codo	Size Code Nominal Tolerance			G		P	W		
Size Code			Nominal	Tolerance	Nominal	Tolerance	Nominal	Tolerance	
9H	0.3	Maximum	0.35	+0.15/-0.2	1.8	±0.2	0.65	±0.1	
9L	0.3	Maximum	0.35	+0.15/-0.2	2.2	±0.2	0.65	±0.1	
9M	0.3	Maximum	0.70	±0.2	3.1	±0.2	0.90	±0.2	
9P	0.3	Maximum	0.70	±0.2	4.6	±0.2	0.90	±0.2	
9R	0.3	Maximum	1.0	±0.2	4.6	±0.2	1.25	±0.2	
9\$	0.3	Maximum	1.0	±0.2	4.6	±0.2	1.25	±0.2	



### **Environmental Compliance**

As an environmentally conscious company, KEMET is working continuously with improvements concerning the environmental effects of both our capacitors and their production. In Europe (RoHS Directive) and in some other geographical areas like China, legislation has been put in place to prevent the use of some hazardous materials, such as lead (Pb), in electronic equipment. All products in this catalog are produced to help our customers' obligations to guarantee their products and fulfill these legislative requirements. The only material of concern in our products has been lead (Pb), which has been removed from all designs to fulfill the requirement of containing less than 0.1% of lead in any homogeneous material. KEMET will closely follow any changes in legislation world wide and make any necessary changes in its products, whenever needed.

Some customer segments such as medical, military and automotive electronics may still require the use of lead in electrode coatings. To clarify the situation and distinguish products from each other, a special symbol is used on the packaging labels for RoHS compatible capacitors.

Due to customer requirements, there may appear additional markings such as lead free (LF) or lead-free wires (LFW) on the label.

### **Performance Characteristics**

Item	Performance Characteristics					
Capacitance Range	22 − 1,000 μF					
Rated Voltage	10 - 50 VDC					
Operating Temperature	-40°C to +125°C					
Capacitance Tolerance	±20% at 120 Hz/20°C					
Life Test	1,000 – 2,000 hours (see conditions in Test Method & Performance)					
Laskana Ouwant	I ≤ 0.01 CV or 3 μA, whichever is greater					
Leakage Current	C = rated capacitance (µF), V = rated voltage (VDC). Voltage applied for 2 minutes at 20°C.					
	Automotive version: Part number position 11 = "S"					
Vibration Test Specifications	5G maximum acceleration. Vibration applied in 3 directions (X, Y, and Z axis). 4-hour sessions at 10 – 2,000 Hz					
	(Capacitor clamped by the body)					

# **Impedance Z Characteristics at 120 Hz**

Rated Voltage (VDC)	10	16	25	35	50	35	50
Z (-25°C)/Z (20°C)	2	2	2	2	2	2	2
Z (-40°C)/Z (20°C)	3	3	3	3	3	3	3



### Compensation Factor of Ripple Current (RC) vs. Frequency

Rated Voltage (VDC)	60 Hz	120 Hz	1 kHz	10 kHz
Coefficient	0.85	1.00	1.10	1.20

#### **Test Method & Performance**

Conditions	Load Li	fe Test	Shelf Life Test			
Temperature	125	125°C				
	Can Ø = 6.3 x 7.7	1,000 hours	1,000 hours			
Test Duration	Can Ø = 8 x 6.2	1,000 hours	500 hours			
	Can Ø ≥ 8 x 10.2 mm	Can Ø ≥ 8 x 10.2 mm 2,000 hours				
Ripple Current	Maximum ripple current s	No ripple current applied				
Voltage	The sum of DC voltage and the pea rated voltage o	ak AC voltage must not exceed the f the capacitor.	No voltage applied			
Performance	The following specification	s will be satisfied when the ca	pacitor is restored to 20°C:			
Capacitance Change	Within ±20% of the initial value					
Dissipation Factor	Does not exceed 200% of the specified value					
Leakage Current	Does not exceed specified value					

#### **Shelf Life**

The capacitance, ESR and impedance of a capacitor will not change significantly after extended storage periods, however, the leakage current will very slowly increase.

KEMET's E aluminum electrolytic capacitors should not be stored in high temperatures or where there is a high level of humidity. The suitable storage condition for KEMET's E aluminum electrolytic capacitors is +5 to +35°C and less than 75% in relative humidity. KEMET's E aluminum electrolytic capacitors should not be stored in damp conditions such as water, saltwater spray or oil spray. KEMET's E aluminum electrolytic capacitors should not be stored in an environment full of hazardous gas (hydrogen sulphide, sulphurous acid gas, nitrous acid, chlorine gas, ammonium, etc.) KEMET's E aluminum electrolytic capacitors should not be stored under exposure to ozone, ultraviolet rays or radiation.

If a capacitor has been stored for more than 18 months under these conditions and it shows increased leakage current, then a treatment by voltage application is recommended.

## Re-Age (Reforming) Procedure

Apply the rated voltage to the capacitor at room temperature for a period of one hour, or until the leakage current has fallen to a steady value below the specified limit. During re-aging a maximum charging current of twice the specified leakage current or 5 mA, whichever is greater, is suggested.



**Table 1 - Ratings & Part Number Reference** 

VDC	VDC Surge Voltage	Rated Capacitance 120 Hz 20°C (µF)	Case Size D x L (mm)	DF 120 Hz 20°C (tan δ%)	RC 120 Hz 125°C (mA)	LC 20°C 2 Minutes (µA)	Part Number
10	13	100	8 x 6.2	26	75	10.0	EDT107M010(1)9LAA
10	13	150	6.3 x 7.7	26	70	15.0	EDT157M010(1)9HAA
10	13	150	8 x 6.2	26	75	15.0	EDT157M010(1)9LAA
10	13	220	8 x 10.2	26	130	22.0	EDT227M010(1)9MAA
10	13	330	8 x 10.2	26	130	33.0	EDT337M010(1)9MAA
10	13	470	10 x 10.2	26	180	47.0	EDT477M010(1)9PAA
10	13	680	10 x 10.2	26	180	68.0	EDT687M010(1)9PAA
10	13	1000	10 x 10.2	26	180	100.0	EDT108M010(1)9PAA
16	20	47	6.3 x 7.7	2	70	7.5	EDT476M016(1)9HAA
16	20	100	6.3 x 7.7	2	70	16.0	EDT107M016(1)9HAA
16	20	100	8 x 6.2	2	75	16.0	EDT107M016(1)9LAA
16	20	150	8 x 10.2	2	130	24.0	EDT157M016(1)9MAA
16	20	220	8 x 10.2	2	130	35.2	EDT227M016(1)9MAA
16	20	330	8 x 10.2	2	180	52.8	EDT337M016(1)9MAA
16	20	470	10 x 10.2	2	180	75.2	EDT477M016(1)9PAA
25	32	47	6.3 x 7.7	18	70	11.8	EDT476M025(1)9HAA
25	32	47	8 x 6.2	18	75	11.8	EDT476M025(1)9LAA
25	32	100	6.3 x 7.7	18	70	25.0	EDT107M025(1)9HAA
25	32	100	8 x 6.2	18	75	25.0	EDT107M025(1)9LAA
25	32	100	8 x 10.2	18	130	25.0	EDT107M025(1)9MAA
25	32	150	8 x 10.2	18	130	37.5	EDT157M025(1)9MAA
25	32	220	10 x 10.2	18	180	55.0	EDT227M025(1)9PAA
25	32	330	10 x 10.2	18	180	82.5	EDT337M025(1)9PAA
35	44	22	6.3 x 7.7	14	70	7.7	EDT226M035(1)9HAA
35	44	33	6.3 x 7.7	14	70	11.6	EDT336M035(1)9HAA
35	44	33	8 x 6.2	14	75	11.6	EDT336M035(1)9LAA
35	44	47	8 x 6.2	14	75	16.5	EDT476M035(1)9LAA
35	44	47	8 x 10.2	14	130	16.5	EDT476M035(1)9MAA
35	44	100	8 x 10.2	14	130	35.0	EDT107M035(1)9MAA
35	44	150	10 x 10.2	14	180	52.5	EDT157M035(1)9PAA
35	44	220	10 x 10.2	14	180	77.0	EDT227M035(1)9PAA
50	63	22	8 x 6.2	12	75	11.0	EDT226M050(1)9LAA
50	63	33	8 x 10.2	12	130	16.5	EDT336M050(1)9MAA
50	63	47	8 x 10.2	12	130	23.5	EDT476M050(1)9MAA
50	63	100	10 x 10.2	12	180	50.0	EDT107M050(1)9PAA
VDC	VDC Surge	Rated Capacitance	Case Size	DF	RC	LC	Part Number

<sup>(1)</sup> Insert Electrical Parameters code. See Part Number System for available options.



### **Mounting Positions (Safety Vent)**

In operation, electrolytic capacitors will always conduct a leakage current, which causes electrolysis. The oxygen produced by electrolysis will regenerate the dielectric layer but, at the same time, the hydrogen released may cause the internal pressure of the capacitor to increase. The overpressure vent, or safety vent, ensures that the gas can escape when the pressure reaches a certain value. All mounting positions must allow the safety vent to work properly.

### Installing

- As a general principle, lower-use temperatures result in a longer, useful life of the capacitor. For this reason, it should be
  ensured that electrolytic capacitors are placed away from heat-emitting components. Adequate space should be allowed
  between components for cooling air to circulate, particularly when high ripple current loads are applied. In any case, the
  maximum category temperature must not be exceeded.
- Do not deform the case of the capacitors or use capacitors with a deformed case.
- Verify that the connections of the capacitors are able to insert on the board without excessive mechanical force.
- If the capacitors require mounting through additional means, the recommended mounting accessories shall be used.
- Verify the correct polarization of the capacitor on the board.
- · Verify that the space around the pressure relief device is according to the following guideline:

Case Diameter	Space Around Safety Vent
≤ 16 mm	> 2 mm
> 16 to ≤ 40 mm	> 3 mm
> 40 mm	> 5 mm

It is recommended that capacitors always be mounted with the safety device uppermost or in the upper part of the capacitor.

- If the capacitors are stored for a long time, the leakage current must be verified. If the leakage current is superior to the value listed in this catalog, the capacitors must be reformed. In this case, they can be reformed by application of the rated voltage through a series resistor approximately 1 k $\Omega$  for capacitors with  $V_R \le 160$  V (5 W resistor) and 10 k $\Omega$  for the other rated voltages.
- In the case of capacitors connected in a series, a suitable voltage sharing must be used.

  In the case of balancing resistors, the approximate resistance value can be calculated as: R = 60/C.

KEMET recommends, nevertheless, to ensure that the voltage across each capacitor does not exceed its rated voltage.



### Electrical Ratings: Capacitance (ESC)



Simplified equivalent circuit diagram of an electrolytic capacitor

The capacitive component of the equivalent series circuit, (equivalent series capacitance - ESC), is determined by applying an alternate voltage of  $\leq 0.5$  V at a frequency of 120 or 100 Hz and 20°C (IEC 384-1, 384-4).

### **Temperature Dependence of the Capacitance**

Capacitance of an electrolytic capacitor depends upon temperature: with decreasing temperature the viscosity of the electrolyte increases, thereby reducing its conductivity.

Capacitance will decrease if temperature decreases. Furthermore, temperature drifts cause armature dilatation and, therefore, capacitance changes (up to 20% depending on the series considered, from 0 to 80°C). This phenomenon is more evident for electrolytic capacitors than for other types.

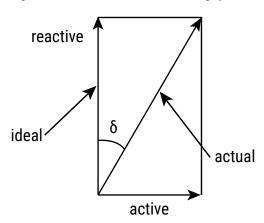
#### **Frequency Dependence of the Capacitance**

Effective capacitance value is derived from the impedance curve, as long as impedance is still in the range where the capacitance component is dominant.

C = 
$$\frac{1}{2\pi \text{ fZ}}$$
 C = capacitance (F)  
f = frequency (Hz)  
Z = impedance ( $\Omega$ )

#### Dissipation Factor tan $\delta$ (DF)

Dissipation Factor  $\tan \delta$  is the ratio between the active and reactive power for a sinusoidal waveform voltage. It can be thought of as a measurement of the gap between an actual and ideal capacitor.



Tan  $\delta$  is measured with the same set-up used for the series capacitance ESC.

Tan  $\delta = \omega \times ESC \times ESR$  where:

ESC = Equivalent series capacitance

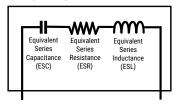
ESR = Equivalent series resistance



#### **Equivalent Series Inductance (ESL)**

Equivalent series inductance or self inductance results from the terminal configuration and internal design of the capacitor.

Capacitor Equivalent Internal Circuit



#### **Equivalent Series Resistance (ESR)**

Equivalent series resistance is the resistive component of the equivalent series circuit. ESR value depends on frequency and temperature, and is related to the tan  $\delta$  by the following equation:

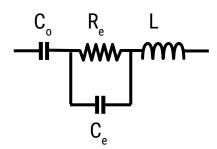
ESR = Equivalent series resistance (Ω)   

$$\tan \delta = \text{Dissipation factor}$$
  
ESC = Equivalent series capacitance (F)   
 $f = \text{Frequency (Hz)}$ 

Tolerance limits of the rated capacitance must be taken into account when calculating this value.

### Impedance (Z)

Impedance of an electrolytic capacitor results from a circuit formed by the following individual equivalent series components:



C<sub>o</sub> = Aluminum oxide capacitance (surface and thickness of the dielectric.)

 $R_e$  = Resistance of electrolyte and paper mixture (other resistances not depending on the frequency are not considered: tabs, plates, etc.)

C<sub>a</sub> = Electrolyte soaked paper capacitance.

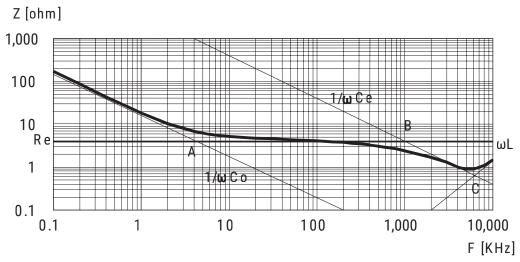
L = Inductive reactance of the capacitor winding and terminals.

Impedance of an electrolytic capacitor is not a constant quantity that retains its value under all conditions; it changes depending on frequency and temperature.

Impedance as a function of frequency (sinusoidal waveform) for a certain temperature can be represented as follows:



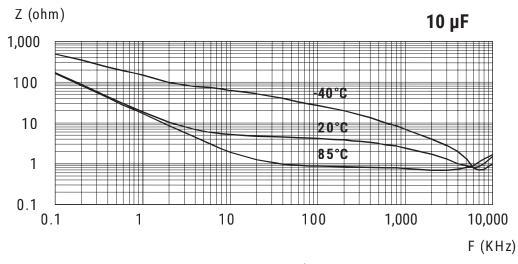
#### Impedance (Z) cont.



- · Capacitive reactance predominates at low frequencies.
- With increasing frequency, capacitive reactance  $Xc = 1/\omega C_o$  decreases until it reaches the order of magnitude of electrolyte resistance  $R_o(A)$
- At even higher frequencies, resistance of the electrolyte predominates: Z = R<sub>a</sub> (A B)
- When the capacitor's resonance frequency is reached ( $\omega_0$ ), capacitive and inductive reactance mutually cancel each other  $1/\omega C_p = \omega L$ ,  $\omega_0 = 1/SQR(LC_p)$
- Above this frequency, inductive reactance of the winding and its terminals (XL = Z = ωL) becomes effective and leads to an increase in impedance

Generally speaking, it can be estimated that  $C_a \approx 0.01 C_o$ .

Impedance as a function of frequency (sinusoidal waveform) for different temperature values can be represented as follows (typical values):



 $R_{\rm e}$  is the most temperature-dependent component of an electrolytic capacitor equivalent circuit. Electrolyte resistivity will decrease if temperature rises.

In order to obtain a low impedance value throughout the temperature range,  $R_e$  must be as little as possible. However,  $R_e$  values that are too low indicate a very aggressive electrolyte, resulting in a shorter life of the electrolytic capacitor at high temperatures. A compromise must be reached.



#### **Leakage Current (LC)**

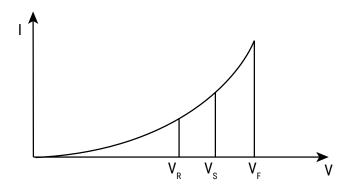
Due to the aluminum oxide layer that serves as a dielectric, a small current will continue to flow even after a DC voltage has been applied for long periods. This current is called leakage current.

A high leakage current flows after applying voltage to the capacitor then decreases in a few minutes, for example, after prolonged storage without any applied voltage. In the course of continuous operation, the leakage current will decrease and reach an almost constant value.

After a voltage-free storage the oxide layer may deteriorate, especially at a high temperature. Since there are no leakage currents to transport oxygen ions to the anode, the oxide layer is not regenerated. The result is that a higher than normal leakage current will flow when voltage is applied after prolonged storage.

As the oxide layer is regenerated in use, the leakage current will gradually decrease to its normal level.

The relationship between the leakage current and voltage applied at constant temperature can be shown schematically as follows:



#### Where:

 $V_{r}$  = Forming voltage

If this level is exceeded, a large quantity of heat and gas will be generated and the capacitor could be damaged.

V<sub>D</sub> = Rated voltage

This level represents the top of the linear part of the curve.

V<sub>s</sub> = Surge voltage

This lies between  $V_R$  and  $V_F$ . The capacitor can be subjected to  $V_S$  for short periods only.

Electrolytic capacitors are subjected to a reforming process before acceptance testing. The purpose of this preconditioning is to ensure that the same initial conditions are maintained when comparing different products.

#### **Ripple Current (RC)**

The maximum ripple current value depends on:

- Ambient temperature
- Surface area of the capacitor (heat dissipation area)  $\tan\delta$  or ESR
- Frequency

The capacitor's life depends on the thermal stress.



#### **Frequency Dependence of the Ripple Current**

ESR and, thus, the tan  $\delta$  depend on the frequency of the applied voltage. This indicates that the allowed ripple current is also a function of the frequency.

### **Temperature Dependence of the Ripple Current**

The data sheet specifies maximum ripple current at the upper category temperature for each capacitor.

#### **Expected Life Calculation**

Expected life depends on operating temperature according to the following formula:  $L = Lo \times 2^{(To-T)/10}$  Where:

L: Expected life

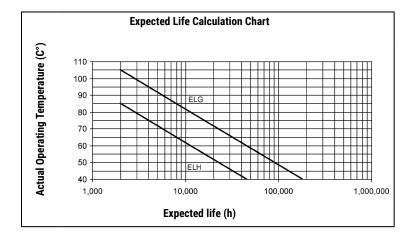
Lo: Load life at a maximum permissible operating

temperature

T: Actual operating temperature

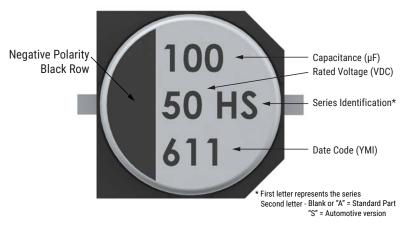
To: Maximum permissible operating temperature

This formula is applicable between 40°C and To.





### **Standard Marking for Surface Mount Types**



Note: 6.3 V rated voltage shall be marked as 6 V, but 6.3 V shall be assured.

### **Batch Code Explanation**

Y = Year

Code	6	7	8	9	0	1	2	3	4	5
Year	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025

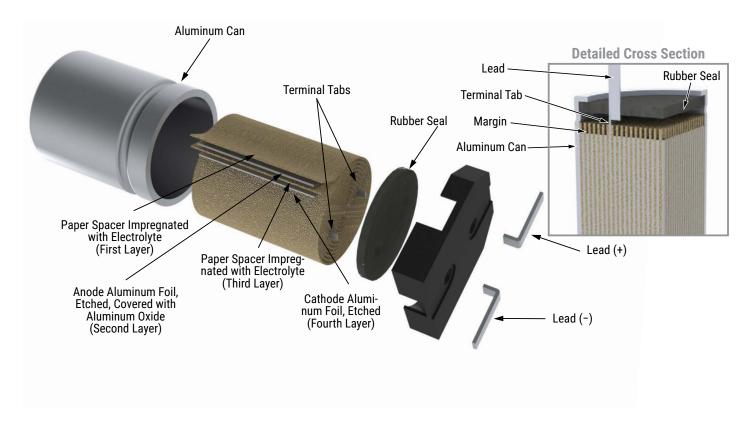
#### M = Month

Code	1	2	3	4	5	6	7	8	9	Α	В	С
Month	1	2	3	4	5	6	7	8	9	10	11	12

I = Internal code



### **Construction**

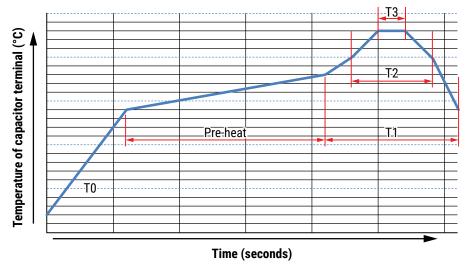




# **Soldering Process**

The soldering conditions should be within the specified conditions below:

Do not dip the capacitor body into the melted solder. Flux should only be applied to the capacitor terminals.



Vapor heat transfer systems are not recommended. The system should be thermal, such as infra-red radiation or hot blast Observe the soldering conditions as shown below.

Do not exceed these limits and avoid repeated reflowing

# **Reflow Soldering**

	Temperature (°C)	Maximum Time (Seconds)
T0	20 - 140	60
Pre-heat	140 - 180	150
T1	180 - 140	100
T2	> 200	60
Т3	230	20

## **Lead-Free Reflow Soldering**

	Temperature (°C)	Maximum Time (Seconds)		
T0	20 - 160	60		
Pre-heat	160 - 190	120		
T1	190 - 180	90		
T2	> 220	60		

# **Lead-Free Reflow Soldering cont.**

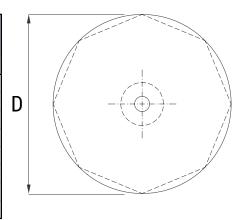
	Size	Temperature (°C)	Maximum Time (Seconds)		
T3	Φ4 ~ Φ5 (4 - 50 V)	250	10		
13 4	Ψ4 ~ Ψ3 (4 - 30 V)	260	5		
	Φ6.3 ~ Φ10 (4 - 50 V)	250	5		
	Ф4 ~ Ф10 (63 - 100 V)	250	5		

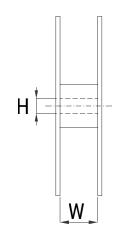
	Size	Temperature (°C)	Maximum Time (Seconds)		
	Ф4 ~ Ф5	250	10		
	(4 - 50 V)	260	5		
Т3	Ф6.3 ~ Ф10 (4 - 50 V)	250	5		
	Φ4 ~ Φ10 (63 - 100 V)	250	5		
	≥ Ф12.5	250	5		



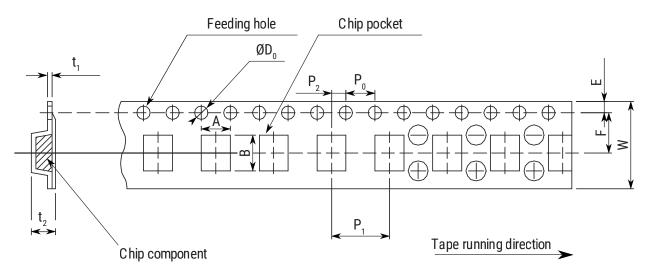
### **Lead Taping & Packaging**

Case Size (mm)	Reel					
	D	Н	W			
	±0.2	±0.8	±1.0			
4 x 5.4		21	14			
5 x 5.4		21	14			
6.3 x 5.4	000	21	18			
6.3 x 5.8		21	18			
6.3 x 7.7	380	21	18			
8 x 6.2		21	18			
8 x 10.2		21	26			
10 x 10.2		21	26			





# **Taping for Automatic Insertion Machines**



Dimensions (mm)	W	A	В	P0	P1	P2	F	D0	E	t1	t2
Tolerance	Nominal	Nominal	Nominal	±0.1	±0.1	±0.1	Nominal	±0.1	Nominal	Nominal	Nominal
4 x 5.4	12	4.7	4.7	4	8	2	5.5	1.5	1.75	0.4	5.8
5 x 5.4	12	5.7	5.7	4	12	2	5.5	1.5	1.75	0.4	5.8
6.3 x 5.4	16	7.0	7.0	4	12	2	7.5	1.5	1.75	0.4	5.8
6.3 x 7.7	16	7.0	7.0	4	12	2	7.5	1.5	1.75	0.4	5.8
8 x 6.2	16	8.7	8.7	4	12	2	7.5	1.5	1.75	0.4	6.8
8 x 10.2	24	8.7	8.7	4	16	2	11.5	1.5	1.75	0.4	11.0
10 x 10.2	24	10.7	10.7	4	16	2	11.5	1.5	1.75	0.4	11.0
12.5 x 13.5	32	13.4	13.4	4	24	2	14.2	1.5	1.75	0.5	14.0
12.5 x 16	32	13.4	13.4	4	24	2	14.2	1.5	1.75	0.5	17.5
16 x 16.5	44	17.5	17.5	4	28	2	20.2	1.5	1.75	0.5	17.5



#### **Construction Data**

The manufacturing process begins with the anode foil being electrochemically etched to increase the surface area and then "formed" to produce the aluminum oxide layer. Both the anode and cathode foils are then interleaved with absorbent paper and wound into a cylinder. During the winding process, aluminum tabs are attached to each foil to provide the electrical contact.

The deck, complete with terminals, is attached to the tabs and then folded down to rest on top of the winding. The complete winding is impregnated with electrolyte before being housed in a suitable container, usually an aluminum can, and sealed. Throughout the process, all materials inside the housing must be maintained at the highest purity and be compatible with the electrolyte.

Each capacitor is aged and tested before being sleeved and packed. The purpose of aging is to repair any damage in the oxide layer and thus reduce the leakage current to a very low level. Aging is normally carried out at the rated temperature of the capacitor and is accomplished by applying voltage to the device while carefully controlling the supply current. The process may take several hours to complete.

Damage to the oxide layer can occur due to variety of reasons:

- Slitting of the anode foil after forming
- Attaching the tabs to the anode foil
- Minor mechanical damage caused during winding

A sample from each batch is taken by the quality department after completion of the production process. This sample size is controlled by the use of recognized sampling tables defined in BS 6001.

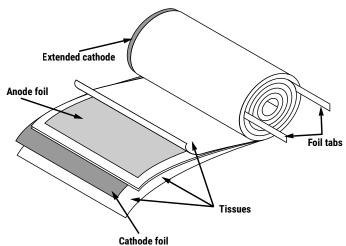
The following tests are applied and may be varied at the request of the customer. In this case the batch, or special procedure, will determine the course of action.

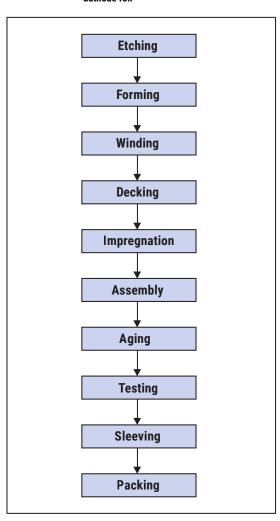
#### Electrical:

- · Leakage current
- Capacitance
- ESR
- Impedance
- Tan Delta

#### Mechanical/Visual:

- Overall dimensions
- Torque test of mounting stud
- Print detail
- · Box labels
- Packaging, including packed quantity







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Although all product-related warnings, cautions and notes must be observed, the customer should not assume that all safety measures are indicated or that other measures may not be required.

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