&TDK Multilayer Ceramic Chip Capacitors

C5750X6S2W225K250KA



TDK item description C5750X6S2W225KT****

Applications	Commercial Grade	<
Feature	Mid Voltage (100 to 630V)	
Series	C5750 [EIA 2220]	
Status	Production	

Size		
Length(L)	5.70mm ±0.40mm	
Width(W)	5.00mm ±0.40mm	
Thickness(T)	2.50mm ±0.30mm	
Terminal Width(B)	0.20mm Min.	
Terminal Spacing(G)		
Recommended Land Pattern (PA)	4.10mm to 4.80mm	
Recommended Land Pattern (PB)	1.20mm to 1.40mm	
Recommended Land Pattern (PC)	4.00mm to 5.00mm	

Electrical Characteristics		
Capacitance	2.2µF ±10%	
Rated Voltage	450VDC	
Temperature Characteristic	X6S(±22%)	
Dissipation Factor (Max.)	2.5%	
Insulation Resistance (Min.)	227ΜΩ	

	Other
Soldering Method	Reflow
AEC-Q200	No
Packing	Blister (Plastic)Taping [180mm Reel]
Package Quantity	500pcs

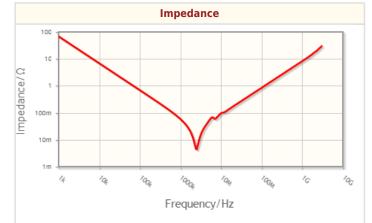
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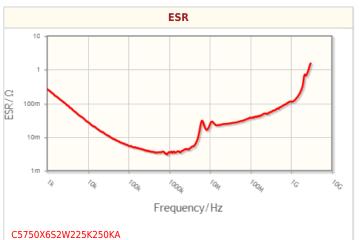
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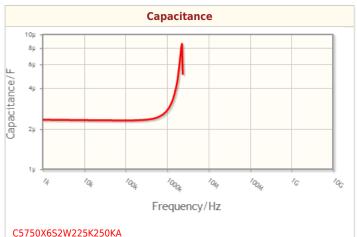


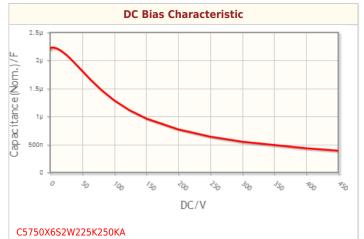


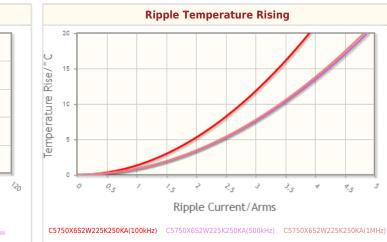
Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)



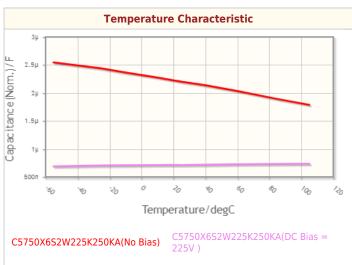
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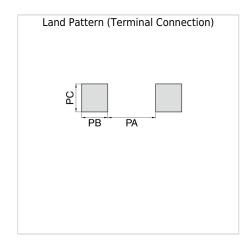
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Associated Images



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