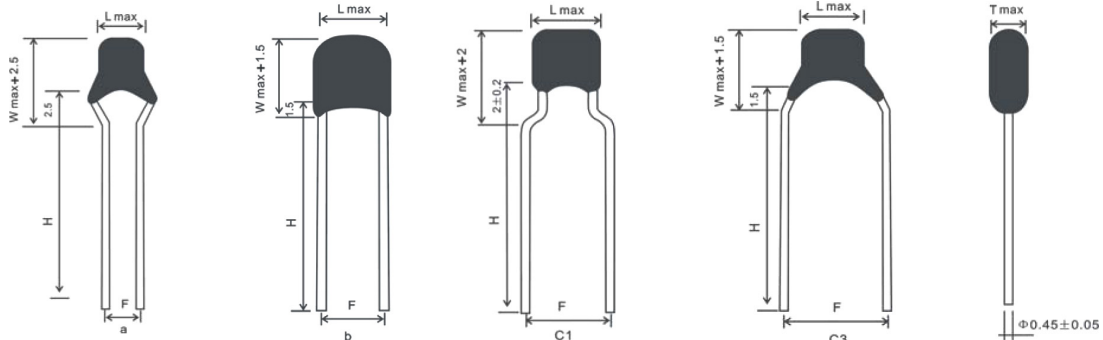


Multilayer Ceramic Capacitor



	NPO	X7R	Y5V
Operating Temperature	-55°C to +125°C	-55°C to +125°C	-25°C to +85°C

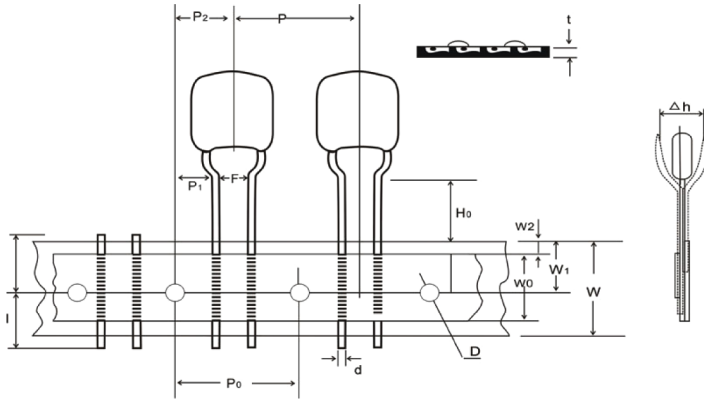


Size Code	Shape	Dimensions (mm)					voltage	Capacitance(pF)		
		F (±0.5)	H (±1)	Lmax	Wmax	Tmax		NPO	X7R	Y5V
0805	a	2.54	5							
	b	2.54	10				25V	0R5~332	331~104	
	C1	5.08	5/10	4.2	3.8	3.8	50V	0R5~222	331~104	103~105
	C2	5.08	5				100V	0R5~102	331~104	103~684
	C3	5.08	5/10							
1206	a	2.54					25V	0R5~682	102~224	
	b	3.5	10	5	4.5	3.8	50V	0R5~472	102~104	103~125
	C1	5.08					100V	0R5~392	102~105	103~105
1210	b	3.5	10	7.6	5.5	3.8	25V	0R5~103	102~334	
	C1	5.08					50V	0R5~472	102~224	104~155
							100V	0R5~392	102~105	
1812	b		10	8.5	8.5	3.8	25V	561~103	103~474	
							50V	561~682	103~334	154~335
							100V	561~472	103~105	
2225	b		10	10.5	9.5	4.2	25V	102~223	103~105	
							50V	102~223	103~105	684~475
							100V	102~103	103~474	
3035	b		10	12.5	10.5	4.2	25V	102~104	103~225	105~106
							50V	102~473	103~205	105~685
							100V	102~333	103~105	

*Lead spacing determined by customer requirements.

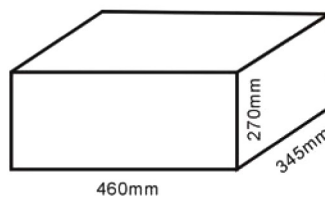
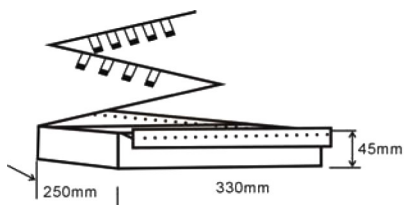
Multilayer Ceramic Capacitor

Radial Leads MLCC Packaging Style:



Description	Symbol	Dimensions (mm)	Remarks
Pitch Of Component	P	12.7± 1	
Feedhold Pitch	PO	12.7± 0.3	Cumulative Pitch Error: ±1mm/20 Pitches
Feed Hold Center to Lead	P1	3.85± 0.7	For F:5.08, 5.1±0.7 For F:2.54
Feed Hold Center to Component Center	P2	8.35±1.3	
Lead To Lead Spacing	F	5.08+0.8/-0.2 or 2.5+0.8/-0.2	To Lead Tip Within Tol
Component Alignment, F-R	ΔH	2 max	The Alignment from the Center of the Lead is ±1mm
Tape Width	W	18± 1	
Adhesive Tape Width	W0	12± 1	
Hole Position	W1	9± 0.5	
Adhesive Tape Position	W2	3 max	Adhesive Tape Must Not Protrude from Bode Paper
Lead-Wire Clinch Height	H0	15-20± 0.5	6.5≤H0-W1
Component Height	H1	32.25 max	
Feed Hole Diameter	D0	4± 0.3	
Total Tape Thickness	T	0.7± 0.2	

Ammo Packaging:



Multilayer Ceramic Capacitor

How To Order Of Radial Leads MLCC:

MC	---	0805	Y	104	M	500	PF3	R
↓		↓	↓	↓	↓	↓	↓	↓
A		B	C	D	E	F	G	H

A:
Product Type

B:	
Unit: Inches	
Cmos Chip Size (L × W)	
Code	Chip
0805	0.08 × 0.05
1206	0.12 × 0.06
1210	0.12 × 0.1
1812	0.18 × 0.12
2225	0.22 × 0.25
3035	0.30 × 0.35

C:	
Dielectric	
N	NPO
B	X7R
Y	Y5V

D:
Capacitance
First two digits are significant third digit is number of zeros. For Example: 104=1,00,000pF 5R6=5.6pF

E:	
Tolerance	
B	±0.1pF
C	±0.25pF
D	±0.5pF
F	±1%
G	±2%
J	±5%
K	±10%
M	±20%
N	±30%
S	+50% - 20%
Z	+80% - 20%
P	+100% - 0%
B.C.D for C<10pF NPO: B.C.D.F.G.J.K.M. X7R: K.M.S.Z. Y5V: M.S.Z.P.	

F:
Rated Voltage
The code meaning is same as capacitance. For Example: 250=25V 500=50V 101=100V

G:		
Packaging Style		
Ammo	PF1	2.54mm
	PF3	5.08mm
Bulk	F1	2.54mm
	F2	4.57mm
	F3	5.08mm
	F4	7.5mm
	F5	3.5mm

H:	
Pb	
R	RoHS

Note: Specifications are subject to change without notice.

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