FARNELL INSTRUM

Issue No. : 151RJ00008162
Date of Issue : March 03.2008
Classification : ■ New ☐ Changed

PRODUCT SPECIFICATION FOR APPROVAL

Product Description : Anti-Pulse Thick Film Chip Resistors (RoHS Appliance)
Product Part Number : ERJT14J＊＊＊U

Country of Origin : JAPAN
Applications : Standard electronic equipment

*If you approve this specification, please fill in and sign the below and return 1 copy to us.

Approval No :

Approval Date :

Executed by :

(signature)

Title :
Dept. :

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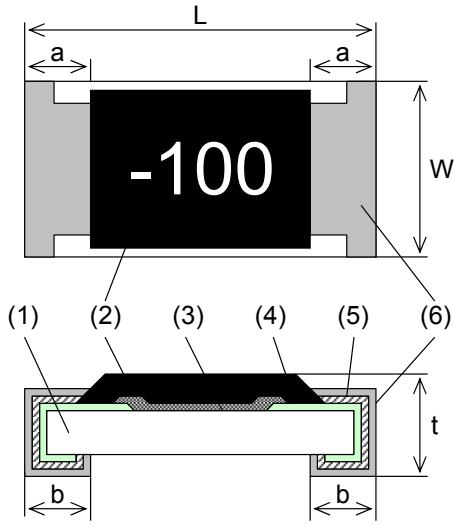
Title : Manager of Engineering

Panasonic

Subject		Spec. No.
Chip Resistor	PRODUCT SPECIFICATION FOR IMFORMATION	151-SRJ-ET1401A
Part No.		
ERJT14		11 - 1

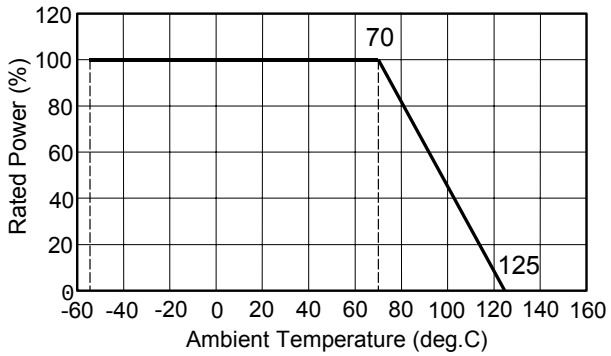
1. Dimension



(1) Substrate : Alumina
(2) Protective Coating : Resin
(3) Resistive Element : Metal glaze
(4) Inner Termination : Thick film material
(5) Middle Termination : Ni Plating
(6) Outer Termination : Sn Plating

	L	W	a	b	t
Dimension (mm)	3.20+/-0.20	2.50+/-0.20	0.35+/-0.20	0.50+/-0.20	0.60+/-0.10

2. Power Derating Curve



Operating temperature range
: -55 to 125 deg.C

Figure 1

3. Ratings

3-1 Resistor

Item	Rated value	Explanation
Power Rating	0.5W	When used at ambient temperature above 70 deg.C, power rating shall be determined in accordance with Figure 1.

Item	Rated value	Explanation
Rated voltage & Rated Continuous Working Voltage	The rated voltage of each resistance should be calculated from the equation below. Rated voltage (V) = $\sqrt{\text{Power rating (W) x Resistance value } (\Omega)}$	
Overload Voltage	Voltage should be 2.5 times the rated voltage.	
Resistance Tolerance	J : +/- 5%	
Resistance range	1.0 ~ 10 ohm (E-24)	

4. Explanation of Part Number

E	R	J	T	1	4	J	1	0	0	U
(1)			(2)			(3)	(4)			(5)

(1) Product Code : Thick Film Chip Resistor

(2) Size and Rated Power : 3.2 mm x 2.5 mm, 0.5W
T : Anti-Pulse type

(3) Resistance Tolerance

Code	Resistance Tolerance
J	+/- 5%

(4) Resistance Value
The first two digits are the significant figures of resistance value, and the last figure shows the number of zero following in ohm. And decimal point is expressed by “R”.

(5) Packaging Configuration

Code	Packaging Configuration
U	Taping (5000pcs/reel)

5. Appearance & Construction

Item	Rated value	Explanation
Appearance & Construction	1. The resistive element should be covered with protective coating that don't fade easily. The surface of coating should avoid unevenness, flaw, pinhole and discoloration. 2. The electrode should be printed uniformly, as shown in the dimensions. The plating should not fade easily, and should avoid unevenness, flaw, pinhole, projection and discoloration. 3. The electrode should be connected electrically, mechanically to resistive element. 4. Dimensions of the substrate should be as in the list and it should not have chipping, flaw, flash and crack. Details of appearance criteria shall be as described in attached sheet.	

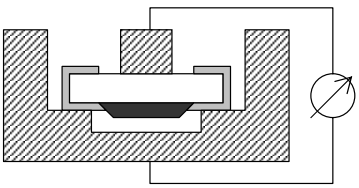
As far as there shall not designation especially, the following tests and measurement shall be operated under the following conditions.

Normal temperature : 5 deg.C to 35 deg.C

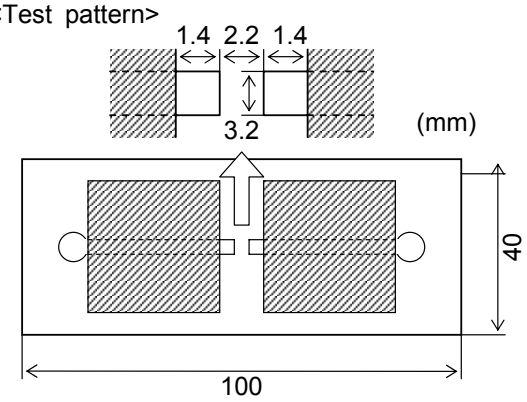
Normal humidity : 45 % to 85 %

Normal atmospheric pressure : 86 k Pa to 106 k Pa

6. Performance Specification

<table>
<tr><th>Item</th><th>Specification</th><th>Test Method (JIS-C5201-1)</th></tr>
<tr><td>DC resistance</td><td>DC resistance value shall be within the specified tolerance.</td><td>At 20 deg.C, 65%RH</td></tr>
<tr><td>Temperature coefficient of resistance (TCR)</td><td><table><tr><th>R (Ω)</th><th>TCR (x10^{-6}/°C)</th></tr><tr><td>< 10</td><td>-100 to +600</td></tr><tr><td>10</td><td>±200</td></tr></table></td><td>Natural resistance change per temperature degree centigrade.
TCR=(R_2-R_1)x10^6/R_1(t_2-t_1) (x10^{-6}/deg.C)
R_1 : Resistance value at reference temperature (t_1)
R_2 : Resistance value at test temperature (t_2)
t_1 : 25 deg.C , t_2 : 125 deg.C</td></tr>
<tr><td>Short time overload</td><td>ΔR : +/-(2%+0.1 ohm)</td><td>Resistors shall be applied 2.5 times the rated voltage for 5 seconds.</td></tr>
<tr><td>Intermittent overload</td><td>ΔR : +/-(5%+0.1 ohm)</td><td>Resistors shall be subjected to 10000 cycles of 2.5 times the rated voltage applied for 1 second with pause of 25 seconds between tests.</td></tr>
<tr><td>Dielectric Withstanding</td><td>No evidence of flashover, mechanical damage, arcing or insulation breakdown</td><td rowspan="2">AC 500V between substrate and termination for 1 minute.
 
AC power supply
or
Insulation resistance meter
Resistors shall be facing down.
After applying DC 500V to the resistor, insulation resistance shall be measured.</td></tr>
<tr><td>Insulation resistance</td><td>Min. 1000M ohm</td></tr>
<tr><td>Noise</td><td><table><tr><th>R. value</th><th>Noise</th></tr><tr><td>R≤ 10 ohm</td><td>-10dB(0.32μV/V)</td></tr></table>Less than upper value</td><td>Noise shall be measured by RESISTOR NOISE TEST SET MODEL 315C by Quan-Tech Div.</td></tr>
</table>

7. Machinery characteristic

Item	Specification	Test Method (JIS-C5201-1)
Bending strength	No mechanical damage.	Substrate : Glass epoxy (t=1.6 mm) Span : 90mm Bending distance : 2mm (10 seconds) <Test pattern> 
	ΔR : +/-(1%+0.05 ohm)	
Solderability	Termination should be covered uniformly with solder (Min. 95% coverage)	Resistors shall be dipped in the melted solder bath at 235 deg.C +/- 5 deg.C for 2 seconds +/- 0.5 second. Flux shall be removed from the surface of termination with clean organic solvent.
Resistance to soldering heat	ΔR : +/-(1%+0.05 ohm)	Resistors shall be dipped in the melted solder bath at 270 deg.C +/- 3 deg.C for 10 seconds +/- 1 second.
Resistance to vibration (Low frequency)	ΔR : +/-(1%+0.05 ohm)	Resistors shall be subjected to a single vibration having as double amplitude of 1.5 mm in 3 directions perpendicular one another for 2 hours each. (6 hours in total) The vibration frequency shall be varied uniformly from 10 Hz to 55 Hz, and return to 10 Hz traversing for 1 min.
Resistance to solvent	Without distinct deformation in appearance	Solvent solution : Isopropyl alcohol (1)Dipping 10 +/- 1 hours, dry in room condition for 30 +/- 10 minutes. (2)Ultrasonic wave washing : 5 +/- 1 min. ($0.3W/cm^2$,28kHz) Dry in room condition for 30 +/-10 minutes.
	ΔR : +/-(0.5% +0.05 ohm)	

8. Environmental test

Item	Specification	Test Method (JIS-C5201-1)
Low temperature exposure	ΔR : +/-(1%+0.05 ohm)	Resistors shall be exposed at -55 deg.C +/- 3 deg.C with no load for 1000 hours +48/-0 hours.
High temperature exposure	ΔR : +/-(1%+0.05 ohm)	Resistors shall be exposed at 125 deg.C +/- 3 deg.C with no load for 1000 hours +48/-0 hours.
Temperature cycling	ΔR : +/-(1%+0.05 ohm)	Resistors shall be tested for 5 cycles continuously in accordance with the following duty cycle.

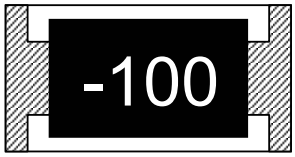
Step	Temperature (deg.C)	Time (min.)
1	-55 +/-3	30
2	Room temperature	Max. 3
3	+125 +/-3	30
4	Room temperature	Max.3

Humidity (Steady state)	ΔR : +/-(1%+0.05 ohm)	Resistors shall be exposed at 60 deg.C +/- 2 deg.C and 90% to 95% relative hummidity in a humidity test chamber for 1000 hours +48/-0 hours.
Load life	ΔR : +/-(3%+0.1 ohm)	Resistors shall be operated at DC rated voltage (1.5 hours "ON", 0.5 hours "OFF") for 1000 hours +48/-0 hours in a test chamber controlled at 70 deg.C +/-2 deg.C.
Load life in humidity	ΔR : +/-(3%+0.1 ohm)	Resistors shall be operated at DC rated voltage (1.5 hours “ON”, 0.5 hours “OFF”) for 1000 hours +48/-0 hours in a test chamber controlled at 60 deg.C +/- 2 deg.C and at 90% to 95% in relative hummidity.

9. Resistance value marking

Express resistance value on resin side with three digits.

“ - ” : Anti-Pulse type



<Example>

-100 = 10 ohm

-2R2 = 2.2 ohm

The first two digits should be significant figures of resistance for E-24 series and the third one denotes number of zeros.

Decimal point should be expressed by “R”.

10. Common precautions in handling resistors

⚠ Notice for use

(1) This specification shows the quality and performance of a unit component. Before adoption, be sure to evaluate and verify the product mounting it in your product.

(2) We take no responsibility for troubles caused by the product usage that is not specified in this specification.

(3) Use fail-safe design and ensure safety by carrying out the following items in cases where it is forecast that the failure of the product gives serious damage to something important like human life, for instant in traffic transportation equipment (trains, cars, traffic signal equipment, etc.), medical equipment, aerospace equipment, electric heating appliances, combustion and gas equipment, rotating equipment, disaster and crime preventive equipment.

*Ensure safety as the system by setting protective circuits and protective equipment.

*Ensure safety as the system by setting such redundant circuits as do not cause danger by a single failure.

(4) When a dogma shall be occurred about safety for this product, be sure to inform us rapidly, operate your technical examination.

(5) The product is designed to use in general standard applications of general electric equipment (AV products, household electric appliances, office equipment, information and communication equipment, etc.); hence, it do not take the use under the following special environments into consideration.

Accordingly, the use in the following special environments, and such environmental conditions may affect the performance of the product; prior to use, verify the performance, reliability, etc. thoroughly.

1) Use in liquids such as water, oil, chemical, and organic solvent.
2) Use under direct sunlight, in outdoor or in dusty atmospheres.
3) Use in places full of corrosive gases such as sea breeze, Cl_2, H_2S, NH_3, SO_2, and NO_X.
4) Use in environment with large static electricity or strong electromagnetic waves.
5) Where the product is close to a heating component, and where an inflammable such as a polyvinyl chloride wire is arranged close to the product.
6) Where the resistor is sealed or coated with resin, etc.
7) Where water or a water-soluble detergent is used in cleaning free soldering and in flux cleaning after soldering (Pay particular attention to soluble flux.)
8) Use in such a place where the product is wetted due to dew condensation.

(6) If transient load (heavy load in a short time) like pulse is expected to be applied, carry out evaluation and confirmation test with resistors actually mounted on your own board. When the load of more than rated power is applied under the load condition at steady state, it may impair performance and/or reliability of resistor. Never exceed the rated power. When the product shall be used under special condition, be sure to ask us in advance.

(7) Halogen type (Chlorine type, Bromine type, etc.) or other high-activity flux is not recommended as the residue may affect performance or reliability of resistors.

(8) When soldering with soldering iron, never touch the body of the chip resistor with a tip of the soldering iron. When using a soldering iron with a tip at high temperature, solder for a time as short as possible. (three seconds or less up to 350 deg.C)

(9) Avoid physical shock to the resistor and nipping of the resistor with hard tool (a pair of pliers or tweezers) as it may damage protective film or the body of resistor and may affect resistor's performance.

(10) Avoid immersion of chip resistor in solvent for long time. Use solvent after the effect of immersion is confirmed.

11. Storage Method

If the product is stored in the following environments and conditions, the performance and solderability may be badly affected, avoid the storage in the following environments.

(1) Storage in places full of corrosive gases such as sea breeze, Cl_2, H_2S, NH_3, SO_2, and NO_X.

(2) Storage in places exposed to direct sunlight.

(3) Storage in places outside the temperature range of 5 deg.C to 35 deg.C and humidity range of 45 %RH to 85 %RH.

(4) Storage over a year after our delivery (This item also applies to the case where the storage method specified in item (1) to (3) has been followed.).

12. Laws and Regulations

(1) This product has not been manufactured with any ozone-depleting chemical controlled under the Montreal Protocol.

(2) This product complies with the RoHS Directive (Restriction of the use of certain Hazardous Substances in electrical and electronic equipment (DIRECTIVE 2002/95/EC)).

(3) All materials used in this part are registered material under the Law Concerning the Examination and Regulation of Manufacturs, etc. of Chemical substances.

(4) All the materials used in this part contain no brominated materials of $PBBO_S$ or PBB_S as the flame-retardant.

(5) If you need the notice by letter of "A preliminary judgement on the Laws of Japan foreign exchange and Foreign Trade control", be sure to let us know.

13. Production site

Country : Japan

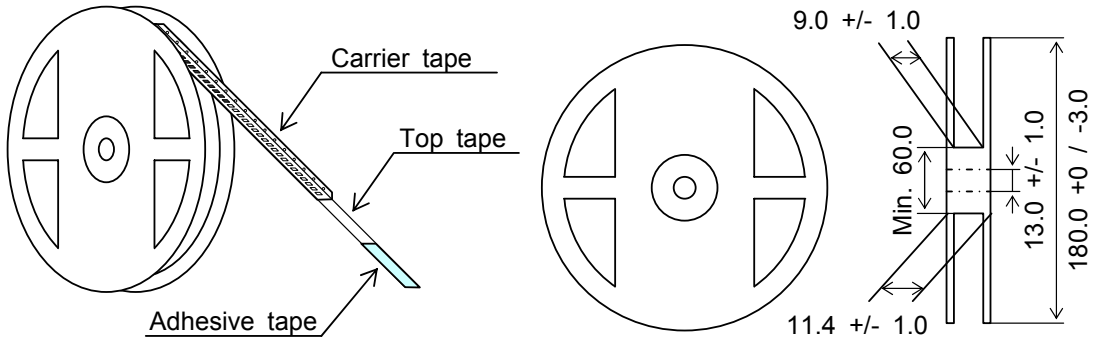
Plant : Panasonic Electronic Devices Fukui Co., Ltd.

14. Taped and Reel Package

14-1 Physical Dimensions

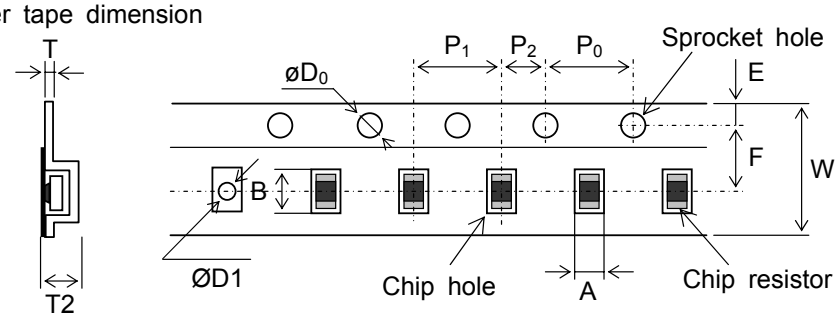
Structure and reel dimensions shall be as shown in the figure below.

Inaccordance with EIAJ ET-7200.



Unit : mm

14-2 Carrier tape dimension



	A	B	W	F	E	P1
Dimension (mm)	2.80+/-0.20	3.50+/-0.20	8.00+/-0.30	3.50+/-0.05	1.75+/-0.10	4.00+/-0.10

	P2	P0	øD0	T	øD1	T2
Dimension (mm)	2.00+/-0.05	4.00+/-0.10	1.50+0.10/-0	Max. 0.40	1.00+0.10/-0	1.00+/-0.10

14-3 Specifications

14-3-1 Taping

(1) Minimum Bending Radius

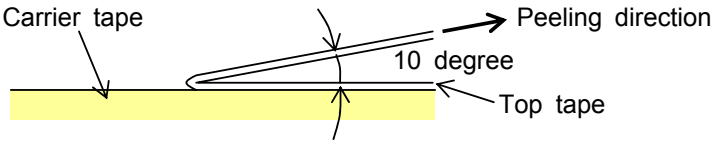
When Carrier tape shall be bent by Minimum Bending Radius (15mm), no defection of chip and no break of carrier tape. However minimum bending radius shall be tested for 1 time.

(2) Resistance to climate of top tape

When it shall be exposed at 60 deg.C, 90 to 95 %RH for 120 hours, no exfoliation of top tape.

(3) When the test shall be operated with the below conditions, peel strength should be 0.098 N to 0.686 N, should not have flash and tear after peeling.

(Test Method)



14-3-2 Quantity in Taping : 5000 pcs./reel

14-3-3 Tape packaging

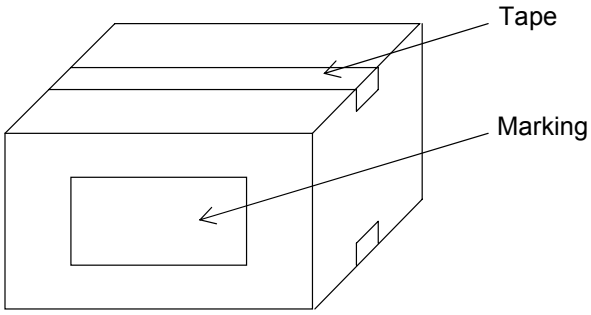
(1) Resistance side shall be facing upward.

(2) Chip resistor shall not be sticking to top tape and bottom tape.

(3) Chip resistor shall be easy to take out from carrier tape and chip hole or sprocket hole shall not have flash and break.

14-4 Outer Packaging

Quantity : 20 reels (Max. 100,000pcs.)



* When taping shall not reach Max. or quantity, the remaining empty space shall be buried with buffer material.

* When the quantity shall be few, alternative packaging methods may be used. No problem must occur during the exportation of the product.

14-5 Marking

At last, production country is displayed in English.

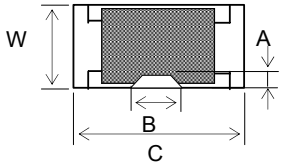
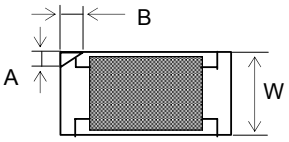
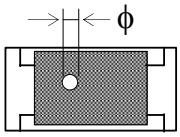
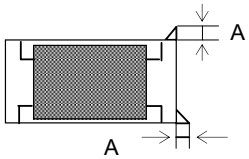
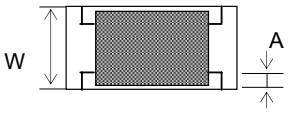
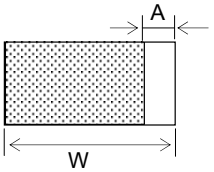
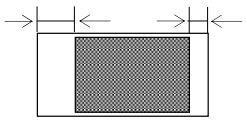
(1) Side of reel (Marking shall be on one side)

1)Part name, 2)Part number, 3)Quantity, 4)Lot number, 5)Maker name
6)Poduction country

(2)Packaging box

1)Customer name, 2)Part name, 3)Part number, 4)Customer part number, 5)Quantity
6)Maker name, 7) Poduction country

15. Appearance Quality Criteria

Item	Figure	Appearance quality criteria	Remark
Protective coating Chipping		A≤W/4 B≤C/2	Chipping on both sides shall be considered defective
Terminal Chipping		A≤W/4 B≤Terminal width	
Pin hole		One pin hole / chip resistor ϕ ≤ 0.2 mm	Pin hole penetrates The resistive material.
Flash		A≤0.1 mm	
Top terminal Lacking		A≤W/4	
Side terminal Lacking		A≤W/4	
Protective coating and terminal aberration		Protective coating and terminal aberration shall be within the terminal width dimension	
Marking	———————	Marking must be readable.	