# 1 A low V<sub>F</sub> dual MEGA Schottky barrier rectifier Rev. 1 — 24 August 2010 Pr

**Product data sheet** 

### **Product profile**

#### 1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier in common cathode configuration with an integrated guard ring for stress protection, encapsulated in a SOT1061 leadless small Surface-Mounted Device (SMD) plastic package with medium power capability.

#### 1.2 Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 1 A
- Reverse voltage: V<sub>R</sub> ≤ 40 V
- Low forward voltage
- Exposed heat sink (cathode pad) for excellent thermal and electrical conductivity
- Leadless small SMD plastic package with medium power capability
- AEC-Q101 qualified

#### 1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications
- Battery chargers for mobile equipment

#### 1.4 Quick reference data

Quick reference data Table 1.  $T_i = 25$  °C unless otherwise specified.

	<u> </u>					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per diode						
I <sub>F(AV)</sub>	average forward current	square wave; $\delta$ = 0.5; f = 20 kHz				
		T <sub>amb</sub> ≤ 110 °C	<u>[1]</u> _	-	1	Α
		T <sub>sp</sub> ≤ 140 °C	-	-	1	Α
$V_R$	reverse voltage		-	-	40	V
$V_{F}$	forward voltage	I <sub>F</sub> = 1 A	-	440	500	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 40 V	-	12	55	μΑ

<sup>[1]</sup> Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.



### 2. Pinning information

Table 2. Pinning

IUDIC Z.	i iiiiiiig		
Pin	Description	Simplified outline	Graphic symbol
1	anode diode 1		
2	anode diode 2	3	3
3	common cathode	1 2 Transparent top view	1 2 006aaa438

### 3. Ordering information

Table 3. Ordering information

Type number	Package	Package				
	Name	Description	Version			
PMEG4010CPA	HUSON3	plastic thermal enhanced ultra thin small outline package; no leads; three terminals; body 2 $\times$ 2 $\times$ 0.65 mm	SOT1061			

### 4. Marking

Table 4. Marking codes

Type number	Marking code
PMEG4010CPA	AN

### 5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Per diode					
$V_R$	reverse voltage	$T_j \le 25  ^{\circ}C$	-	40	V
$I_{F(AV)}$	average forward current	square wave; $\delta$ = 0.5; f = 20 kHz			
		T <sub>amb</sub> ≤ 110 °C	<u>[1]</u> _	1	Α
		T <sub>sp</sub> ≤ 140 °C	-	1	Α
I <sub>FRM</sub>	repetitive peak forward current	$t_p \leq 1 \text{ ms; } \delta \leq 0.25$	-	7	Α
I <sub>FSM</sub>	non-repetitive peak forward current	square wave; t <sub>p</sub> = 8 ms	[2] _	9	Α

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Table 5. Limiting values ... continued

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Per device,	one diode loaded				
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25  ^{\circ}C$	[3][4]	500	mW
			[3][5]	960	mW
			[1][3]	1800	mW
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		<b>–55</b>	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

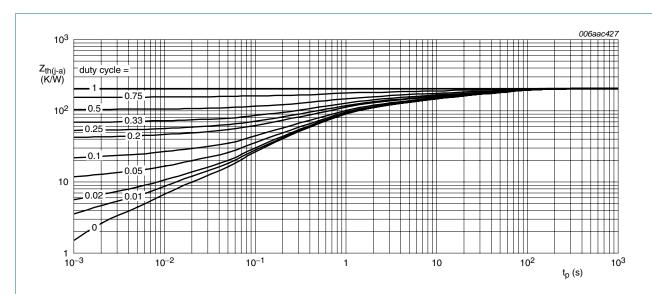
- [1] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [2]  $T_i = 25$  °C prior to surge.
- [3] Reflow soldering is the only recommended soldering method.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [5] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

### 6. Thermal characteristics

Table 6. Thermal characteristics

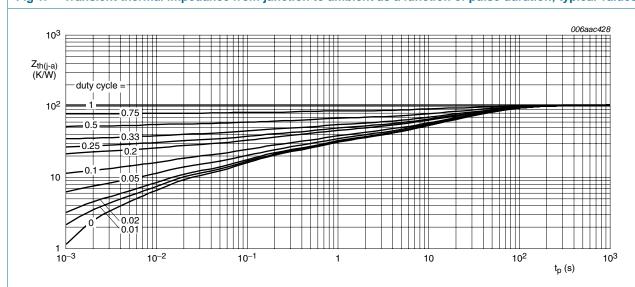
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per device	e, one diode loaded					
R <sub>th(j-a)</sub>	thermal resistance from	in free air	[1][2]			
	junction to ambient		[3] _	-	250	K/W
			[4] _	-	130	K/W
			[5] _	-	70	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		<u>[6]</u> _	-	12	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.
- [2] Reflow soldering is the only recommended soldering method.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [5] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [6] Soldering point of cathode tab.



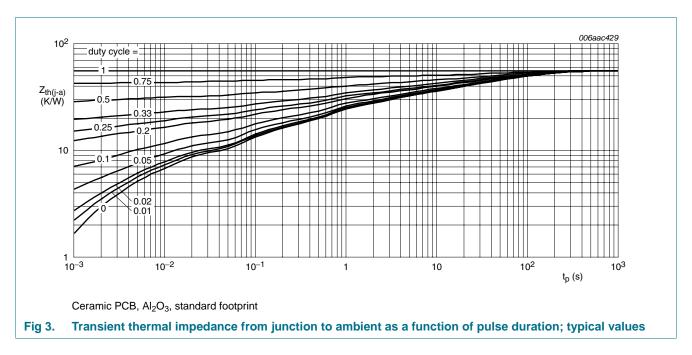
FR4 PCB, standard footprint

Fig 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

Fig 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



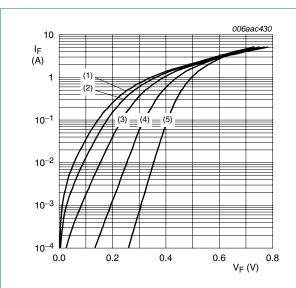
### 7. Characteristics

 Table 7.
 Characteristics

 $T_i = 25$  °C unless otherwise specified.

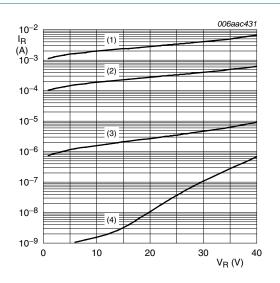
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per diode						
V <sub>F</sub> forward voltage	$I_F = 100 \text{ mA}$	-	320	-	mV	
	I <sub>F</sub> = 1 A	-	440	500	mV	
I <sub>R</sub>	reverse current	$V_R = 10 V$	-	2.5	-	μΑ
		V <sub>R</sub> = 40 V	-	12	55	μΑ
$C_d$	diode capacitance	f = 1 MHz				
		$V_R = 1 V$	-	130	-	pF
		V <sub>R</sub> = 10 V	-	50	-	pF
t <sub>rr</sub>	reverse recovery time	)	<u>[1]</u> -	45	-	ns

<sup>[1]</sup> When switched from  $I_F$  = 10 mA to  $I_R$  = 10 mA;  $R_L$  = 100  $\Omega$ ; measured at  $I_R$  = 1 mA.



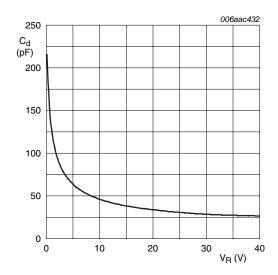
- (1)  $T_i = 150 \,^{\circ}\text{C}$
- (2)  $T_i = 125 \, ^{\circ}\text{C}$
- (3)  $T_i = 85 \, ^{\circ}C$
- (4)  $T_j = 25 \, ^{\circ}C$
- (5)  $T_i = -40 \, ^{\circ}C$

Fig 4. Forward current as a function of forward voltage; typical values



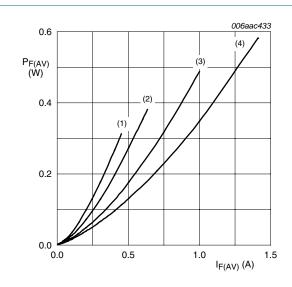
- (1)  $T_j = 125 \, ^{\circ}C$
- (2)  $T_j = 85 \, ^{\circ}C$
- (3)  $T_j = 25 \, ^{\circ}C$
- (4)  $T_j = -40 \, ^{\circ}C$

Fig 5. Reverse current as a function of reverse voltage; typical values



f = 1 MHz; T<sub>amb</sub> = 25 °C

Fig 6. Diode capacitance as a function of reverse voltage; typical values



T<sub>i</sub> = 150 °C

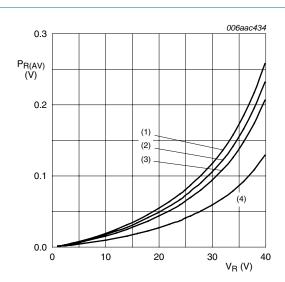
(1)  $\delta = 0.1$ 

(2)  $\delta = 0.2$ 

(3)  $\delta = 0.5$ 

(4)  $\delta = 1$ 

Fig 7. Average forward power dissipation as a function of average forward current; typical values



T<sub>i</sub> = 125 °C

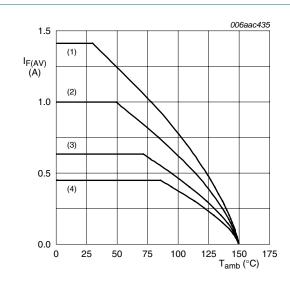
(1)  $\delta = 1$ 

(2)  $\delta = 0.9$ 

(3)  $\delta = 0.8$ 

(4)  $\delta = 0.5$ 

Fig 8. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

T<sub>i</sub> = 150 °C

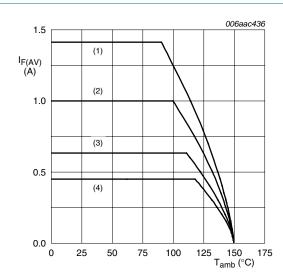
(1)  $\delta = 1$ ; DC

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig 9. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1  $\mbox{cm}^2$ 

T<sub>j</sub> = 150 °C

(1)  $\delta = 1$ ; DC

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

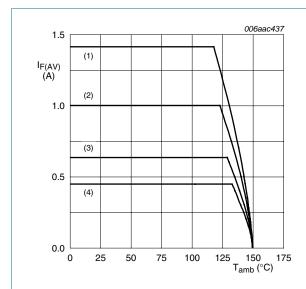
(4)  $\delta = 0.1$ ; f = 20 kHz

Fig 10. Average forward current as a function of ambient temperature; typical values

PMEG4010CPA

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Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

T<sub>i</sub> = 150 °C

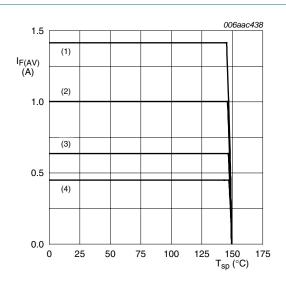
(1)  $\delta = 1$ ; DC

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig 11. Average forward current as a function of ambient temperature; typical values



T<sub>j</sub> = 150 °C

(1)  $\delta = 1$ ; DC

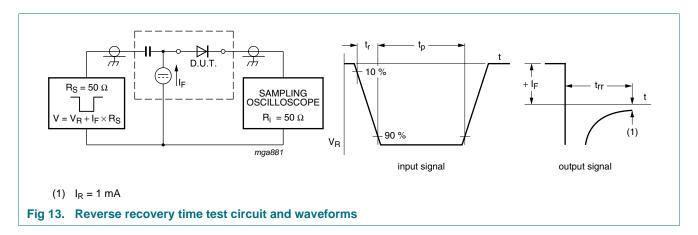
(2)  $\delta = 0.5$ ; f = 20 kHz

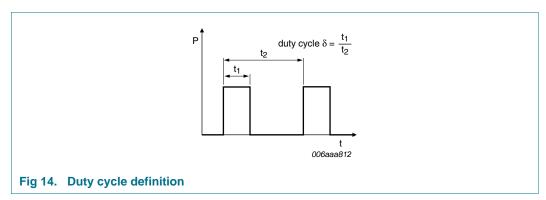
(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig 12. Average forward current as a function of solder point temperature; typical values

### 8. Test information



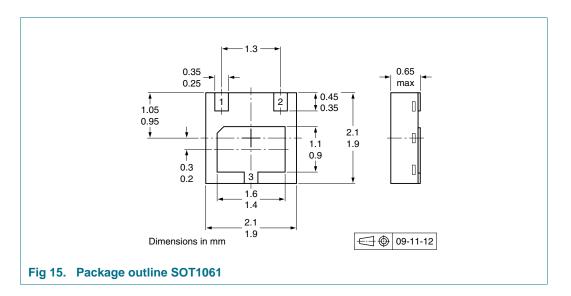


The current ratings for the typical waveforms as shown in Figure 9, 10, 11 and 12 are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

#### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

### 9. Package outline



### 10. Packing information

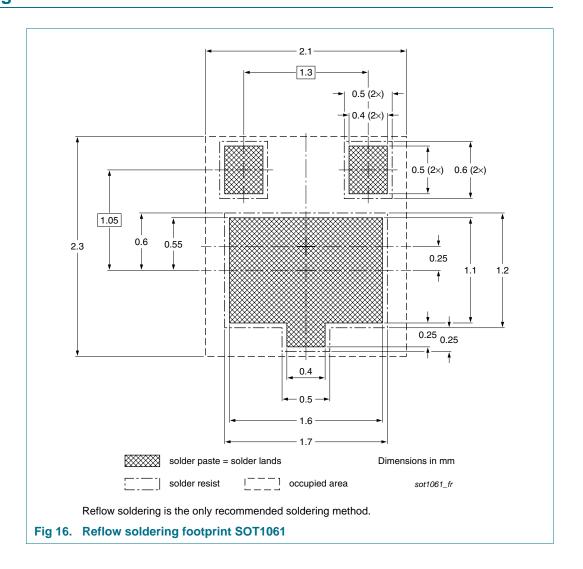
Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing quantity
			3000
PMEG4010CPA	SOT1061	4 mm pitch, 8 mm tape and reel	-115

[1] For further information and the availability of packing methods, see Section 14.

### 11. Soldering



1 A low V<sub>F</sub> dual MEGA Schottky barrier rectifier

### 12. Revision history

#### Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG4010CPA v.1	20100824	Product data sheet	-	-

### 13. Legal information

#### 13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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#### 1 A low V<sub>F</sub> dual MEGA Schottky barrier rectifier

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

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