

# BDW93C BDW94B/BDW94C

# COMPLEMENTARY SILICON POWER DARLINGTON TRANSISTORS

- STMicroelectronics PREFERRED . SALESTYPES
- COMPLEMENTARY PNP NPN DEVICES
- INTEGRATED ANTIPARALLEL . COLLECTOR-EMITTER DIODE

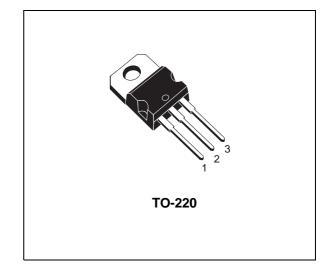
## **APPLICATIONS**

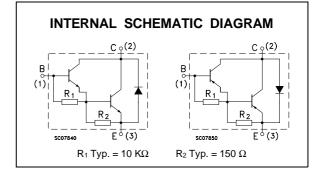
LINEAR AND SWITCHING INDUSTRIAL EQUIPMENT

### DESCRIPTION

The BDW93C is a silicon Epitaxial-Base NPN power transistor in monolithic Darlington configuration mounted in Jedec TO-220 plastic package. It is intented for use in power linear and switching applications.

The complementary PNP type is BDW94C. Also BDW94B is a PNP type.





## **ABSOLUTE MAXIMUM RATINGS**

Symbol	Parameter		Value		
		NPN		BDW93C	
		PNP	BDW94B BD	BDW94C	
V <sub>CBO</sub>	Collector-Base Voltage ( $I_E = 0$ )		80	100	V
V <sub>CEO</sub>	Collector-Emitter Voltage $(I_B = 0)$		80	100	V
Ιc	Collector Current		12		Α
I <sub>CM</sub>	Collector Peak Current		15		А
Ι <sub>Β</sub>	Base Current		0.2		Α
Ptot	Total Dissipation at $T_c \le 25$ °C		80		W
T <sub>stg</sub>	Storage Temperature		-65 to 150		°C
Tj	Max. Operating Junction Temperature	150		°C	

For PNP types voltage and current values are negative.

#### October 1999

# THERMAL DATA

Rthj-case Thermal Resistance Junction-case	1.56	°C/W
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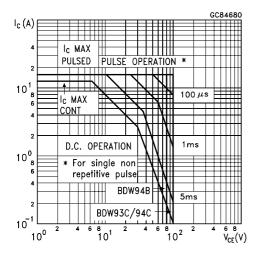
# **ELECTRICAL CHARACTERISTICS** ( $T_{case} = 25 \ ^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Test Con	Min.	Тур.	Max.	Unit	
I <sub>СВО</sub>	Collector Cut-off Current (I <sub>E</sub> = 0)	for <b>BDW94B</b> for <b>BDW93C/94C</b> $T_{case} = 150 ^{\circ}C$ for <b>BDW94B</b> for <b>BDW93C/94C</b>	$V_{CB} = 80 V$ $V_{CB} = 100 V$ $V_{CB} = 80 V$ $V_{CB} = 100 V$			100 100 5 5	μΑ μΑ mA mA
I <sub>CEO</sub>	Collector Cut-off Current (I <sub>B</sub> = 0)	for BDW94B for BDW93C/94C	$V_{CE} = 80 V$ $V_{CE} = 100 V$			1	mA mA
I <sub>EBO</sub>	Emitter Cut-off Current $(I_{C} = 0)$	V <sub>EB</sub> = 5 V				2	mA
VCEO(sus)*	Collector-Emitter Sustaining Voltage (I <sub>B</sub> = 0)	Ic = 100 mA for <b>BDW94B</b> for <b>BDW93C/94C</b>		80 100			V V
V <sub>CE(sat)</sub> *	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 5 A I <sub>C</sub> = 10 A	I <sub>B</sub> = 20 mA I <sub>B</sub> = 100 mA			2 3	V V
V <sub>BE(sat)</sub> *	Base-Emitter Saturation Voltage	I <sub>C</sub> = 5 A I <sub>C</sub> = 10 A	I <sub>B</sub> = 20 mA I <sub>B</sub> = 100 mA			2.5 4	V V
h <sub>FE</sub> *	DC Current Gain	$I_{C} = 3 A$ $I_{C} = 5 A$ $I_{C} = 10 A$	V <sub>CE</sub> = 3 V V <sub>CE</sub> = 3 V V <sub>CE</sub> = 3 V	1000 750 100		20K	
V <sub>F</sub> *	Parallel-diode Forward Voltage	I <sub>F</sub> = 5 A I <sub>F</sub> = 10 A			1.3 1.8	2 4	V V
h <sub>fe</sub>	Small Signal Current Gain	I <sub>C</sub> = 1 A f = 1 MHz	$V_{CE} = 10 V$	20			

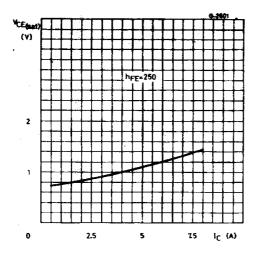
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\* Pulsed: Pulse duration =  $300 \ \mu$ s, duty cycle 1.5 % For PNP types voltage and current values are negative.

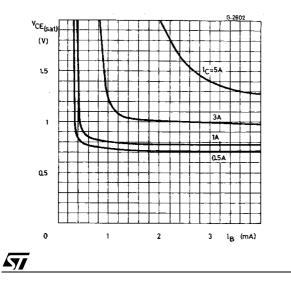
## Safe Operating Area



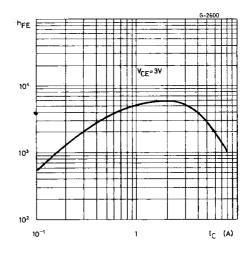
Collector Emitter Saturation Voltage (NPN types)



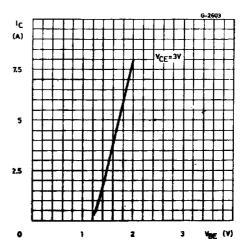
Collector Emitter Saturation Voltage (NPN types)



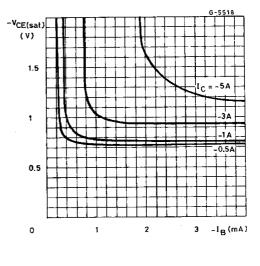
DC Current Gain (NPN types)



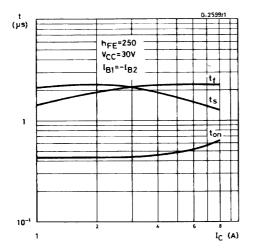
DC Transconductance (NPN types)



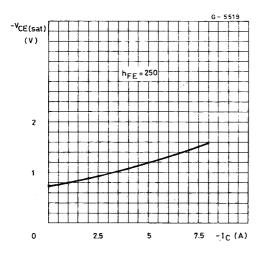
Collector Emitter Saturation Voltage (PNP types)



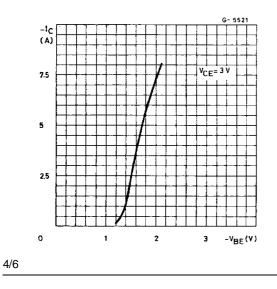
Saturated Switching Characteristics (NPN types)



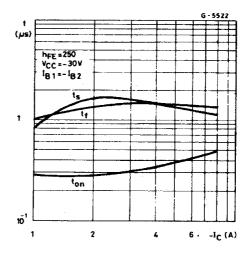
Collector Emitter Saturation Voltage (PNP types)

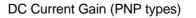


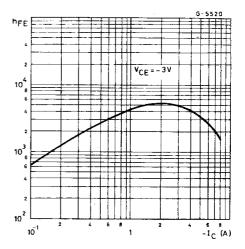
DC Transconductance (PNP types)



Saturated Switching Characteristics (PNP types)



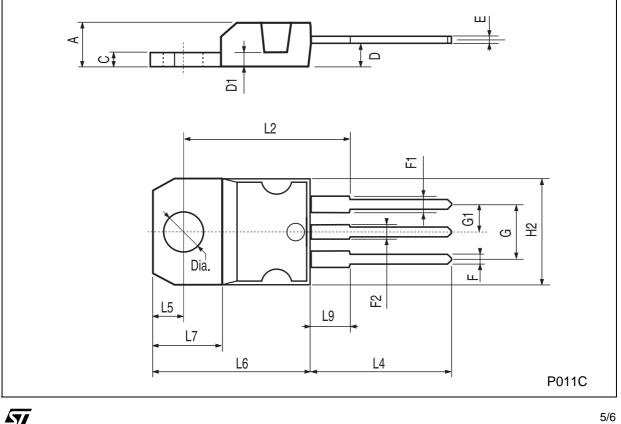






DIM.	mm			inch			
DIN.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
А	4.40		4.60	0.173		0.181	
С	1.23		1.32	0.048		0.051	
D	2.40		2.72	0.094		0.107	
D1		1.27			0.050		
E	0.49		0.70	0.019		0.027	
F	0.61		0.88	0.024		0.034	
F1	1.14		1.70	0.044		0.067	
F2	1.14		1.70	0.044		0.067	
G	4.95		5.15	0.194		0.203	
G1	2.4		2.7	0.094		0.106	
H2	10.0		10.40	0.393		0.409	
L2		16.4			0.645		
L4	13.0		14.0	0.511		0.551	
L5	2.65		2.95	0.104		0.116	
L6	15.25		15.75	0.600		0.620	
L7	6.2		6.6	0.244		0.260	
L9	3.5		3.93	0.137		0.154	
DIA.	3.75		3.85	0.147		0.151	

# **TO-220 MECHANICAL DATA**



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