

### SBR2A30P1

2.0A SBR<sup>®</sup> SURFACE MOUNT SUPER BARRIER RECTIFIER *PowerDI*<sup>®</sup> 123

#### **Features**

- Low Forward Voltage Drop
- Low Leakage Current
- Superior Reverse Avalanche Capability
- Excellent High Temperature Stability
- Patented Interlocking Clip Design for High Surge Current Capacity
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150°C Operating Junction Temperature
- ±16KV ESD Protection (HBM, 3B)
- ±25KV ESD Protection (IEC61000-4-2 Level 4, Air Discharge)
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

### Mechanical Data

- Case: PowerDl<sup>®</sup>123
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Polarity Indicator: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 🕄
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.018 grams (approximate)



Top View

### Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>RM</sub>	30	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	21	V
Average Rectified Output Current (See Figure 1)	lo	2.0	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	75	A

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance Thermal Resistance Junction to Soldering (Note 2) Thermal Resistance Junction to Ambient (Note 3) Thermal Resistance Junction to Ambient (Note 4)	R <sub>θ</sub> js R <sub>θ</sub> ja R <sub>θ</sub> ja	5 175 100	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

Notes: 1. RoHS revision 13.2.2003. High temperature solder exemption applied, see *EU Directive Annex Note 7*.

2. Theoretical R<sub>eus</sub> calculated from the top center of the die straight down to the PCB cathode tab solder junction.

3. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.

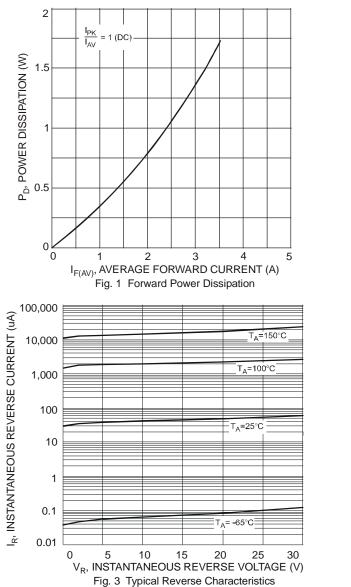
4. Polymide PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.



# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 5)	V <sub>(BR)R</sub>	30	-	-	V	I <sub>R</sub> = 250μA
Forward Voltage Drop	VF	- - - - -	0.23 0.34 0.40 0.50 0.13 0.275	0.28 0.39 0.45 - 0.19 0.33	V	$\begin{split} I_F &= 0.1A, \ T_J = 25^{\circ}C \\ I_F &= 1.0A, \ T_J = 25^{\circ}C \\ I_F &= 2.0A, \ T_J = 25^{\circ}C \\ I_F &= 4.0A, \ T_J = 125^{\circ}C \\ I_F &= 0.1A, \ T_J = 125^{\circ}C \\ I_F &= 1.0A, \ T_J = 125^{\circ}C \\ \end{split}$
Leakage Current (Note 5)	I <sub>R</sub>	-	50 55 5 7	100 200 10 15	μΑ μΑ mA mA	

Notes: 5. Short duration pulse test used to minimize self-heating effect.



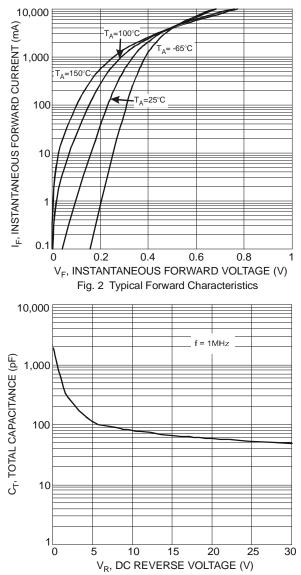
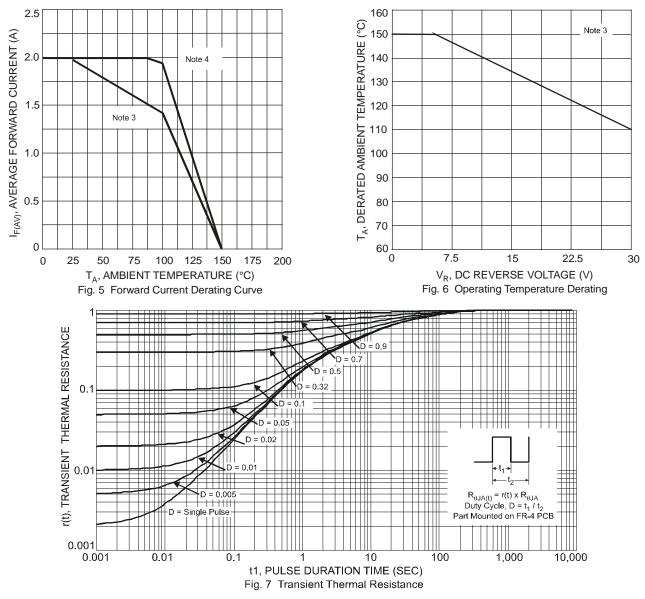


Fig. 4 Total Capacitance vs. Reverse Voltage

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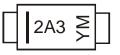


## Ordering Information (Note 6)

Part Number	Case	Packaging
SBR2A30P1-7	PowerDl <sup>®</sup> 123	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

### **Marking Information**



2A3 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: T = 2006)M = Month (ex: 9 = September)

Date Code Kev

Year	2006	2007	20	08	2009	2010	2011	2012	20	)13	2014	2015
Code	Т	U	,	V	W	Х	Y	Z	1	Ą	В	С
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec

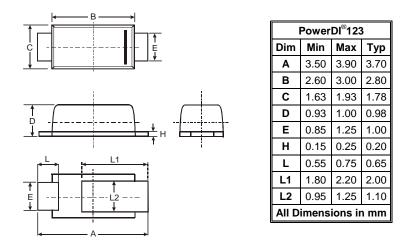
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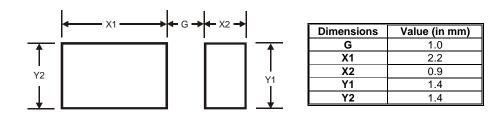
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### **Package Outline Dimensions**



## Suggested Pad Layout



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