



IQS7223 OVERVIEW

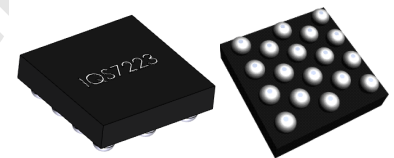
WearMax™ Capacitive Touch and Proximity Controller for accurate long-term and power-on presence detection with I2C communications interface, configurable GPIOs and low power options

1 Device Overview

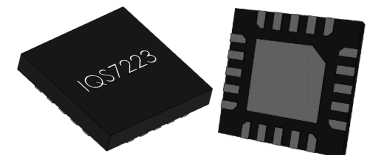
The IQS7223 ProxFusion® IC is a sensor fusion device for various long-term wear or presence detection purposes. The sensor is fully I²C compatible and on-chip calculations enable the IC to respond effectively even in lowest power modes.

1.1 Main Features

- > Highly flexible ProxFusion® device
- > 9 sensor pad connections (QFN-20) / 8 sensor pad connections (WLCSP-18)
- > Dedicated WearMax™ Sensor implementation for optimal wear / presence detection
- > Built-in basic functions:
 - Dedicated Intelligent Wear Output
 - Differential measurements (Advanced reference channel capabilities)
 - Automatic tuning
 - Noise filtering
 - Debounce & hysteresis
- > Design simplicity
 - PC Software for debugging and obtaining optimal settings and performance
 - One-time programmable settings for custom power-on IC configuration
 - Auto-run from programmed settings for simplified integration
- > Automated system power modes for optimal response vs consumption
- > I2C communication interface with IRQ/RDY(up to fast plus -1MHz)
- > Event and streaming modes
- > Customizable user interface due to programmable memory
- > Supply Voltage 1.8V(-5%)to 3.5V
- > Small packages
 - WLCSP18 (1.62 x 1.62 x 0.5 mm) - interleaved 0.4mm x 0.6mm ball pitch
 - QFN20 (3 x 3 x 0.5 mm) - 0.4mm pitch



WLCSP18 & QFN20
package
Representation only



1.2 Applications

- > Fitness band & Smartwatch Wear Detection
- > Headphone Wear Detection
- > TWS Earbud Wear Detection



1.3 Block Diagram

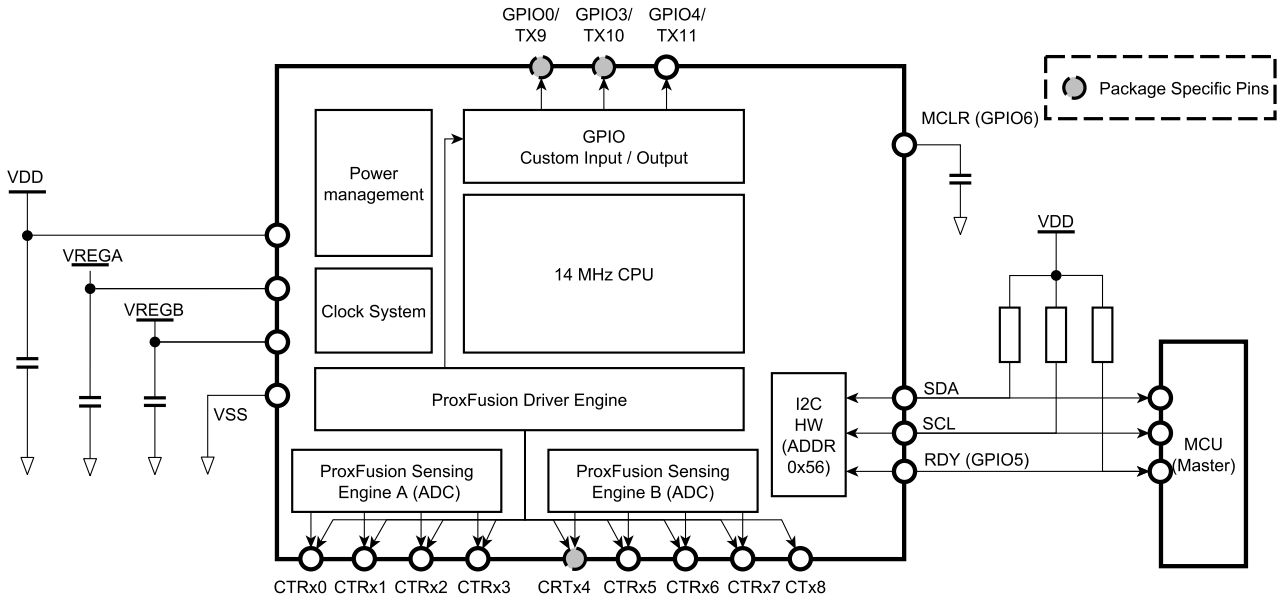


Figure 1.1: Functional Block Diagram

Preliminary

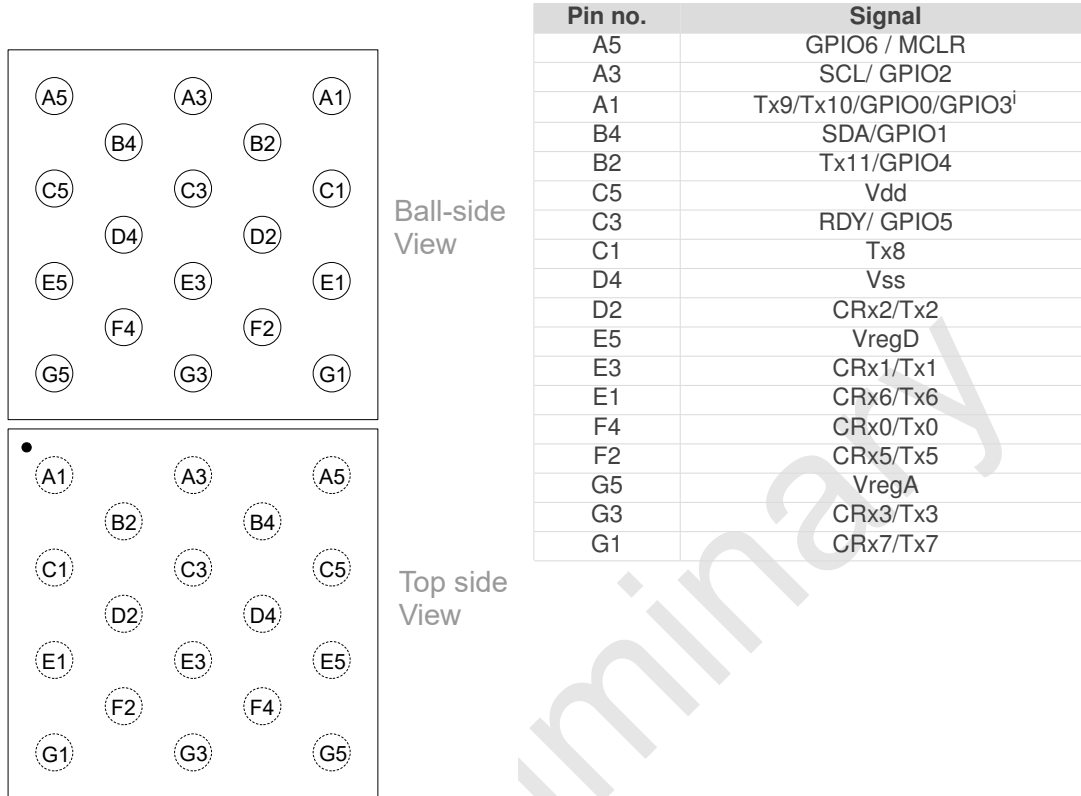
ⁱWLCSP18 packages do not have a CRX4 and combines GPIO0 and GPIO3



2 Hardware Connection

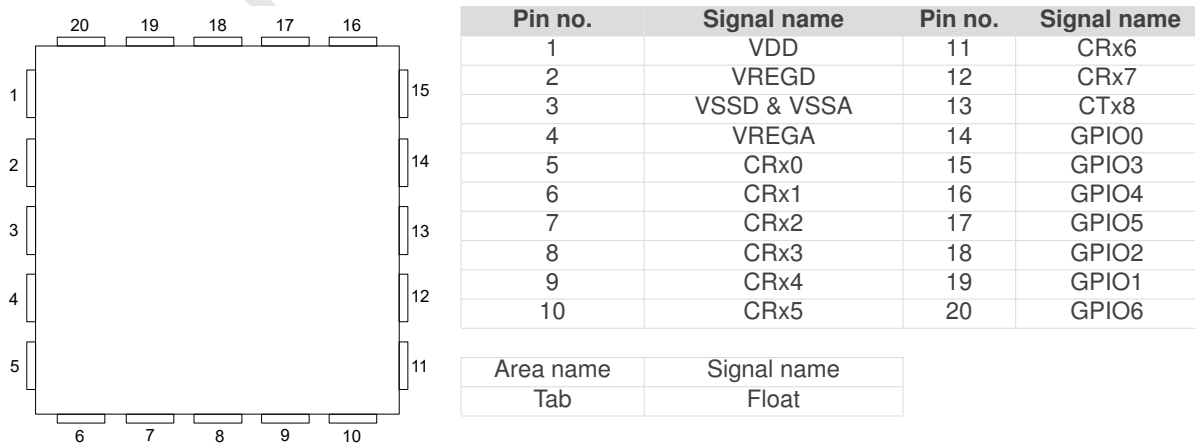
2.1 WLCSP18 Pin Diagrams

Table 2.1: 18-pin WLCSP18 Package (Bottom/Ball-side View)



2.2 QFN20 Pin Diagram

Table 2.2: 20-pin QFN Package (Top View)



ⁱPlease note that CTx9 and CTx10 are shorted in the WLCSP18 package



2.3 Reference Schematic

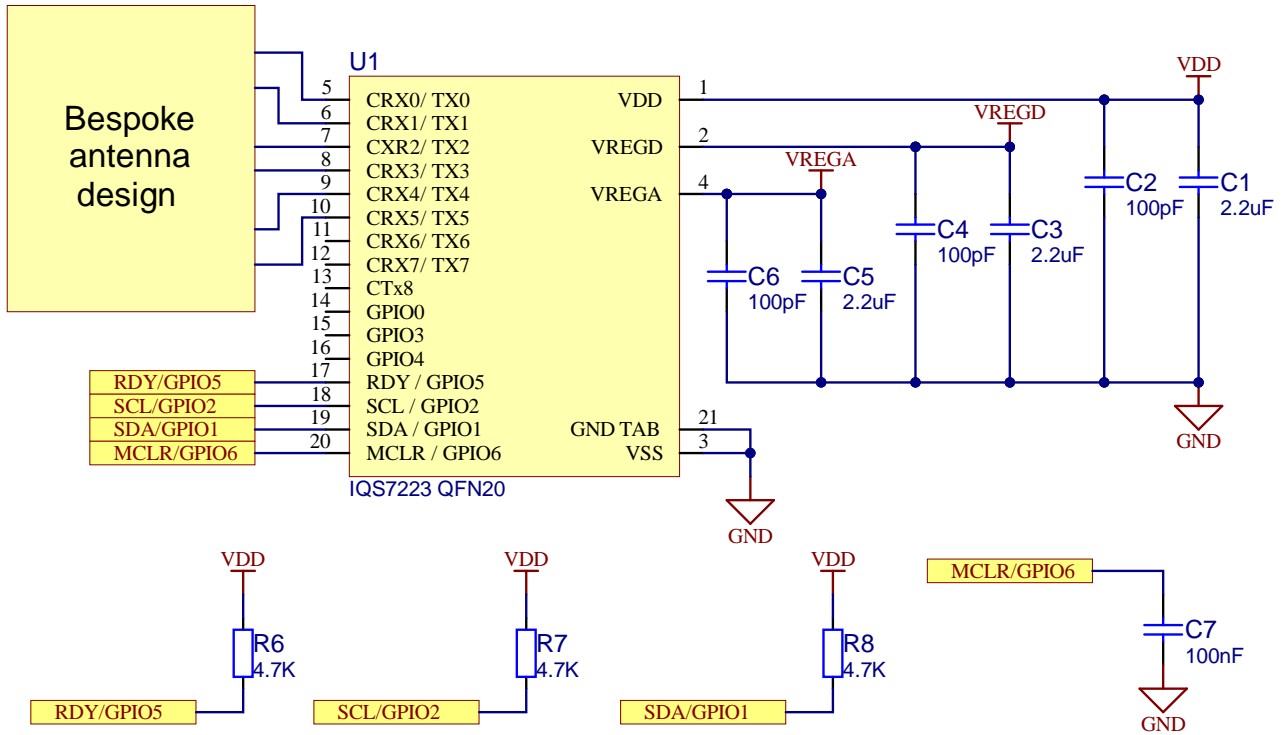


Figure 2.1: IQS7223 Reference Schematic

Preliminary



3 Electrical Characteristics

3.1 Absolute Maximum Ratings

	Min	Max	Unit
Voltage applied at VDD pin to VSS	1.71	3.5	V
Voltage applied to any ProxFusion® pin	-0.3	VREG	V
Voltage applied to any other pin (referenced to VSS)	-0.3	VDD + 0.3 (3.5V max)	V
Storage temperature, T _{stg}	-40	85	°C

3.2 ESD Rating

	Value	Unit
V _(ESD) Electrostatic discharge Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001 ⁱ	± 4000	V

3.3 Recommended Operating Conditions

Recommended operating conditions		Min	Nom	Max	Unit
VDD	Supply voltage applied at VDD pin	1.71		3.5	V
VregA	Analog-domain Regulator output at VregA	1.5	1.53	1.75	V
VregD	Digital-domain Regulator output at VregD	1.57	1.59	1.8 ⁱⁱ	V
VSS	Supply voltage applied at VSS pin	0	0	0	V
T _A	Operating free-air temperature	-40	25	85	°C
C _{VDD}	Recommended capacitor at VDD	1	2	10	μF
C _{VREGA}	Recommended external buffer capacitor at VREG, ESR ≤ 200mΩ	1	2	10	μF
C _{VREGD}	Recommended external buffer capacitor at VREG, ESR ≤ 200mΩ	1	2	10	μF
C _{X_SELF-VSS}	Maximum capacitance of all external electrodes on all ProxFusion® blocks (self-capacitance mode)	-	-	400	pF
C _{m_CTX-CRX}	Capacitance of all external electrodes on all ProxFusion® blocks (mutual-cap mode)	0.1	-	90	pF
C _{X_CRX-VSS-1M}	Maximum capacitance of all external electrodes on all ProxFusion® blocks (mutual-capacitance mode @ f _{xfer} =1MHz)			100	pF
C _{X_CRX-VSS-4M}	Maximum capacitance of all external electrodes on all ProxFusion® blocks (mutual-capacitance mode @ f _{xfer} =4MHz sensing)			25	pF
$\frac{C_{X_{CRX-VSS}}}{C_{m_{CTX-CRX}}}$	Capacitance ratio for optimal SNR in mutual capacitance mode	10		20	n/a
RC _{X_CRX/CTX}	Series (in-line) resistance of all mutual capacitance pins (Tx & Rx pins) in mutual capacitance mode	0 ⁱⁱⁱ	0.47	10 ^{iv}	kΩ
RC _{X_SELF}	Series (in-line) resistance of all self capacitance pins in self capacitance mode	0 ⁱⁱ	0.47	10 ^{iv}	kΩ

ⁱ JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process. Pins listed as ±4000 V may actually have higher performance.

ⁱⁱ V_{dd} ≥ 2V

ⁱⁱⁱ Nominal series resistance of 470Ω is recommended to prevent received and emitted EMI effects. Typical resistance also adds additional ESD protection

^{iv} Series resistance limit is a function of f_{xfer} and the circuit time constant, RC. R_{max} × C_{max} = $\frac{1}{(6 \times f_{xfer})}$ where "C" is the pin capacitance to Vss.



4 Ordering Information

IQS7223 y zzz ppb

IC NAME	IQS7223	=	IQS7223	
MINOR PRODUCT REVISION	y	=	A	Default
POWER-ON CONFIGURATION	zzz	=	000	I ² C with initialize settings requirement
PACKAGE TYPE	pp	=	CS	WLCSP-18 package
		=	QN	QFN-20 package
BULK PACKAGING	b	=	R	WLCSP-18 Reel (3000pcs/reel)
				QFN-20 Reel (2000pcs/reel)

Figure 4.1: Order Code Description

Preliminary

5 Package Specification

5.1 Package Outline Description - WLCSP18

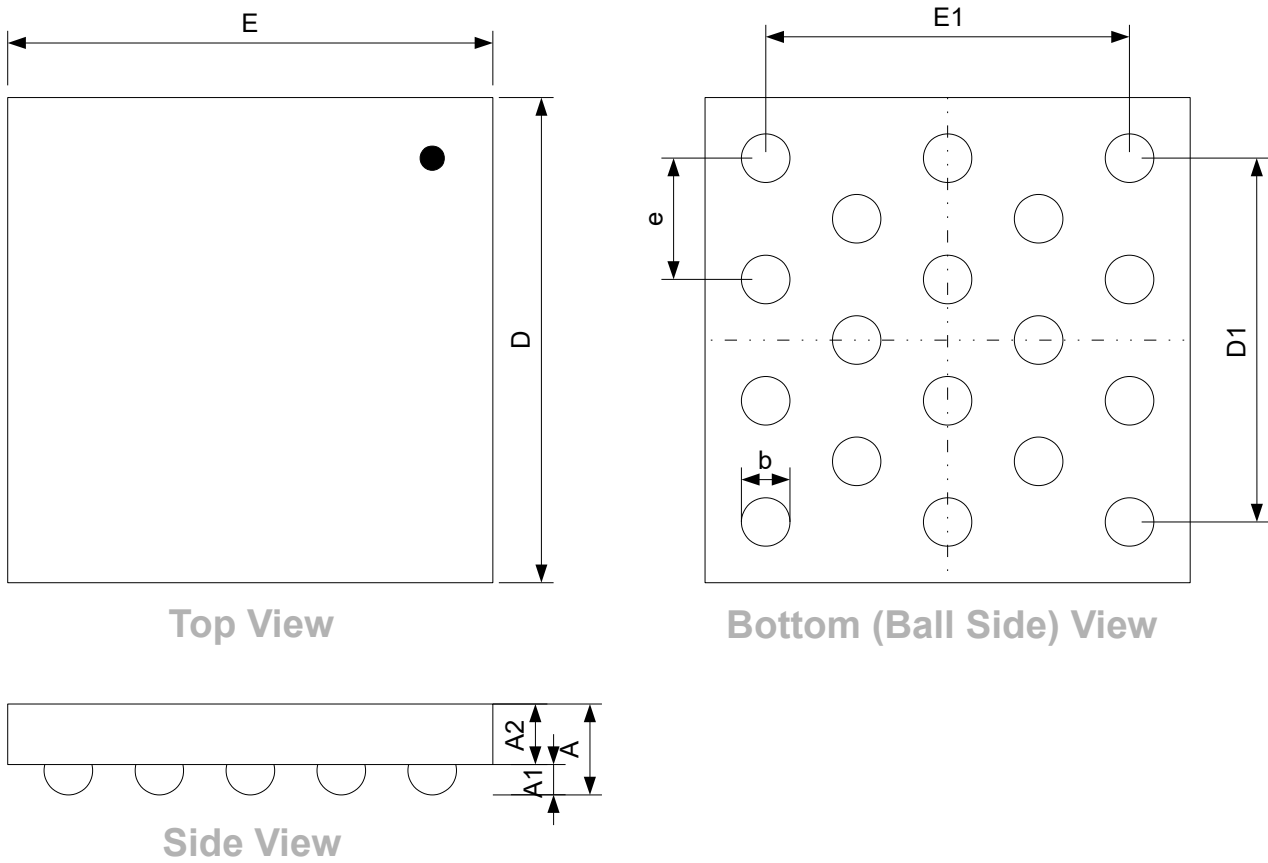


Figure 5.1: WLCSP (1.62x1.62) - 18 Package Outline Visual Description

Table 5.1: WLCSP (1.62x1.62) - 18 Package Outline Visual Description

Dimension	[mm]	Dimension	[mm]
A	0.525±0.05	D1	1.2
A1	0.2±0.02	E	1.620±0.015
A2	0.3±0.025	E1	1.2
b	0.260±0.39	e	0.4
D	1.620±0.015		



5.2 Package Outline Description - QFN20

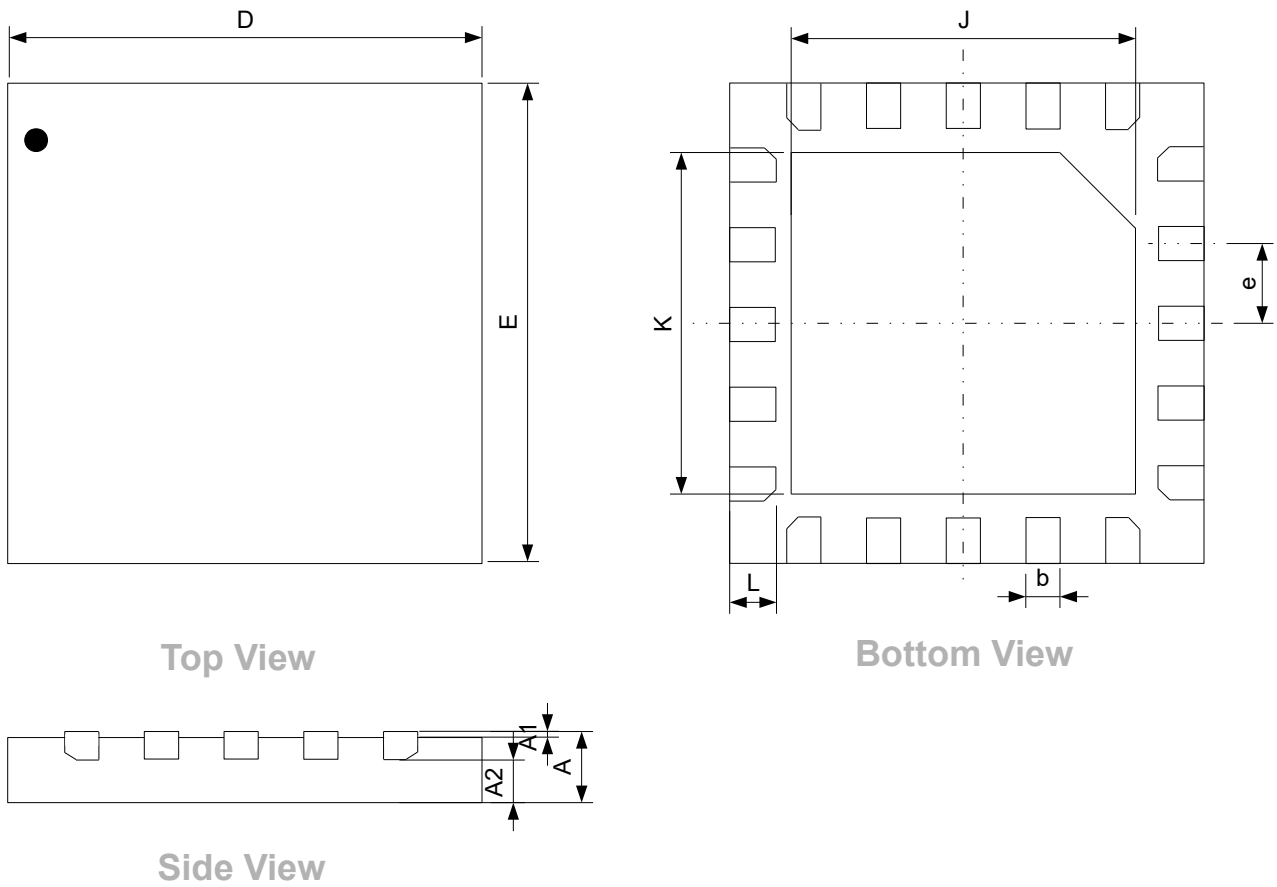


Figure 5.2: QFN (3x3)-20 Package Outline Visual Description

Table 5.2: QFN (3x3)-20 Package Outline Visual Description

Dimension	[mm]	Dimension	[mm]
A	0.5±0.1	E	3
A1	0.035±0.05	e	0.4
A2	0.3	J	1.7±0.1
A3	0.203	K	1.7±0.1
b	0.2±0.05	L	0.4±0.05
D	3		




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