

1200 V, 10 A Silicon Carbide Schottky Diode

Features

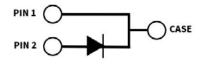
- 1.2 kv Schottky rectifier
- Zero reverse recovery current
- High-frequency operation
- Temperature-independent switching behavior
- Extremely fast switching
- Positive temperature coefficient on V_F







TO-252-2



Package Types: TO-252-2

PN: C4D10120

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Applications

- Solar inverters
- Switch mode power supplies (SMPS)
- Boost diodes in PFC or DC/DC stages
- Free wheeling diodes in inverter stages
- AC/DC converters

Benefits

- Replace bipolar with unipolar rectifiers
- Essentially no switching losses
- Higher efficiency
- Reduction of heat sink requirements
- Parallel devices without thermal runaway

Maximum Ratings (T_c = 25 °C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Note	
Repetitive Peak Reverse Voltage	V _{RRM}	1200				
Surge Peak Reverse Voltage	V _{RSM}	1300	V			
DC Blocking Voltage	V _{DC}	1200				
	I _F	33		T _c = 25 °C	Fig. 3	
Continuous Forward Current		16		T _c = 135 °C		
		10		T _c = 156 °C		
Repetitive Peak Forward Surge Current	I _{FRM}	47	A	T _C = 25 °C, t _P = 10 ms, Half Sine Pulse		
		31.5		T _C = 110 °C, t _P = 10 ms, Half Sine Pulse		
Non-Repetitive Peak Forward Surge Current	I _{FSM}	71		T _C = 25 °C, t _P = 10 ms, Half Sine Pulse	Fig. 8	
		59		T _C = 110 °C, t _P = 10 ms, Half Sine Pulse		
Non-Repetitive Peak Forward Current	I _{F, Max}	750		T _c = 25 °C, t _P = 10 μs, Pulse	Fig. 8	
		620		T_{c} = 110 °C, t_{p} = 10 μ s, Pulse		
Power Dissipation	P _{tot}	166.5	W	T _c = 25 °C	Fig. 4	
		72		T _c =110 °C	Fig. 4	
Diode dV/dt Ruggedness	dV/dt	200	V/ns	V _R = 0-960 V		
i²t Value	∫i²dt	25	A²s	$T_c = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$		
		17.5		$T_{c} = 110 {}^{\circ}\text{C}, t_{p} = 10 \text{ms}$		
Operating Junction and Storage Temperature	T _J , T _{stg}	-55 to +175	°C			

Electrical Characteristics

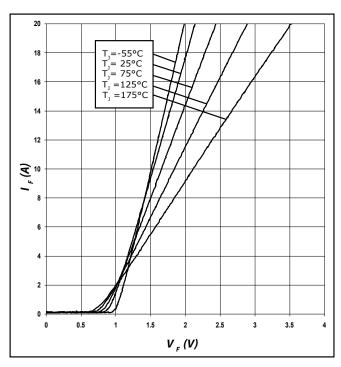
Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Note
Forward Voltage	V _F	1.5	1.8	V	I _F = 10 A, T _J = 25 °C	Fig. 1
		2.2	3		I _F = 10 A, T _J = 175 °C	
Reverse Current I _R		30	250		V _R = 1200 V, T _J = 25 °C	Fig. 2
	I _R	55	350	μΑ	V _R = 1200 V, T _J = 175 °C	
Total Capacitive Charge	Q _c	52		nC	$V_R = 800 \text{ V, } I_F = 10 \text{ A}$ $di/dt = 200 \text{ A}/\mu\text{S}$ $T_J = 25 \text{ °C}$	Fig. 5
Total Capacitance		754		pF	$V_R = 0 \text{ V}, T_J = 25 \text{ °C}, f = 1 \text{ MHz}$	Fig. 6
	С	45			V _R = 400 V, T _J = 25 °C, f = 1 MHz	
		38			V _R = 800 V, T _J = 25 °C, f = 1 MHz	
Capacitance Stored Energy	E _c	14.5		μJ	V _R = 800 V	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

Thermal Characteristics

Parameter	Symbol	Тур.	Unit	Note
Thermal Resistance from Junction to Case	$R_{\theta JC}$	0.9	°C/W	Fig. 9

Typical Performance





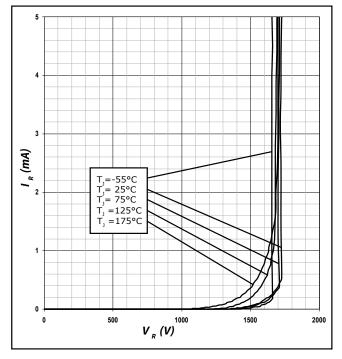


Figure 2. Reverse Characteristics

Typical Performance

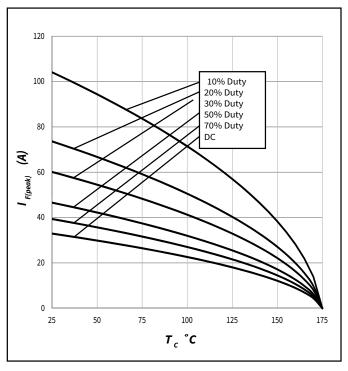


Figure 3. Current Derating

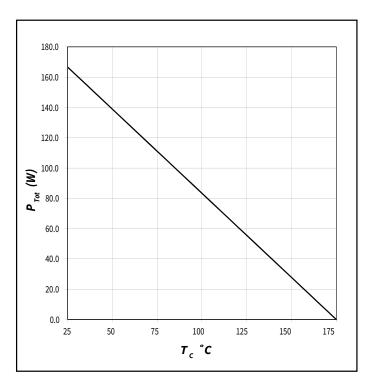


Figure 4. Power Derating

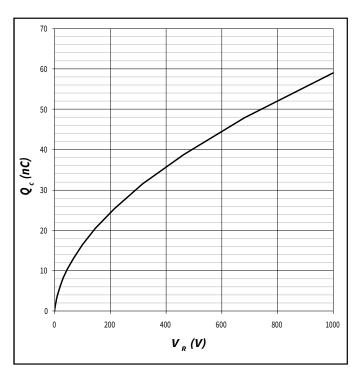


Figure 5. Recovery Charge vs. Reverse Voltage

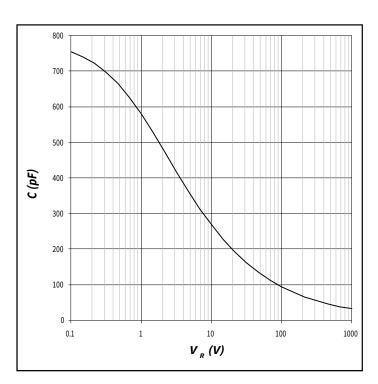
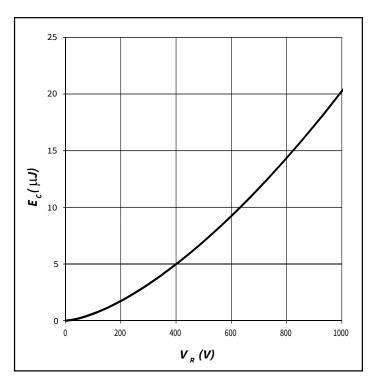


Figure 6. Capacitance vs. Reverse Voltage

1000

Typical Performance



T_{J,initial} = 25°C
T_{J,initial} = 110°C

10
1E-05
1E-04
1E-03
1E-02

t_p (s)

Figure 7. Typical Capacitance Stored Energy

Figure 8. Non-Repetitive Peak Forward Surge Current Versus Pulse Duration (Sinusoidal Waveform)

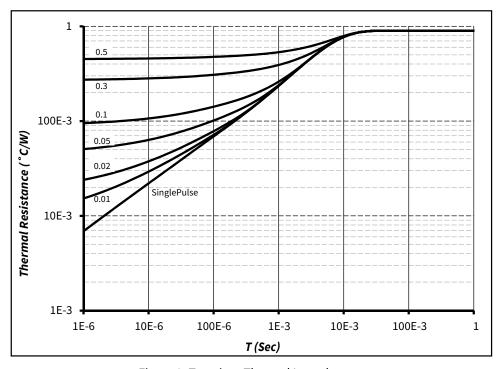
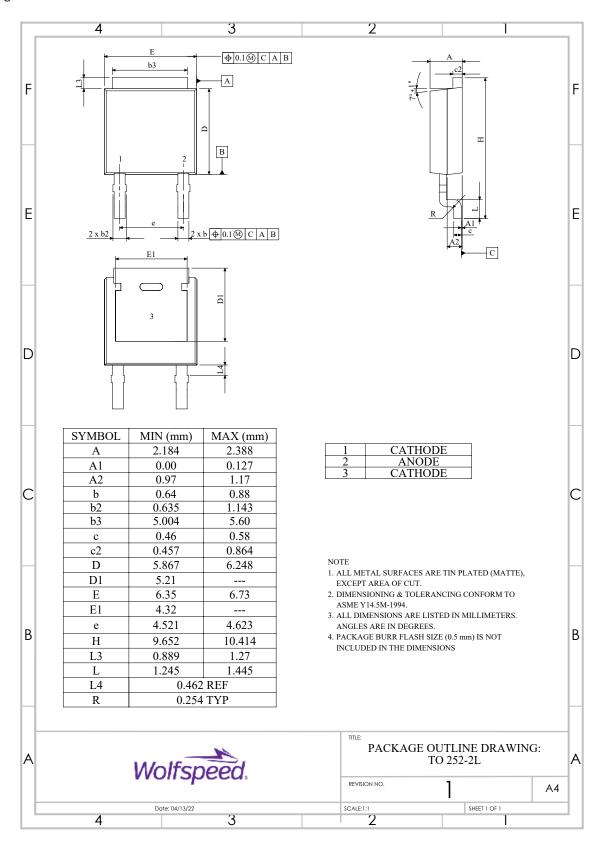


Figure 9. Transient Thermal Impedance

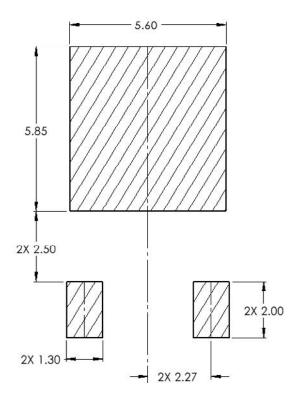
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Package Dimensions

Package: TO-252-2



Recommended Solder Pad Layout



Part Number	art Number Package	
C4D10120E	TO-252-2	C4D10120

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Diode Model

$$V_{fT} = V_T + If^*R_T$$

$$V_T = 0.98 + (T_J^* - 1.71^*10^{-3})$$

$$R_T = 0.040 + (T_J^* 5.32^*10^{-4})$$

Note: T_j = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

Revision History

Current Revision Date of Release		Description of Changes		
9	September-2023	Updated Wolfspeed branding, package drawing, and solder pad layout		
10	October-2023	Corrected solder pad layout and diode model		

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