

Sealed Choke Coil PST031T type

■ Features

Low profile : 3.0mm x 3.0mm x 1.0mm

Low coil resistance with large currents.

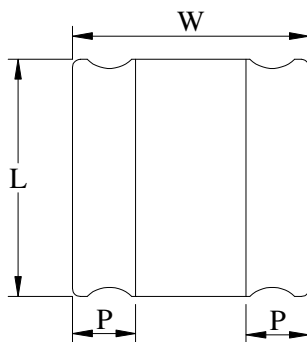
High magnetic shield construction should actualize high resolution for EMC protection.

100% lead (Pb) free meet RoHS standard

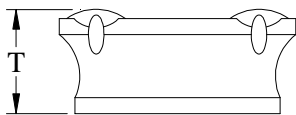
■ Application

Cellular phones, LCD displays, HDDs, DVCs, DSCs, PDAs etc..

■ Outline Dimensions



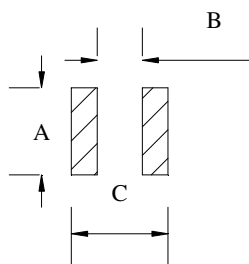
Code	Dimensions (mm)
L	3.0 ± 0.1
W	3.0 ± 0.1
T	1.0 Max
P	0.75 ± 0.2



Note : This graph is in regard to outline dimensions spec. For outer appearance, please refer to actual product.

■ Recommend Land Pattern Dimensions

The customer shall determine the land dimensions shown above after confirming and safety.



A	2.7 ~ 2.9
B	1.2 ~ 1.4
C	3.0

Unit : mm

■ Specifications

Part Number	L0 Inductance (μ H) @ (0A)	R _{dc} (m Ω)		Heat Rating Current DC Amps. Idc (A)		Saturation Current DC Amps. Isat (A)	
		Typical	Maximum	Typical	Maximum	Typical	Maximum
PST031T-R47MS	0.47	38	46	3.20	2.88	3.70	3.33
PST031T-1R0MS	1.0	68	82	2.75	2.48	3.00	2.70
PST031T-1R5MS	1.5	65	78	1.90	1.71	1.80	1.62
PST031T-2R2MS	2.2	90	108	1.60	1.44	1.60	1.44
PST031T-3R3MS	3.3	110	132	1.45	1.31	1.25	1.13
PST031T-4R7MS	4.7	167	200	1.20	1.08	1.00	0.90
PST031T-6R8MS	6.8	250	300	0.95	0.86	0.85	0.76
PST031T-8R2MS	8.2	311	373	0.85	0.77	0.80	0.72
PST031T-100MS	10.0	366	439	0.81	0.73	0.75	0.68
PST031T-150MS	15.0	672	807	0.72	0.64	0.58	0.52
PST031T-220MS	22.0	708	850	0.55	0.50	0.45	0.41
PST031T-330MS	33.0	1,360	1,632	0.50	0.45	0.38	0.34
PST031T-470MS	47.0	2,170	2,604	0.38	0.34	0.30	0.27

* : If you require another part number please contact with us.

** : Inductance Tolerance \pm 20%

Note 1. : All test data is referenced to 25 $^{\circ}$ C ambient.

Note 2. : Test Condition: 1MHz, 1.0Vrms

Note 3. : Idc : DC current (A) that will cause an approximate Δ T of 40 $^{\circ}$ C

Note 4. : Isat : DC current (A) that will cause Lo to drop approximately 30%

Note 5. : Operating Temperature Range -55 $^{\circ}$ C to + 125 $^{\circ}$ C

Note 6. : The part temperature (ambient + temp rise) should not exceed 125 $^{\circ}$ C under worse case operating conditions. Circuit design , component placement, PWB trace size and thickness, airflow and other cooling provision all affect the part temperature. Part temperature should be verified in the end application.

Note 7. : The rated current as listed is either the saturation current or the heating current depending on which value is lower.

Current Characteristic

