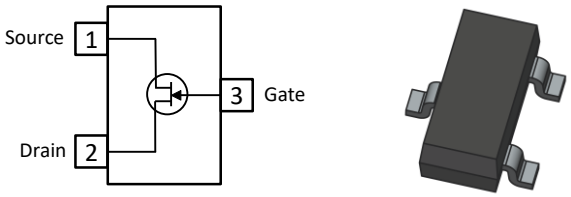
J304, J305 N-Channel JFET

Features

- InterFET N0026S Geometry
- Low Noise: 4 nV/√Hz Typical
- Low Ciss: 4.3pF Typical
- Low Leakage: 10pA Typical
- RoHS Compliant
- SMT, TH, and Bare Die Package options.

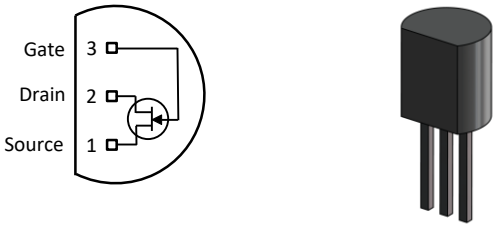
SOT23 Top View



Applications

- Mixers
- Oscillators
- VHF/UHF Amplifiers

TO-92 Bottom View



Description

The -30V InterFET J304 and J305 are targeted for low noise low leakage VHF/UHF amplifier designs as well as mixers and oscillators. Gate leakages are typically less than 10pA at room temperatures.

Product Summary

Parameters		J304 Min	J305 Min	Unit
BV_{GSS}	Gate to Source Breakdown Voltage	-30	-30	V
I_{DSS}	Drain to Source Saturation Current	5	1	mA
$V_{GS(off)}$	Gate to Source Cutoff Voltage	-2	-0.5	V
G_{FS}	Forward Transconductance	4500	3000	µS

Ordering Information Custom Part and Binning Options Available

Part Number	Description	Case	Packaging
J304; J305	Through-Hole	TO-92	Bulk
SMPJ304; SMPJ305	Surface Mount	SOT23	Bulk
SMPJ304TR; SMPJ305TR	7" Tape and Reel: Max 3,000 Pieces 13" Tape and Reel: Max 9,000 Pieces	SOT23	Minimum 1,000 Pieces Tape and Reel
J304COT; J305COT	Chip Orientated Tray (COT Waffle Pack)	COT	400/Waffle Pack
J304CFT; J305CFT	Chip Face-up Tray (CFT Waffle Pack)	CFT	400/Waffle Pack

Electrical Characteristics

Maximum Ratings (@ T_A = 25°C, Unless otherwise specified)

Parameters		Value	Unit
V_{RGS}	Reverse Gate Source and Gate Drain Voltage	-30	V
I_{FG}	Continuous Forward Gate Current	10	mA
P_D	Continuous Device Power Dissipation	360	mW
P	Power Derating	3.27	mW/°C
T_J	Operating Junction Temperature	-55 to 125	°C
T_{STG}	Storage Temperature	-65 to 200	°C

Static Characteristics (@ TA = 25°C, Unless otherwise specified)

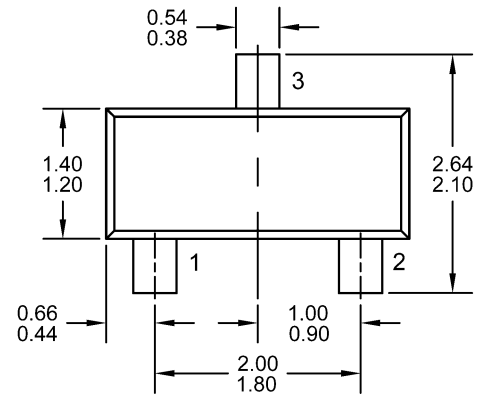
	Parameters	Conditions	J304			J305			Unit
			Min	Typ	Max	Min	Typ	Max	
$V_{(BR)GSS}$	Gate to Source Breakdown Voltage	V_{DS} = 0V, I_G = -1µA	-30			-30			V
I_{GSS}	Gate to Source Reverse Current	V_{GS} = -20V, V_{DS} = 0V			-100			-100	pA
$V_{GS(OFF)}$	Gate to Source Cutoff Voltage	V_{DS} = 15V, I_D = 1nA	-2		-6	-0.5		-3	V
I_{DSS}	Drain to Source Saturation Current	V_{GS} = 0V, V_{DS} = 15V (Pulsed)	5		15	1		8	mA

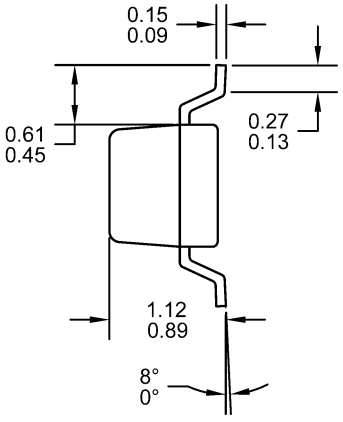
Dynamic Characteristics (@ TA = 25°C, Unless otherwise specified)

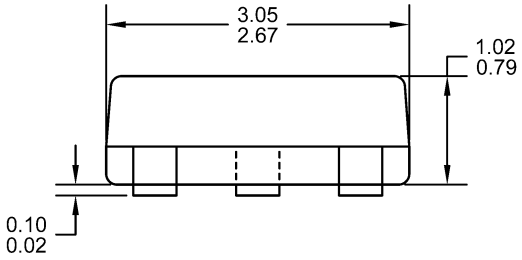
	Parameters	Conditions	J304			J305			Unit
			Min	Typ	Max	Min	Typ	Max	
G_{FS}	Forward Transconductance	V_{DS} = 15V, V_{GS} = 0V, f = 1kHz	4500		7500	3000			µS
		V_{DS} = 15V, V_{GS} = 0V, f = 100MHz					3000		
		V_{DS} = 15V, V_{GS} = 0V, f = 400MHz		4200					
G_{OS}	Output Conductance	V_{DS} = 15V, V_{GS} = 0V, f = 1kHz			50			50	µS
		V_{DS} = 15V, V_{GS} = 0V, f = 100MHz		60			60		
		V_{DS} = 15V, V_{GS} = 0V, f = 400MHz		80					
G_{IS}	Input Conductance	V_{DS} = 15V, V_{GS} = 0V, f = 100MHz		80			80		µS
		V_{DS} = 15V, V_{GS} = 0V, f = 400MHz		800					
G_{PS}	Power Gain	V_{DS} = 15V, I_D = 5mA, f = 100MHz		20					dB
		V_{DS} = 15V, I_D = 5mA, f = 400MHz		11					
B_{OS}	Output Susceptance	V_{DS} = 15V, V_{GS} = 0V, f = 100MHz		800			800		µS
		V_{DS} = 15V, V_{GS} = 0V, f = 400MHz		3600					
B_{IS}	Input Susceptance	V_{DS} = 15V, V_{GS} = 0V, f = 100MHz		2000			2000		µS
		V_{DS} = 15V, V_{GS} = 0V, f = 400MHz		7500					
C_{iss}	Input Capacitance	V_{DS} = 15V, V_{GS} = 0V, f = 1MHz		3			3		pF
C_{rss}	Reverse Transfer Capacitance	V_{DS} = 15V, V_{GS} = 0V, f = 1MHz		0.85			0.85		pF
C_{oss}	Output Capacitance	V_{DS} = 15V, V_{GS} = 0V, f = 1MHz		1			1		pF
NF	Noise Figure	V_{DS} = 15V, I_D = 5mA, f = 100MHz		1.7					dB
		R_G = 1Ω f = 400MHz		3.8					

SOT23 (TO-236AB) Mechanical and Layout Data

Package Outline Data

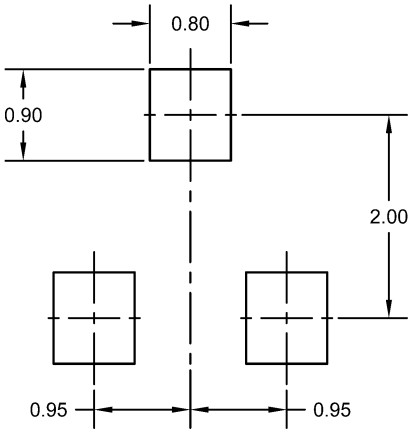






1. All linear dimensions are in millimeters.
2. Package weight approximately 0.12 grams
3. Molded plastic case UL 94V-0 rated
4. For Tape and Reel specifications refer to InterFET CTC-021 Tape and Reel Specification, Document number: IF39002
5. Bulk product is shipped in standard ESD shipping material
6. Refer to JEDEC standards for additional information.

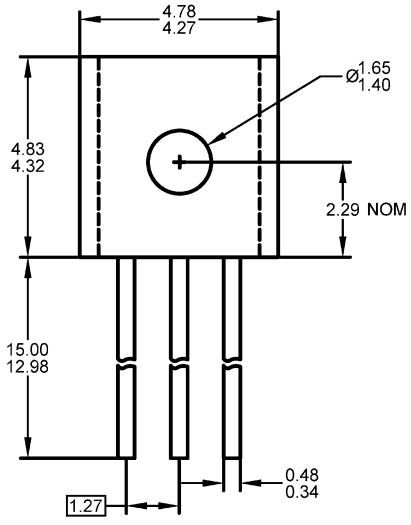
Suggested Pad Layout

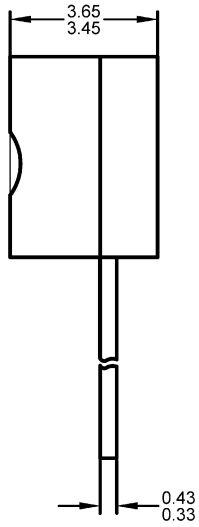


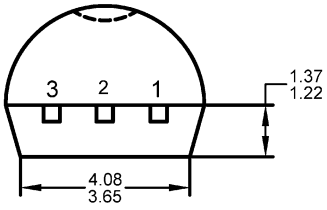
1. All linear dimensions are in millimeters.
2. The suggested land pattern dimensions have been provided for reference only. A more robust pattern may be desired for wave soldering.

TO-92 Mechanical and Layout Data

Package Outline Data

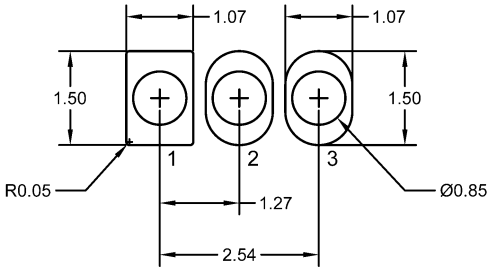






1. All linear dimensions are in millimeters.
2. Package weight approximately 0.19 grams
3. Molded plastic case UL 94V-0 rated
4. Bulk product is shipped in standard ESD shipping material
5. Refer to JEDEC standards for additional information.

Suggested Through-Hole Layout



1. All linear dimensions are in millimeters.
2. The suggested land pattern dimensions have been provided as a straight lead reference only. A more robust pattern may be desired for wave soldering and/or bent lead configurations.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

InterFET:

J305 J304 SMPJ304 SMPJ305 SMPU304