## BD136G, BD138G, BD140G

## Plastic Medium-Power Silicon PNP Transistors

This series of plastic, medium-power silicon PNP transistors are designed for use as audio amplifiers and drivers utilizing complementary or quasi complementary circuits.

## Features

- High DC Current Gain
- BD 136, 138, 140 are complementary with BD 135, 137, 139
- These Devices are $\mathrm{Pb}-$ Free, Halogen Free/BFR Free and are RoHS Compliant*


## MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: |
| ```Collector-Emitter Voltage BD136G BD138G BD140G``` | $\mathrm{V}_{\text {CEO }}$ | $\begin{aligned} & 45 \\ & 60 \\ & 80 \end{aligned}$ | Vdc |
|  | $\mathrm{V}_{\text {CBO }}$ | $\begin{gathered} 45 \\ 60 \\ 100 \end{gathered}$ | Vdc |
| Emitter-Base Voltage | $\mathrm{V}_{\text {EBO }}$ | 5.0 | Vdc |
| Collector Current | $\mathrm{I}_{\mathrm{C}}$ | 1.5 | Adc |
| Base Current | $\mathrm{I}_{\mathrm{B}}$ | 0.5 | Adc |
| Total Device Dissipation @ $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ Derate above $25^{\circ} \mathrm{C}$ | $\mathrm{P}_{\mathrm{D}}$ | $\begin{gathered} 1.25 \\ 10 \end{gathered}$ | Watts $\mathrm{mW} /{ }^{\circ} \mathrm{C}$ |
| Total Device Dissipation @ $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ Derate above $25^{\circ} \mathrm{C}$ | $\mathrm{P}_{\mathrm{D}}$ | $\begin{aligned} & 12.5 \\ & 100 \end{aligned}$ | Watts $\mathrm{mW} /{ }^{\circ} \mathrm{C}$ |
| Operating and Storage Junction Temperature Range | $\mathrm{T}_{\mathrm{J}}, \mathrm{T}_{\text {stg }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
| :--- | :---: | :---: | :---: |
| Thermal Resistance, Junction-to-Case | $\mathrm{R}_{\theta \mathrm{JC}}$ | 10 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Thermal Resistance, Junction-to-Ambient | $\mathrm{R}_{\theta \mathrm{JA}}$ | 100 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

[^0]ON Semiconductor ${ }^{\circledR}$
www.onsemi.com

### 1.5 A POWER TRANSISTORS PNP SILICON

45, 60, 80 V, 12.5 W


ORDERING INFORMATION

| Device | Package | Shipping |
| :---: | :---: | :---: |
| BD136G | TO-225 <br> $(\mathrm{Pb}-\mathrm{Free})$ | 500 Units/Box |
| BD138G | TO-225 <br> $(\mathrm{Pb}-$ Free $)$ | 500 Units/Box |
| BD140G | TO-225 <br> $(\mathrm{Pb}-$ Free $)$ | 500 Units/Box |

ELECTRICAL CHARACTERISTICS $\left(T_{C}=25^{\circ} \mathrm{C}\right.$ unless otherwise noted)

| Characteristic | Symbol | Min | Max | Unit |
| :---: | :---: | :---: | :---: | :---: |
| ```Collector-Emitter Sustaining Voltage (Note 1) (IC = 0.03 Adc, IB = 0) BD136G BD138G BD140G``` | BV CEO | $\begin{aligned} & 45 \\ & 60 \\ & 80 \end{aligned}$ |  | Vdc |
| $\begin{aligned} & \text { Collector Cutoff Current } \\ & \left(V_{C B}=30 \mathrm{Vdc}, I_{E}=0\right) \\ & \left(V_{C B}=30 \mathrm{Vdc}, I_{E}=0, T_{C}=125^{\circ} \mathrm{C}\right) \end{aligned}$ | $\mathrm{I}_{\text {cbo }}$ | - | $\begin{aligned} & 0.1 \\ & 10 \end{aligned}$ | $\mu \mathrm{Adc}$ |
| Emitter Cutoff Current $\left(\mathrm{V}_{\mathrm{BE}}=5.0 \mathrm{Vdc}, \mathrm{I}_{\mathrm{C}}=0\right)$ | $\mathrm{l}_{\text {ebo }}$ | - | 10 | $\mu \mathrm{Adc}$ |
| $\begin{aligned} & \hline \text { DC Current Gain } \\ & \left(\mathrm{I}_{\mathrm{C}}=0.005 \mathrm{~A}, \mathrm{~V}_{\mathrm{CE}}=2 \mathrm{~V}\right) \\ & \left(\mathrm{I}_{\mathrm{C}}=0.15 \mathrm{~A}, \mathrm{~V}_{\mathrm{CE}}=2 \mathrm{~V}\right) \\ & \left(\mathrm{I}_{\mathrm{C}}=0.5 \mathrm{~A}, \mathrm{~V}_{\mathrm{CE}}=2 \mathrm{~V}\right) \end{aligned}$ | $\mathrm{h}_{\text {FE }}{ }^{\text {* }}$ | $\begin{aligned} & 25 \\ & 40 \\ & 25 \end{aligned}$ | $-\overline{250}$ | - |
| $\begin{aligned} & \text { Collector-Emitter Saturation Voltage (Note 1) } \\ & \left(I_{C}=0.5 \mathrm{Adc}, \mathrm{I}_{\mathrm{B}}=0.05 \mathrm{Adc}\right) \end{aligned}$ | $\mathrm{V}_{\mathrm{CE} \text { (sat) }}{ }^{*}$ | - | 0.5 | Vdc |
| Base-Emitter On Voltage (Note 1) $\left(I_{C}=0.5 \mathrm{Adc}, \mathrm{V}_{\mathrm{CE}}=2.0 \mathrm{Vdc}\right)$ | $\mathrm{V}_{\mathrm{BE}(\text { (on) }}{ }^{\text {* }}$ | - | 1 | Vdc |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width $\leq 300 \mu \mathrm{~s}$, Duty Cycle $\leq 2.0 \%$.

## TYPICAL CHARACTERISTICS



Figure 1. DC Current Gain

$\mathrm{I}_{\mathrm{C}}$, COLLECTOR CURRENT (A)
Figure 2. Collector-Emitter Saturation Voltage

## BD136G, BD138G, BD140G

TYPICAL CHARACTERISTICS


Figure 3. Base-Emitter Saturation Voltage


Figure 5. Capacitance

$\mathrm{I}_{\mathrm{C}}$, COLLECTOR CURRENT (A)
Figure 4. Base-Emitter On Voltage


Figure 6. Active-Region Safe Operating Area


Figure 7. Power Derating

## PACKAGE DIMENSIONS




FRONT VIEW


SIDE VIEW

NOTES

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS. 3. NUMBER AND SHAPE OF LUGS OPTIONAL.

|  | MILLIMETERS |  |
| :---: | ---: | ---: |
| DIM | MIN | MAX |
| A | 2.40 | 3.00 |
| A1 | 1.00 | 1.50 |
| b | 0.60 | 0.90 |
| b2 | 0.51 | 0.88 |
| c | 0.39 | 0.63 |
| D | 10.60 | 11.10 |
| E | 7.40 | 7.80 |
| e | 2.04 | 2.54 |
| L | 14.50 | 16.63 |
| L1 | 1.27 | 2.54 |
| P | 2.90 | 3.30 |
| Q | 3.80 | 4.20 |

STYLE 1:
PIN 1. EMITTER
2., 4. COLLECTOR 3. BASE

ON Semiconductor and (UiN) are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## PUBLICATION ORDERING INFORMATION

## LITERATURE FULFILLMENT

Literature Distribution Center for ON Semiconductor
P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com
N. American Technical Support: 800-282-9855 Toll Free

USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421337902910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com
Order Literature: http://www.onsemi.com/orderlit
For additional information, please contact your local Sales Representative


[^0]:    *For additional information on our $\mathrm{Pb}-F r e e$ strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

