

**Features**

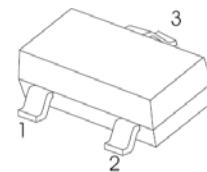
- $V_{DS}$  (V) = 20V
- $R_{DS(ON)} < 24m\Omega$  ( $V_{GS} = 4.5V$ )
- $R_{DS(ON)} < 31m\Omega$  ( $V_{GS} = 2.5V$ )

**Application(s)**

- Load/ System Switch

**Benefits**

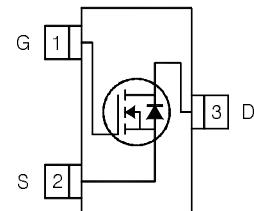
- Multi-vendor compatibility
- Environmentally friendly
- Increased Reliability

**SOT - 23**

1. GATE

2. SOURCE

3. DRAIN

**Absolute Maximum Ratings**

$I_{DS}$	Drain-Source Voltage	20	V
$I_D$ @ $T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	6.3	
$I_D$ @ $T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	5.1	A
$I_{DM}$	Pulsed Drain Current	32	
$P_D$ @ $T_A = 25^\circ C$	Maximum Power Dissipation	1.3	
$P_D$ @ $T_A = 70^\circ C$	Maximum Power Dissipation	0.80	
	Linear Derating Factor	0.01	W/°C
$V_{GS}$	Gate-to-Source Voltage	$\pm 12$	V
$T_J, T_{STG}$	Junction and Storage Temperature Range	-55 to + 150	°C

**Thermal Resistance**

$R_{0JA}$	Junction-to-Ambient ③	—	100	
④	Junction-to-Ambient ( $t < 10s$ ) ④	—	99	°C/W

**Notes:**

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Pulse width  $\leq 400\mu s$ ; duty cycle  $\leq 2\%$ .
- ③ Surface mounted on 1 in square Cu board.

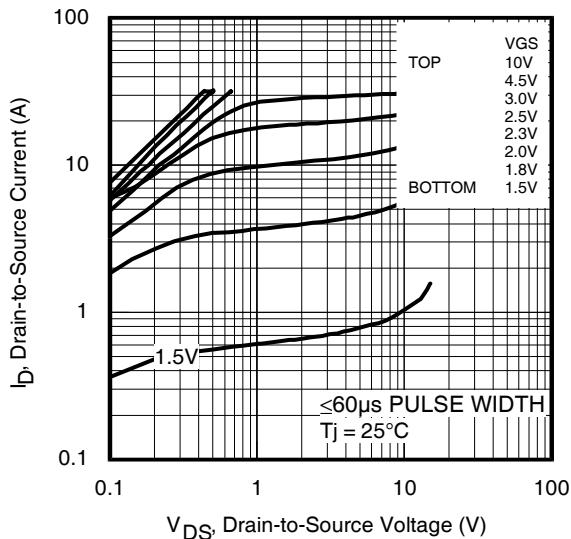
**Electric Characteristics @  $T_J = 25^\circ\text{C}$  (unless otherwise specified)**

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(\text{BR})\text{DSS}}$	Drain-to-Source Breakdown Voltage	20			V	$V_{GS} = 0V, I_D = 250\mu\text{A}$
$\Delta V_{(\text{BR})\text{DSS}}/\Delta T_J$	Breakdown Voltage Temp. Coefficient		7.8		mV/°C	Reference to $25^\circ\text{C}, I_D = 1\text{mA}$
$R_{DS(\text{on})}$	Static Drain-to-Source On-Resistance		21	24	mΩ	$V_{GS} = 4.5V, I_D = 6.3\text{A}$ ②
			27	31		$V_{GS} = 2.5V, I_D = 5.1\text{A}$ ②
$V_{GS(\text{th})}$	Gate Threshold Voltage	0.5	0.9	1.1	V	$V_{DS} = V_{GS}, I_D = 10\mu\text{A}$
$I_{DSS}$	Drain-to-Source Leakage Current			1.0	μA	$V_{DS} = 16V, V_{GS} = 0V$
				150		$V_{DS} = 16V, V_{GS} = 0V, T_J = 125^\circ\text{C}$
$I_{GSS}$	Gate-to-Source Forward Leakage			100	nA	$V_{GS} = 12V$
	Gate-to-Source Reverse Leakage			-100		$V_{GS} = -12V$
$R_G$	Internal Gate Resistance		1.7		Ω	
$g_{fs}$	Forward Transconductance	17			S	$V_{DS} = 10V, I_D = 6.3\text{A}$
$Q_g$	Total Gate Charge		8.9		nC	$I_D = 6.3\text{A}$
$Q_{gs}$	Gate-to-Source Charge		0.68			$V_{DS} = 10V$
$Q_{gd}$	Gate-to-Drain ("Miller") Charge		4.4			$V_{GS} = 4.5V$ ②
$t_{d(on)}$	Turn-On Delay Time		4.9		ns	$V_{DD} = 10V$ ②
$t_r$	Rise Time		7.5			$I_D = 1.0\text{A}$
$t_{d(off)}$	Turn-Off Delay Time		19			$R_G = 6.8\Omega$
$t_f$	Fall Time		12			$V_{GS} = 4.5V$
$C_{iss}$	Input Capacitance		700		pF	$V_{GS} = 0V$
$C_{oss}$	Output Capacitance		140			$V_{DS} = 16V$
$C_{rss}$	Reverse Transfer Capacitance		98			$f = 1.0\text{MHz}$

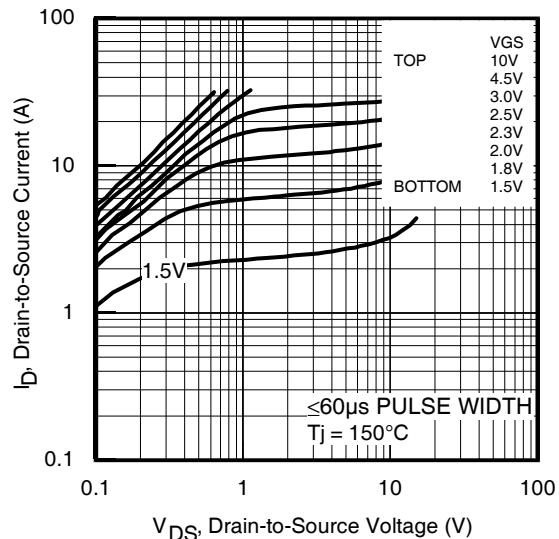
**Source - Drain Ratings and Characteristics**

Symbol	Parameter	Min.	Typ.	Max.	Units	
$I_S$	Continuous Source Current (Body Diode)			1.3	A	MOSFET symbol showing the integral reverse p-n junction diode.
$I_{SM}$	Pulsed Source Current (Body Diode) ①			32		
$V_{SD}$	Diode Forward Voltage			1.2	V	$T_J = 25^\circ\text{C}, I_S = 6.3\text{A}, V_{GS} = 0V$ ②
$t_{rr}$	Reverse Recovery Time		12	18		$T_J = 25^\circ\text{C}, V_R = 15V, I_F = 1.3\text{A}$
$Q_{rr}$	Reverse Recovery Charge		5.1	7.7	nC	$dI/dt = 100\text{A}/\mu\text{s}$ ②

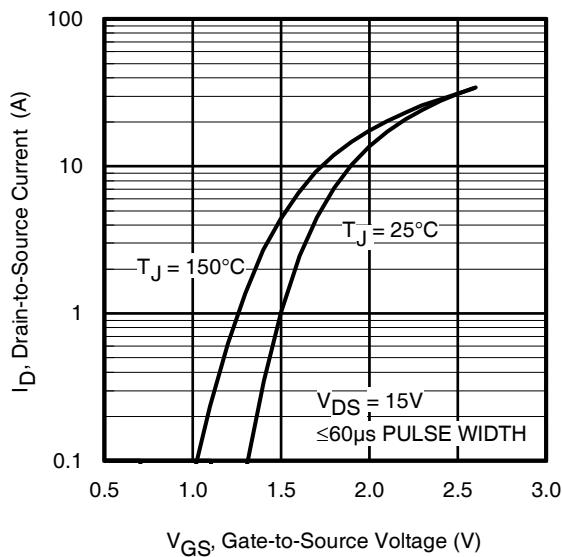
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



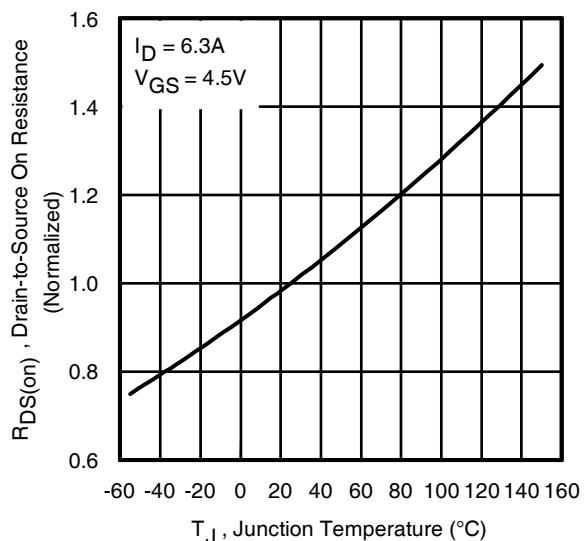
**Fig 1.** Typical Output Characteristics



**Fig 2.** Typical Output Characteristics

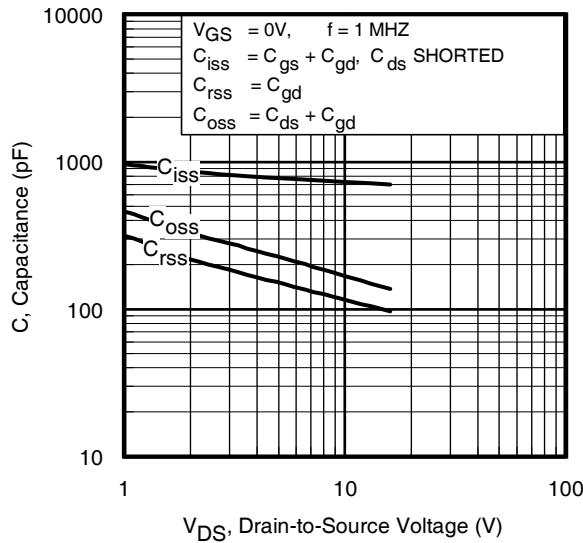


**Fig 3.** Typical Transfer Characteristics

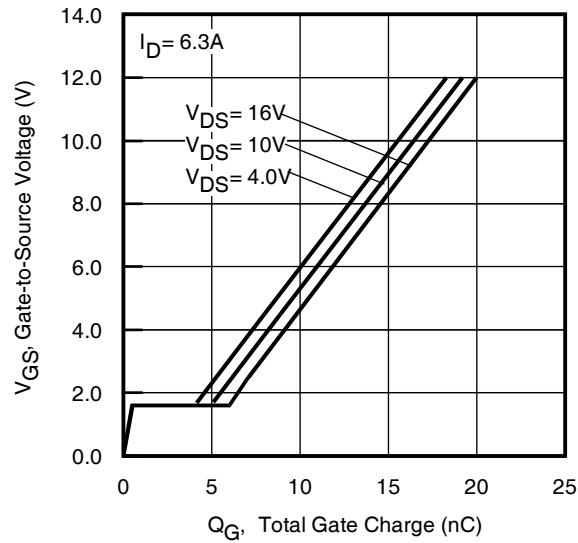


**Fig 4.** Normalized On-Resistance  
vs. Temperature

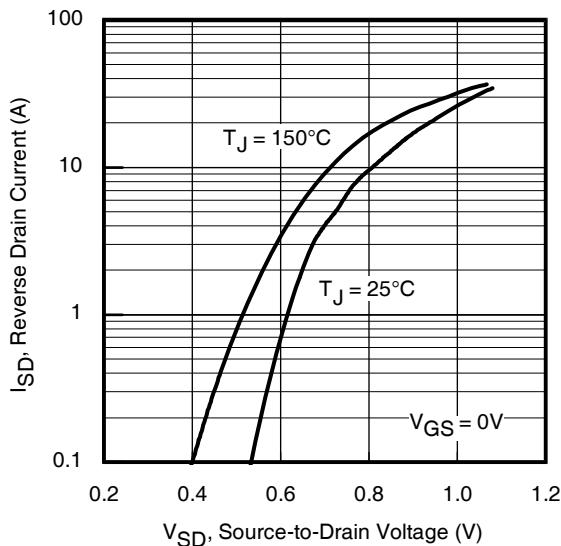
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



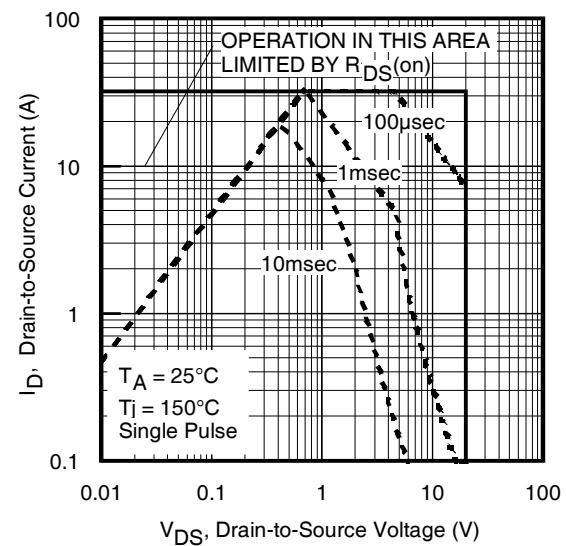
**Fig 5.** Typical Capacitance vs.  
Drain-to-Source Voltage



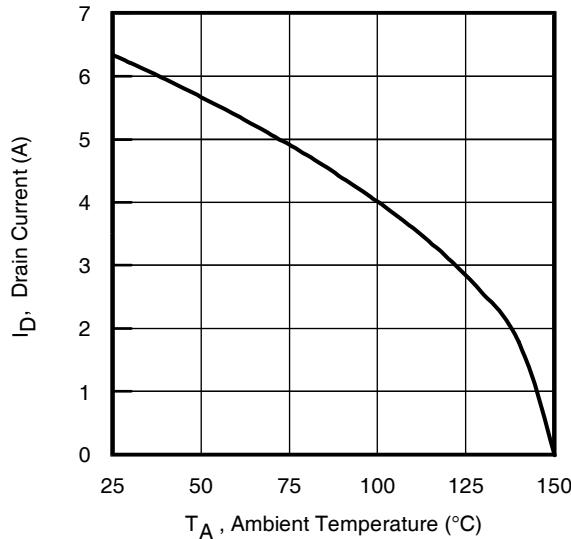
**Fig 6.** Typical Gate Charge vs.  
Gate-to-Source Voltage



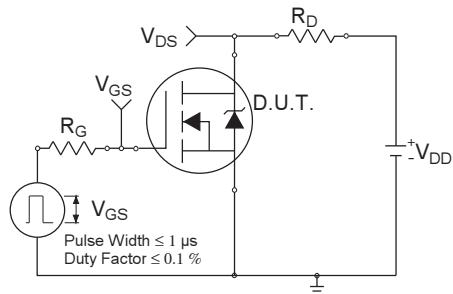
**Fig 7.** Typical Source-Drain Diode  
Forward Voltage



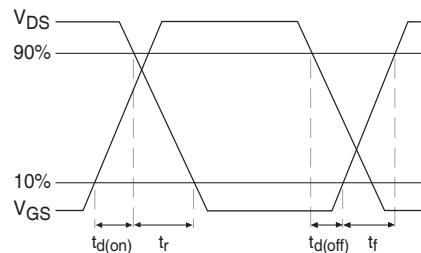
**Fig 8.** Maximum Safe Operating Area



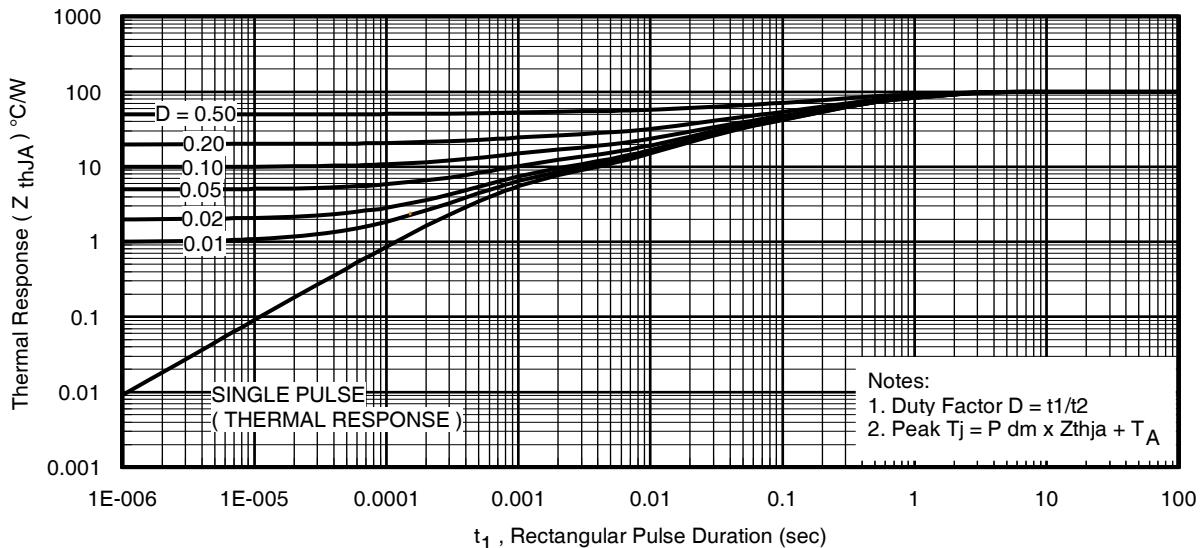
**Fig 9.** Maximum Drain Current vs.  
Ambient Temperature



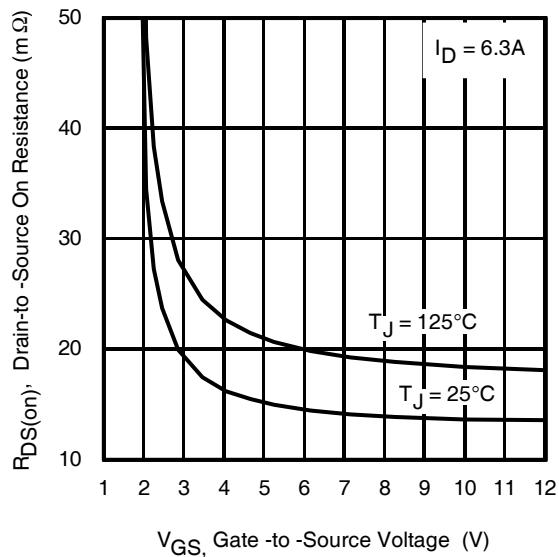
**Fig 10a.** Switching Time Test Circuit



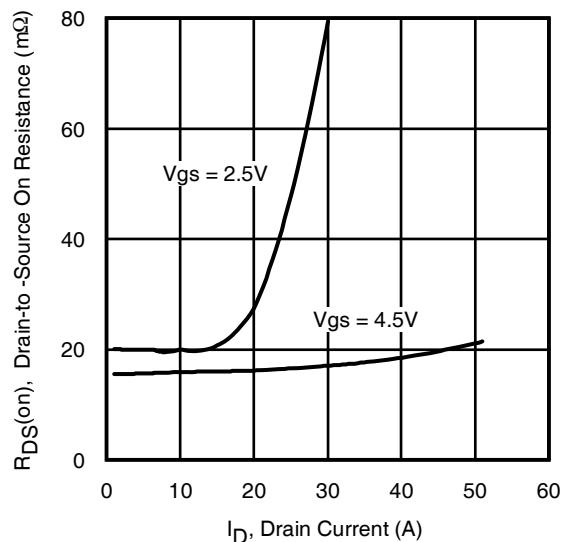
**Fig 10b.** Switching Time Waveforms



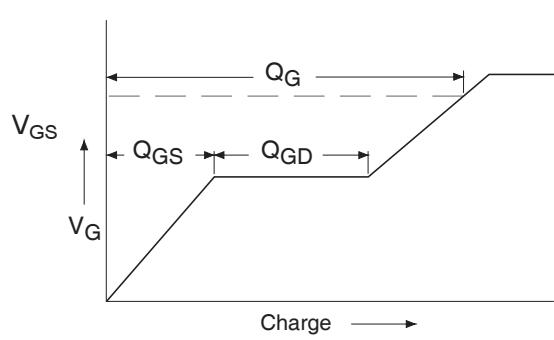
**Fig 11.** Typical Effective Transient Thermal Impedance, Junction-to-Ambient



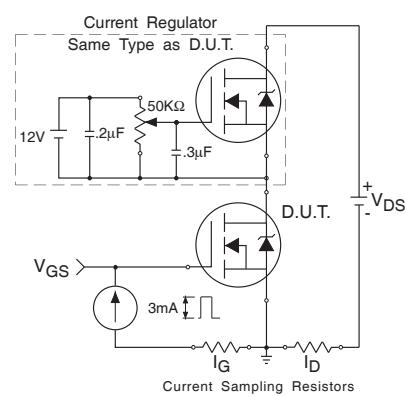
**Fig 12.** Typical On-Resistance vs.  
Gate Voltage



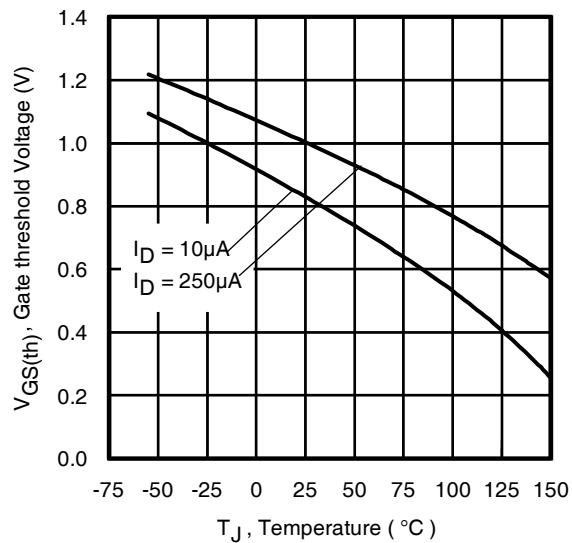
**Fig 13.** Typical On-Resistance vs.  
Drain Current



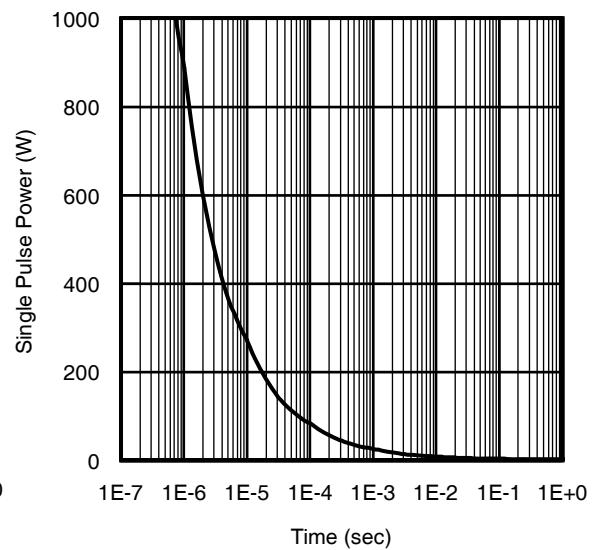
**Fig 14a.** Basic Gate Charge Waveform



**Fig 14b.** Gate Charge Test Circuit

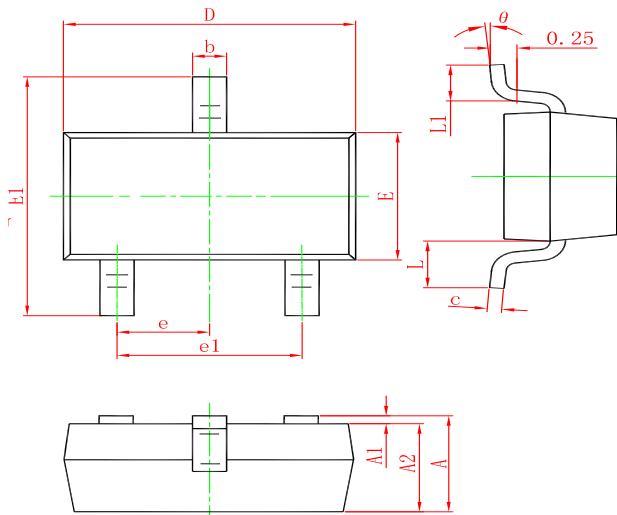


**Fig 15.** Typical Threshold Voltage vs.  
Junction Temperature



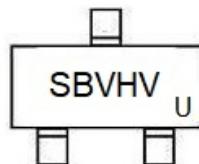
**Fig 16.** Typical Power vs. Time

## SOT-23 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

## Marking



## Ordering information

Order code	Package	Baseqty	Deliverymode
UMW IRLML6244TR	SOT-23	3000	Tape and reel