Nch 100V 10A Power MOSFET

| V _{DSS} | 100V |
|----------------------------|-------|
| R _{DS(on)} (Max.) | 133mΩ |
| I _D | ±10A |
| P _D | 20W |

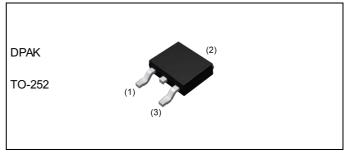
● Features

- 1) Low on resistance
- 2) Fast switching speed
- 3) Drive circuits can be simple
- 4) Parallel use is easy
- 5) Pb-free lead plating; RoHS compliant
- 6) AEC-Q101 Qualified

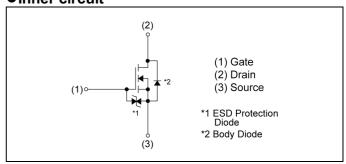
Application

Switching

Outline



•Inner circuit



Packaging specifications

| - 1 dokaging opcomoducione | | | | | | |
|----------------------------|-----------------|------------------|--|--|--|--|
| | Packing | Embossed Tape | | | | |
| | Reel size (mm) | 330 | | | | |
| Туре | Tape width (mm) | 16 | | | | |
| | Quantity (pcs) | 2500 | | | | |
| | Taping code | TL | | | | |
| | Marking | RD3P100SN | | | | |

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

| | • | | |
|--|--------------------|-------------|------|
| Parameter | Symbol | Value | Unit |
| Drain - Source voltage | V _{DSS} | 100 | V |
| Continuous drain current | I _D *1 | ±10 | Α |
| Pulsed drain current | I _{DP} *2 | ±20 | Α |
| Gate - Source voltage | V _{GSS} | ±20 | V |
| Power dissipation | P _D *3 | 20 | W |
| Junction temperature | T _j | 150 | °C |
| Operating junction and storage temperature range | T _{stg} | -55 to +150 | °C |

●Thermal resistance

| Parameter | Symbol | Values | | | Lleit |
|-------------------------------------|----------------------|--------|------|------|-------|
| | | Min. | Тур. | Max. | Unit |
| Thermal resistance, junction - case | R _{thJC} *3 | - | - | 6.25 | °C/W |

● Electrical characteristics (T_a = 25°C)

| Parameter | Symbol Conditions | | Values | | | Unit | |
|--|--|--|--------|-----------|-----|-------|--|
| Parameter | Symbol | Conditions | Min. | lin. Typ. | | Offic | |
| Drain - Source breakdown voltage | V _{(BR)DSS} | $V_{GS} = 0V$, $I_D = 1mA$ | 100 | - | - | V | |
| Breakdown voltage temperature coefficient | $\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$ | I _D = 1mA referenced to 25°C | - | 116.9 | - | mV/°C | |
| Zero gate voltage drain current | I _{DSS} | V _{DS} = 100V, V _{GS} = 0V | - | - | 1 | μA | |
| Gate - Source leakage current | e - Source leakage current I_{GSS} $V_{GS} = \pm 20V, V_{DS} = 0V$ | | - | 1 | ±10 | μA | |
| Gate threshold voltage | hreshold voltage $V_{GS(th)}$ $V_{DS} = 10V$ | | 1.0 | 1 | 2.5 | V | |
| Gate threshold voltage temperature coefficient | | I _D = 1mA referenced to 25°C | - | -3.6 | - | mV/°C | |
| | | V _{GS} = 10V, I _D = 5A | - | 95 | 133 | | |
| Static drain - source on - state resistance | R _{DS(on)} *4 | V _{GS} = 4.5V, I _D = 5A | - | 100 | 140 | mΩ | |
| | | $V_{GS} = 4.0V, I_D = 5A$ | - | 105 | 147 | | |
| Gate resistance | R_{G} | R _G f = 1MHz, open drain | | 6.3 | 1 | Ω | |
| Forward Transfer Admittance | Y _{fs} *4 | V _{DS} = 10V, I _D = 5A | 4.5 | - | - | S | |

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10 μ s , Duty cycle \leq 1%

^{*3} T_C=25°C

^{*4} Pulsed

●Electrical characteristics (T_a = 25°C)

| Davamatav | Cy made al | Conditions | Values | | | l limit | |
|------------------------------|------------------------|---|--------|------|------|---------|--|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit | |
| Input capacitance | C _{iss} | V _{GS} = 0V | - | 700 | - | | |
| Output capacitance | C _{oss} | V _{DS} = 25V | - | 65 | - | pF | |
| Reverse transfer capacitance | C _{rss} | f = 1MHz | - | 40 | - | | |
| Turn - on delay time | t _{d(on)} *4 | V _{DD} ≈ 50V,V _{GS} = 10V | - | 10 | - | | |
| Rise time | t _r *4 | I _D = 5A | - | 17 | - | no | |
| Turn - off delay time | t _{d(off)} *4 | $R_L \simeq 10\Omega$ | - | 50 | - | ns | |
| Fall time | t _f *4 | $R_G = 10\Omega$ | - | 20 | - | | |

• Gate charge characteristics $(T_a = 25^{\circ}C)$

| | \ a | , | | | | |
|----------------------|-----------------------|--|--------|------|------|-------|
| Parameter | Occupation Conditions | | Values | | | Unit |
| raianietei | Symbol | Conditions | Min. | Тур. | Max. | Offic |
| Total gate charge | Qg*4 | V _{DD} ≃ 50V. | - | 18 | - | |
| Gate - Source charge | Q _{gs} *4 | V _{DD} ≃ 50V, I _D = 10A, V _{GS} = 10V | - | 2 | - | nC |
| Gate - Drain charge | Q _{gd} *4 | | - | 4.5 | - | |

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

| Darameter | Cumbal | Conditions | Values | | | l leit |
|----------------------------|--------------------|--|--------|------|------|--------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit |
| Continuous forward current | I _S *1 | T = 25°C | - | - | 10 | Α |
| Pulse forward current | I _{SP} *2 | T _a = 25°C | - | - | 20 | Α |
| Forward voltage | V _{SD} *4 | V _{GS} = 0V, I _S = 10A | - | - | 1.5 | V |

Fig.1 Power Dissipation Derating Curve

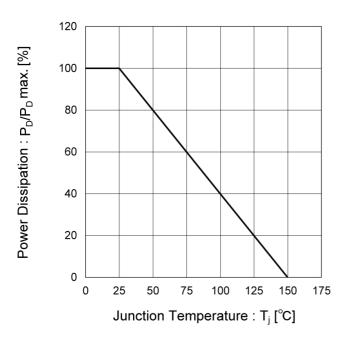


Fig.2 Maximum Safe Operating Area

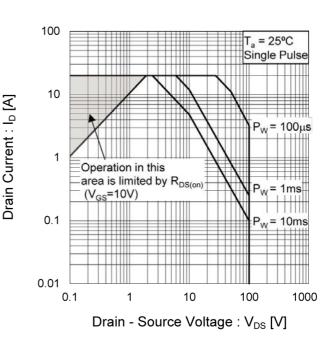


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

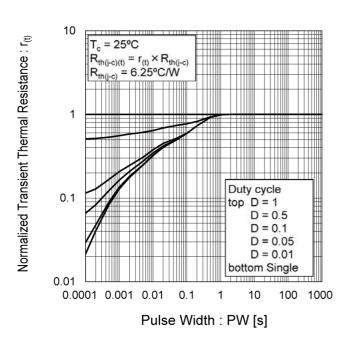
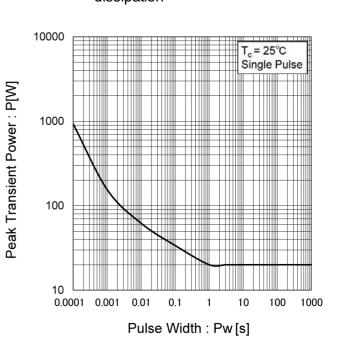


Fig.4 Single Pulse Maximum Power dissipation



Drain Current : I_D [A]

• Electrical characteristic curves

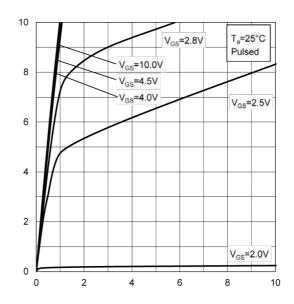
Fig.5 Typical Output Characteristics(I)

10 T_=25°C Pulsed V_{GS}=10.0V 8 V_{GS}=4.5V V_{GS}=4.0V 6 V_{GS}=2.8V 4 V_{GS}=2.5V 2 V_{GS} =2.0V0 0.2 0.4 0.6

Drain Current : I_D [A]

Drain - Source Voltage: V_{DS}[V]

Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature

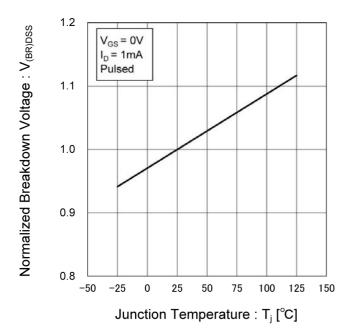


Fig.8 Typical Transfer Characteristics

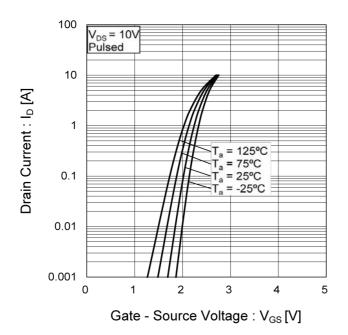


Fig.9 Gate Threshold Voltage vs.

Junction Temperature

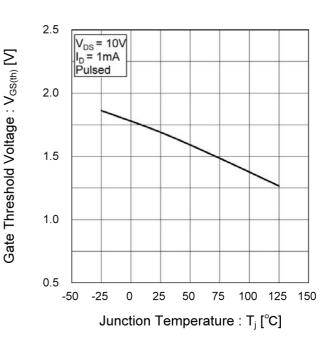
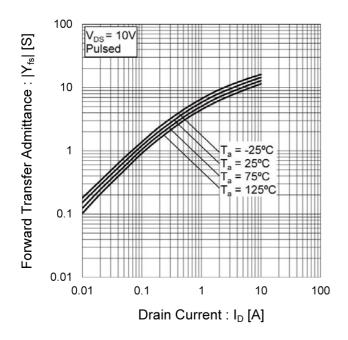


Fig.10 Forward Transfer Admittance vs.
Drain Current



RD3P100SNFRA

Fig.11 Drain Current Derating Curve

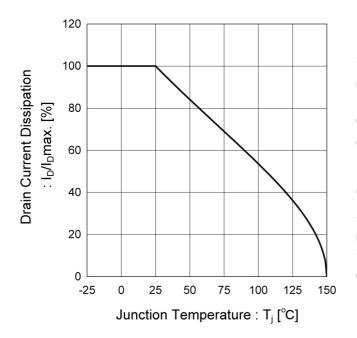


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

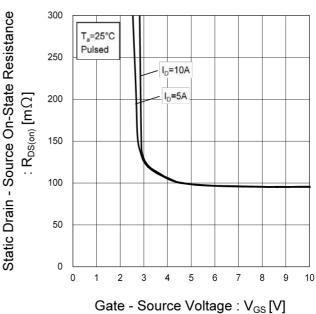


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

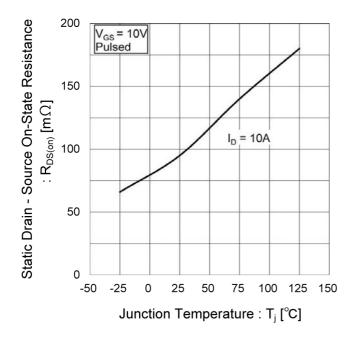


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

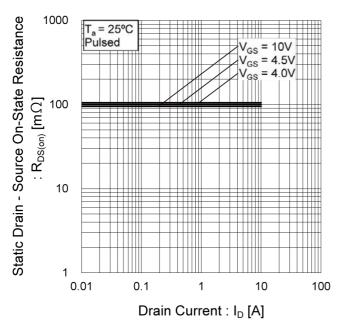


Fig.15 Static Drain - Source On - State
Resistance vs. Drain Current(II)

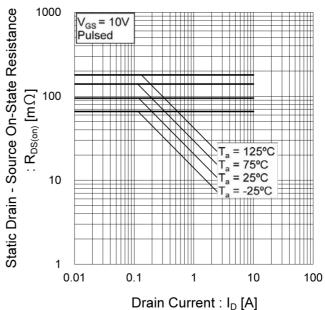


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

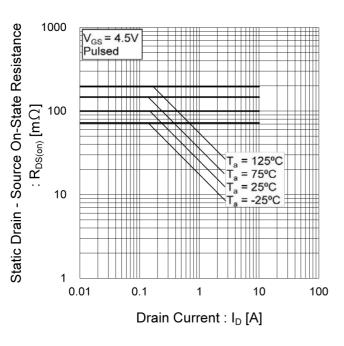


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)

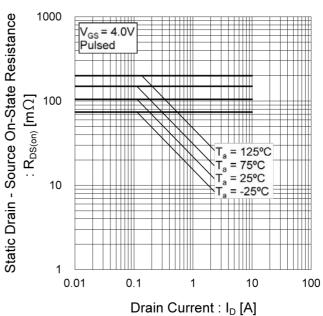
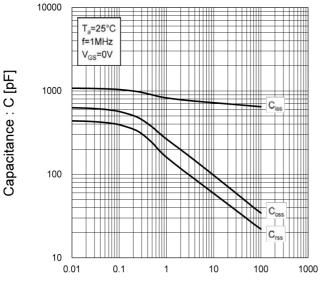


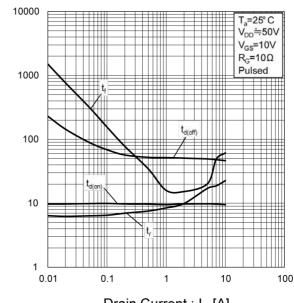
Fig.18 Typical Capacitance vs.

Drain - Source Voltage



Drain - Source Voltage : V_{DS} [V]

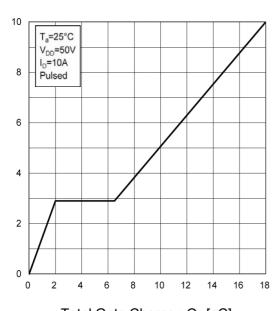
Fig.19 Switching Characteristics



Switching Time : t [ns]

Drain Current : I_D [A]

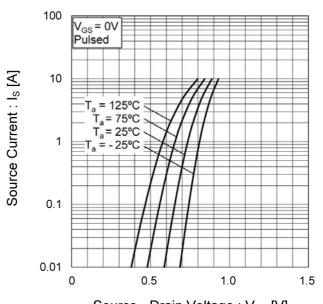
Fig.20 Dynamic Input Characteristics



Total Gate Charge: Qq [nC]

Fig.21 Source Current vs.

Source Drain Voltage



Source - Drain Voltage : V_{SD} [V]

Gate - Source Voltage : V_{GS} [V]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

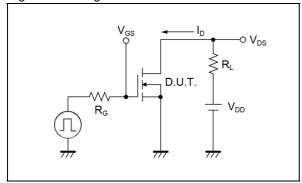


Fig.2-1 Gate Charge Measurement Circuit

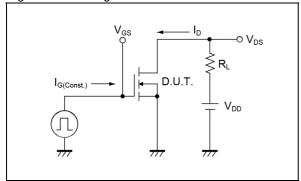


Fig.1-2 Switching Waveforms

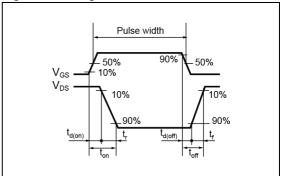
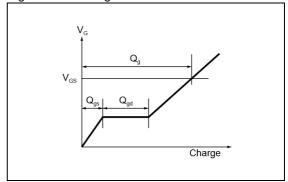
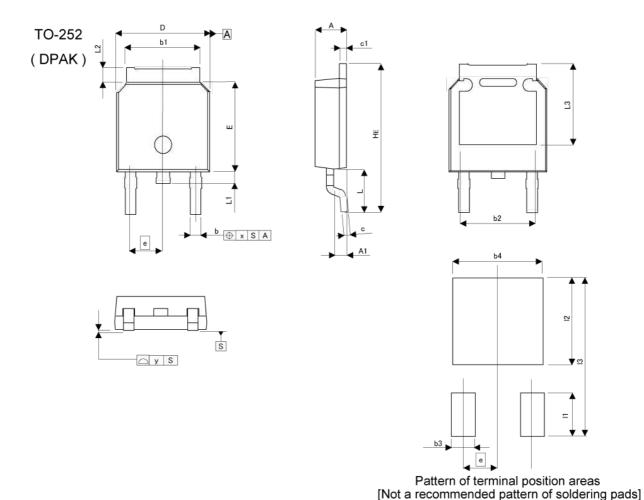


Fig.2-2 Gate Charge Waveform



Dimensions



INCHES MILIMETERS DIM MIN MAX MIN MAX 2.10 0.083 0.091 Α 2.30 A1 0.70 1.10 0.028 0.043 b 0.65 0.85 0.026 0.033 0.213 5.10 5.40 b1 0.201 b2 5.10 0.201 0.40 0.60 0.016 0.024 C 0.40 0.60 0.016 0.024 c1 D 6.40 6.80 0.252 0.268 е 0.236 6.00 6.40 0.252 E HE 9.50 10.50 0.374 0.413 0.114 0.70 0.028 L1 0.90 0.035 0.70 0.028 L2 1.30 0.051 L3 0.209 0.10 0.004 X у 0.10 0.004

MILIMETERS **INCHES** DIM MIN MAX MIN MAX b3 1.10 0.043 5.40 0.213 b4 11 2.90 0.114 12 5.50 0.217 13 10.50 0.413

Dimension in mm/inches



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| ſ | JAPAN USA | | EU | CHINA |
|---|-----------|-----------|------------|--------|
| Ī | CLASSⅢ | CL ACCIII | CLASS II b | СГУССШ |
| ſ | CLASSIV | CLASSⅢ | CLASSⅢ | CLASSⅢ |

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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