



74LV07A

HEX BUFFERS WITH OPEN DRAIN OUTPUTS

Description

The 74LV07A provides provides six independent buffers with open drain outputs. The device is designed for operation with a power supply range of 2.0V to 5.5V.

The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output preventing damaging current backflow when the device is powered down.

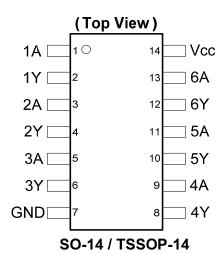
The gates perform the Boolean function:

Y = A

Features

- Wide Supply Voltage Range from 2.0V to 5.5V
- Sinks 12mA at V_{CC} = 4.5V
- CMOS low power consumption
- IOFF Supports Partial -Power Down Operation
- Inputs or Outputs accept up to 5.5V
- Inputs can be driven by 3.3V or 5V allowing for voltage translation applications.
- Schmitt Trigger Action at All Inputs
- ESD Protection Tested per JESD 22
 - Exceeds 200-V Machine Model (A115)
 - Exceeds 2000-V Human Body Model (A114)
 - Exceeds 1000-V Charged Device Model (C101)
 - Latch-Up Exceeds 100mA per JESD 78, Class I
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



Applications

- General Purpose Logic
- Power Down Signal Isolation
- Wide array of products such as:
 - PCs, networking, notebooks, ultrabooks, netbooks
 - Computer peripherals, hard drives, CD/DVD ROM
 - TV, DVD, DVR, set top box

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

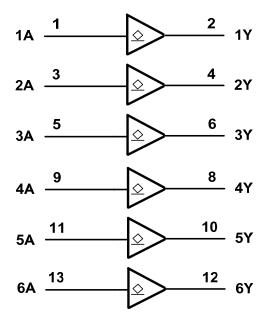
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Pin Descriptions

Pin Number	Pin Name	Description
1	1A	Data Input
2	1Y	Data Output
3	2A	Data Input
4	2Y	Data Output
5	3A	Data Input
6	3Y	Data Output
7	GND	Ground
8	4Y	Data Output
9	4A	Data Input
10	5Y	Data Output
11	5A	Data Input
12	6Y	Data Output
13	6A	Data Input
14	Vcc	Supply Voltage

Logic Diagram



Function Table

Input	Output
Α	Y
Н	Z
L	L

Absolute Maximum Ratings (Note 4) (@TA = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
V _{CC}	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range (Note 4)	-0.5 to +7.0	V
I _{IK}	Input Clamp Current V _I < 0V	-20	mA
Іок	Output Clamp Current V _O < 0V	-50	mA
Io	Continuous Output Current -0.5V < V _O V _{CC} +0.5V	- 25	mA
Icc	Continuous Current Through V _{CC}	50	mA
I _{GND}	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
P _{TOT}	Total Power Dissipation	500	mW

Note: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.



Recommended Operating Conditions (Note 5) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit	
Vcc	Supply Voltage	_	2.0	5.5	V	
VI	Input Voltage	_	0	5.5	V	
Vo	Output Voltage	_	0	5.5	V	
	Low-Level Output Current	2.0V	_	50	μΑ	
		2.3V to 2.7V	_	2	mA	
l _{OL}		3.0V to 3.6V	_	6	mA	
		4.5V to 5.5V	_	12	mA	
		2.3V to 2.7V	_	200		
Δt/ΔV	Input Transition Rise or Fall Rate	3.0V to 3.6V	_	100	ns/V	
		4.5V to 5.5V	_	20		
T _A	Operating Free-Air Temperature	_	-40	125	°C	

Note:

5. Unused inputs should be held at $\ensuremath{V_{\text{CC}}}$ or Ground.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Test Conditions	V	T _A = -40°(C to +85°C	T _A = -40°C	to +125°C	Unit
Syllibol	Parameter	rest Conditions	V _{cc}	Min	Max	Min	Max	UIIIL
		_	2.0V	1.5	_	1.5	_	
.,	High-Level Input	_	2.3V to 2.7V	V _{CC} X 0.7	_	V _{CC} X 0.7	_	V
V _{IH}	Voltage	_	3.0V to 3.6V	V _{CC} X 0.7	_	V _{CC} X 0.7	_	
		_	4.5V to 5.5V	V _{CC} X 0.7	_	V _{CC} X 0.7	_	_
		_	2.0V	_	0.5	_	0.5	
	Low-Level Input	_	2.3V to 2.7V	_	V _{CC} X 0.3	_	V _{CC} X 0.3	V
V _{IL} Voltage	_	3.0V to 3.6V	_	V _{CC} X 0.3	_	V _{CC} X 0.3		
		_	4.5V to 5.5V	_	V _{CC} X 0.3	_	V _{CC} X 0.3	_
		I _{OL} = 50μA	2.0V to 5.5V	_	0.1	_	0.1	
.,	Low-Level Output	I _{OL} = 2mA	2.3V	_	0.4	_	0.4	V
V _{OL}	Voltage	I _{OL} = 6mA	3.0V	_	0.44	_	0.44	V
		I _{OL} = 12mA	4.5V	_	0.55	_	0.55	
l _{OFF}	Power Down Leakage Current	V_1 or $V_0 = 0$ to 5.5V	0V	_	5	_	5	μΑ
II	Input Current	V _I =GND or 5.5V	0 to 5.5V		±1	_	±1	μA
Icc	Supply Current	$V_I = GND \text{ or } V_{CC}$ $I_O = 0$	5.5V	_	20	_	20	μΑ



Switching Characteristics

 $V_{CC} = 2.5V \pm 0.2V$

Symbol	Parameter	Test Conditions	Т	T _A = +25°C		-40°C to +85°C		-40°C to +125°C		Unit
Symbol	Farailletei	rest Conditions	Min	Тур	Max	Min	Max	Min	Max	Oiiit
t _{PLZ}		Figure 1	_	6.6	10.4	1	13	1	13	ns
t _{PZL}	Propagation Delay A _N	$C_L = 15pF$	_	7.5	10.4	1	13	1	13	115
t_{PLZ}	to Y _N	Figure 1	_	11.1	15.2	1	18	1	18	no
t _{PZL}		C _L = 50pF	_	9.6	15.2	1	18	1	18	ns

 V_{CC} = 3.3V ± 03 V

Symbol	Symbol Parameter	Test Conditions	Т	T _A = +25°C		-40°C to +85°C		-40°C to +125°C		Unit
Syllibol		rest Conditions	Min	Тур	Max	Min	Max	Min	Max	Onit
t _{PLZ}		Figure 1	_	5	7.1	1	8.5	1	8.5	20
t _{PZL}	Propagation Delay A _N	$C_L = 15pF$	_	5	7.1	1	8.5	1	8.5	ns
t _{PLZ}	to Y _N	Figure 1	_	8.2	10.6	1	12	1	12	
t _{PZL}		$C_L = 50pF$		6.6	10.6	1	12	1	12	ns

 $V_{CC} = 5.0V \pm 0.5V$

Symbol	Parameter	Test Conditions	1	T _A = +25°C		-40°C to +85°C		-40°C to +125°C		Unit
Symbol	Parameter	rest Conditions	Min	Тур	Max	Min	Max	Min	Max	Ullit
t_{PLZ}		Figure 1	_	3.8	5.5	1	6.5	1	6.5	ns
t_{PZL}	Propagation Delay A _N to	$C_L = 15pF$	_	3.4	5.5	1	6.5	1	6.5	115
t_{PLZ}	Y _N	Figure 1	_	5.7	7.5	1	8.5	1	8.5	2
t _{PZL}		$C_L = 50 pF$	_	4.5	7.5	1	8.5	1	8.5	ns

Operating Characteristics

T_A = +25°C

	Parameter	Test Conditions	V _{CC}	Тур	Unit
_	Power Dissipation	f = 10MHz	3.3V	2.9	۲
C_{pd}	Capacitance per Gate	$C_L = 50pF$	5.0V	5.3	pF

Noise Characteristics

 $V_{CC} = 3V$, $C_L = 50pF$, $T_A = +25^{\circ}C$

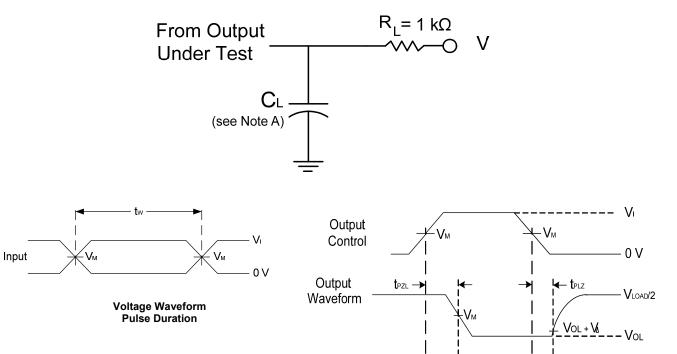
Symbol	Parameter	Min	Тур	Max	Unit
$V_{OL(p)}$	Quiet output, maximum dynamic V _{OL}	_	0.2	0.8	V
V _{OL(V)}	Quiet output, minimum dynamic V _{OL}	_	-0.1	-0.8	V
V _{OH(V)}	Quiet output, minimum dynamic V _{OH}	_	3.1	_	V
V _{IH(D)}	High Level dynamic input voltage	2.31	_	_	V
V _{IL(D)}	Low Level dynamic input voltage	_	_	0.99	V

Package Characteristics

Symbol	Parameter	Test Conditions	Vcc	Min	Тур	Max	Unit
C _i	Input Capacitance	$V_i = V_{CC} - \text{ or GND}$	2.0 to 5.5V		3.3	10	pF



Parameter Measurement Information



Voltage Waveform Propagation Delay Times

A. Includes test lead and test apparatus capacitance. Notes:

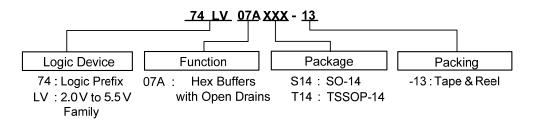
- B. All pulses are supplied at pulse repetition rate ≤ 10 MHz.
- C. The inputs are measured one at a time with one transition per measurement. D. For the open drain device t_{PLZ} and t_{PZL} are the same as t_{PD} .

- E. t_{PZL} is measured at V_{M} . D. t_{PLZ} is measured at V_{OL} + V_{Δ} where V_{Δ} = 0.3V.

Figure 1 Load Circuit and Voltage Waveforms



Ordering Information



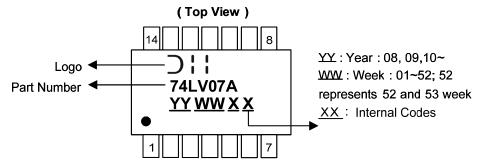
Dovice	Backage Code	Packaging	13" Tape and Reel			
Device	Device Package Code (Note 6)		Quantity	Part Number Suffix		
74LV07AS14-13	S14	SO-14	2500/Tape & Reel	-13		
74LV07AT14-13	T14	TSSOP-14	2500/Tape & Reel	-13		

Note:

6. The taping orientation and tape details can be found at http://www.diodes.com/datasheets/ap02007.pdf

Marking Information

(1) SO14, TSSOP14



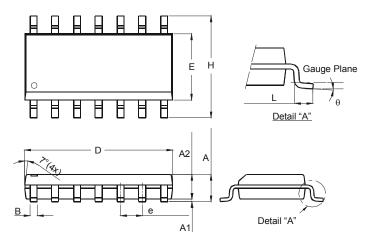
Part Number	Package
74LV07AS14	SO-14
74LV07AT14	TSSOP-14



Package Outline Dimensions (All Dimensions in mm)

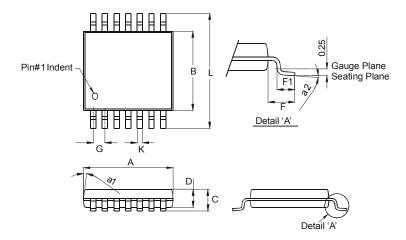
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

Package Type: SO-14



SO-14				
Dim	Min	Max		
Α	1.47	1.73		
A 1	0.10	0.25		
A2	1.45 Typ			
В	0.33	0.51		
D	8.53	8.74		
Е	3.80	3.99		
е	1.27 Typ			
Η	5.80	6.20		
L	0.38	1.27		
θ	0°	8°		
All Dimensions in mm				

Package Type: TSSOP-14



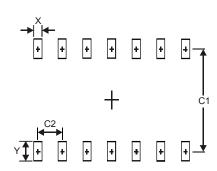
TSSOP-14			
Dim	Min	Max	
a1	7° (4X)		
a2	0°	8°	
Α	4.9	5.10	
В	4.30	4.50	
С		1.2	
D	8.0	1.05	
F	1.00 Typ		
F1	0.45	0.75	
G	0.65 Typ		
K	0.19	0.30	
L	6.40 Typ		
All Dimensions in mm			



Suggested Pad Layout

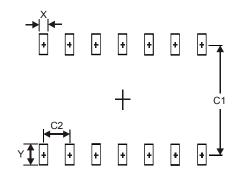
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

Package Type: SO-14



Dimensions	Value (in mm)
Х	0.60
Υ	1.50
C1	5.4
C2	1.27

Package Type: TSSOP-14



Dimensions	Value (in mm)
Х	0.45
Y	1.45
C1	5.9
Co	0.65



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