

N-channel TrenchMOS standard level FET 25 August 2014

**Product data sheet** 

### 1. General description

Standard level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product has been designed and qualified to the appropriate AEC standard for use in automotive critical applications.

### 2. Features and benefits

- AEC Q101 compliant
- Low conduction losses due to low on-state resistance
- Suitable for standard level gate drive sources
- Suitable for thermally demanding environments due to 175 °C rating

### 3. Applications

- 12 V and 24 V loads
- Automotive and general purpose power switching
- Motors, lamps and solenoids

### 4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> ≥ 25 °C; T <sub>j</sub> ≤ 175 °C	-	-	55	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 25 °C; <u>Fig. 2</u> ; <u>Fig. 3</u>	-	-	20.3	А
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; <u>Fig. 1</u>	-	-	62	W
Static chara	acteristics	·				
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 10 A; T <sub>j</sub> = 175 °C; Fig. 11; Fig. 12	-	-	150	mΩ
		V <sub>GS</sub> = 10 V; I <sub>D</sub> = 10 A; T <sub>j</sub> = 25 °C; Fig. 11; Fig. 12	-	64	75	mΩ
Avalanche	ruggedness		•			
E <sub>DS(AL)S</sub>	non-repetitive drain- source avalanche energy	$\begin{split} I_D &= 11 \text{ A};  \text{V}_{sup} \leq 55 \text{ V};  \text{R}_{GS} = 50  \Omega; \\ \text{V}_{GS} &= 10  \text{V};  \text{T}_{j(\text{init})} = 25 ^{\circ}\text{C}; \text{ unclamped} \end{split}$	-	-	30.3	mJ

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# 5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	D
2	D	drain		
3	S	source		G-UF4
mb	D	mounting base; connected to drain	D2PAK (SOT404)	mbb076 S

# 6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
BUK7675-55A	D2PAK	plastic single-ended surface-mounted package (D2PAK); 3 leads (one lead cropped)	SOT404			

### 7. Marking

Table 4. Marking codes	
Type number	Marking code
BUK7675-55A	BUK7675-55A

### 8. Limiting values

#### Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	$T_j \ge 25 \text{ °C}; T_j \le 175 \text{ °C}$	-	55	V
V <sub>DGR</sub>	drain-gate voltage	R <sub>GS</sub> = 20 kΩ	-	55	V
V <sub>GS</sub>	gate-source voltage		-20	20	V
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; <u>Fig. 1</u>	-	62	W
I <sub>D</sub>	drain current	T <sub>mb</sub> = 25 °C; V <sub>GS</sub> = 10 V; <u>Fig. 2; Fig. 3</u>	-	20.3	А
		T <sub>mb</sub> = 100 °C; V <sub>GS</sub> = 10 V; <u>Fig. 2</u>	-	14.3	А
I <sub>DM</sub>	peak drain current	$T_{mb}$ = 25 °C; pulsed; $t_p \le 10 \ \mu$ s; Fig. 3	-	81	А
T <sub>stg</sub>	storage temperature		-55	175	°C
Tj	junction temperature		-55	175	°C
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# **BUK7675-55A**

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Symbol	Parameter	Conditions		Min	Max	Unit
Source-drain diode						
l <sub>S</sub>	source current	T <sub>mb</sub> = 25 °C		-	20.3	А
I <sub>SM</sub>	peak source current	pulsed; $t_p \le 10 \ \mu s$ ; $T_{mb} = 25 \ ^\circ C$		-	81	А
Avalanche ruggedness						
E <sub>DS(AL)S</sub>	non-repetitive drain-source avalanche energy	$\begin{split} I_D &= 11 \text{ A};  \text{V}_{\text{sup}} \leq 55 \text{ V};  \text{R}_{\text{GS}} = 50  \Omega; \\ \text{V}_{\text{GS}} &= 10  \text{V};  \text{T}_{\text{j(init)}} = 25 ^{\circ}\text{C}; \text{ unclamped} \end{split}$		-	30.3	mJ

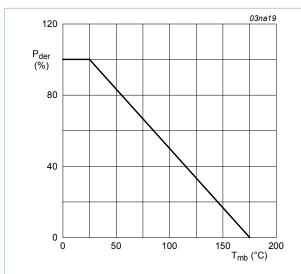
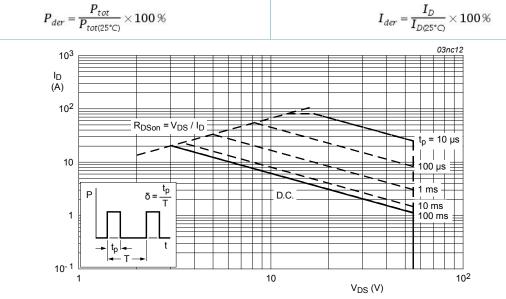
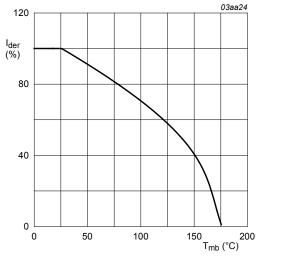


Fig. 1. Normalized total power dissipation as a function of mounting base temperature

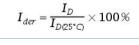




 $T_{mb} = 25^{\circ}C; I_{DM}$  is single pulse



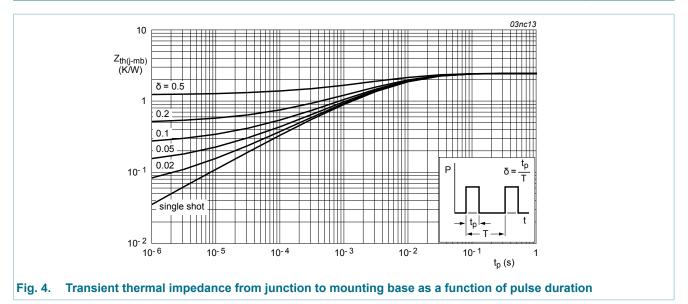




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## 9. Thermal characteristics

Table 6. Th	ermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base	Fig. <u>4</u>	-	-	2.4	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	mounted on printed-circuit board; minimum footprint	-	50	-	K/W



# **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics	· · · ·	1			
V <sub>(BR)DSS</sub> drain-source breakdown voltage		I <sub>D</sub> = 0.25 mA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	55	-	-	V
	I <sub>D</sub> = 0.25 mA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = -55 °C	50	-	-	V	
00(11)	gate-source threshold voltage	I <sub>D</sub> = 1 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 175 °C; Fig. 10	1	-	-	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C};$ Fig. 10	2	3	4	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C};$ Fig. 10	-	-	4.4	V
I <sub>DSS</sub>	drain leakage current	$V_{DS}$ = 55 V; $V_{GS}$ = 0 V; $T_j$ = 175 °C	-	-	500	μA
		V <sub>DS</sub> = 55 V; V <sub>GS</sub> = 0 V; T <sub>i</sub> = 25 °C	-	0.05	10	μA

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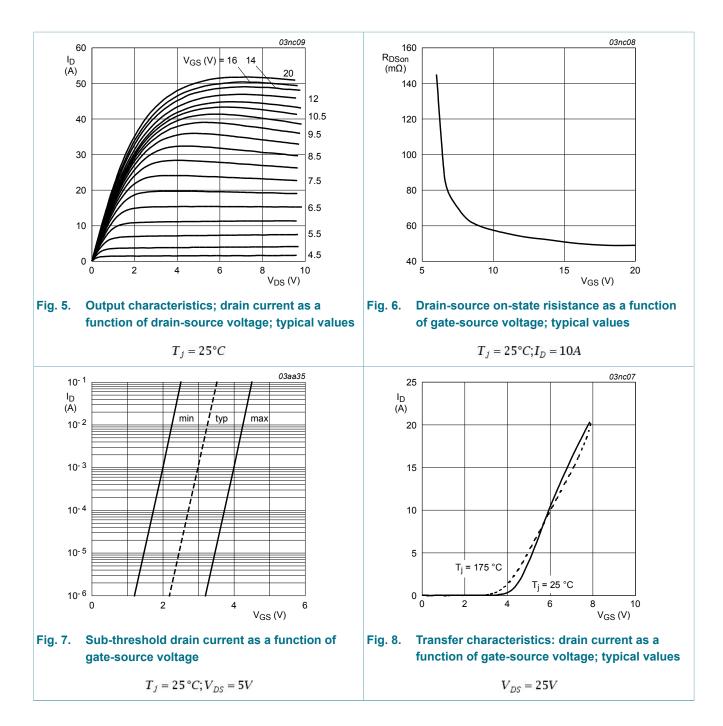
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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = 20 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	2	100	nA
		$V_{GS}$ = -20 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	2	100	nA
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 10 A; T <sub>j</sub> = 175 °C; Fig. 11; Fig. 12	-	-	150	mΩ
		$V_{GS} = 10 \text{ V}; \text{ I}_D = 10 \text{ A}; \text{ T}_j = 25 \text{ °C};$ Fig. 11; Fig. 12	-	64	75	mΩ
Dynamic cl	haracteristics		I			
C <sub>iss</sub>	input capacitance	T - 25 °C; Fig. 12		320	483	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C; <u>Fig. 13</u>	-	92	113	pF
C <sub>rss</sub>	reverse transfer capacitance		-	64	90	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 30 V; R <sub>L</sub> = 1.2 Ω; V <sub>GS</sub> = 10 V; R <sub>G(ext)</sub> = 10 Ω; T <sub>j</sub> = 25 °C	-	10	-	ns
t <sub>r</sub>	rise time		-	50	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	70	-	ns
t <sub>f</sub>	fall time		-	40	-	ns
L <sub>D</sub>	internal drain inductance	from upper edge of drain mounting base to centre of die; $T_j = 25 \text{ °C}$	-	2.5	-	nH
		from drain lead 6 mm from package to centre of die; $T_j = 25 \degree C$	-	4.5	-	nH
L <sub>S</sub>	internal source inductance	from source lead to source bond pad; T <sub>j</sub> = 25 °C	-	7.5	-	nH
Source-dra	in diode					
V <sub>SD</sub>	source-drain voltage	$I_{S}$ = 15 A; $V_{GS}$ = 0 V; $T_{j}$ = 25 °C; <u>Fig. 14</u>	-	0.85	1.2	V
t <sub>rr</sub>	reverse recovery time	I <sub>S</sub> = 20 A; dI <sub>S</sub> /dt = -100 A/μs;	-	32	-	ns
Q <sub>r</sub>	recovered charge	$V_{GS}$ = -10 V; $V_{DS}$ = 30 V; $T_j$ = 25 °C	-	120	-	nC

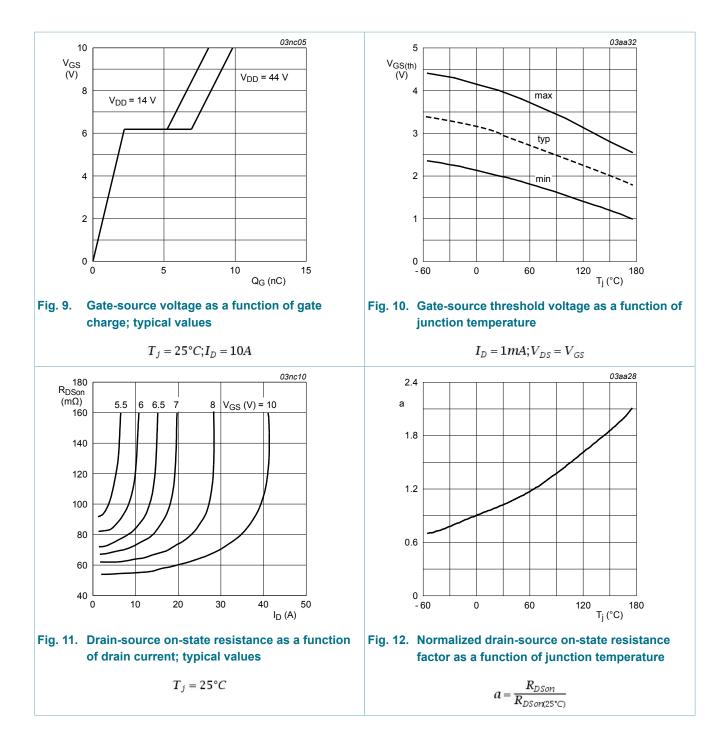
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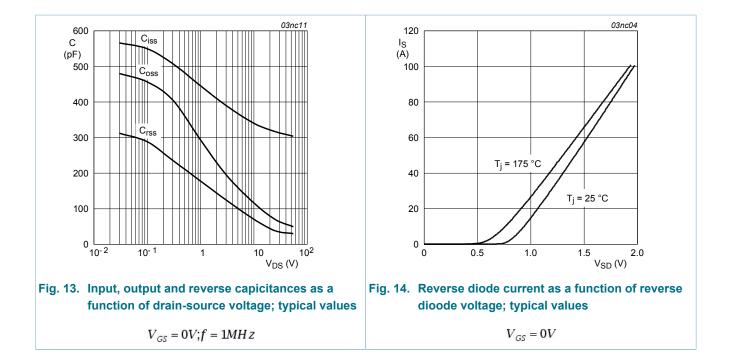
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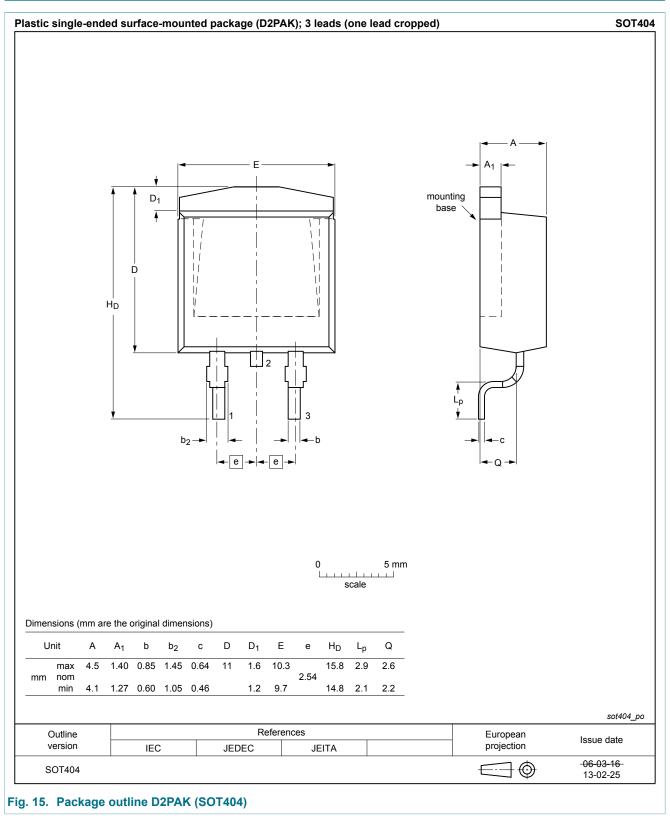
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# 11. Package outline



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### 12. Legal information

#### 12.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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