

# ON Semiconductor® ISL9V5036S3S / ISL9V5036P3 / ISL9V5036S3

EcoSPARK® 500mJ, 360V, N-Channel Ignition IGBT

### **General Description**

## Applications

Automotive Ignition Coil Driver CircuitsCoil-On Plug Applications

The ISL9V5036S3S, ISL9V5036P3, and ISL9V5036S3 are the next generation IGBTs that offer outstanding SCIS capability in the D<sup>2</sup>-Pak (TO-263) and TO-220 plastic package. These devices are intended for use in automotive ignition circuits, specifically as coil drivers. Internal diodes provide voltage clamping without the need for external components.

**EcoSPARK®** devices can be custom made to specific clamp voltages. Contact your nearest ON Semiconductor sales office for more information.

Features

- Industry Standard D<sup>2</sup>-Pak package
- SCIS Energy = 500mJ at  $T_J = 25^{\circ}C$
- Logic Level Gate Drive
- Qualified to AEC Q101
- RoHS Compliant

Formerly Developmental Type 49443



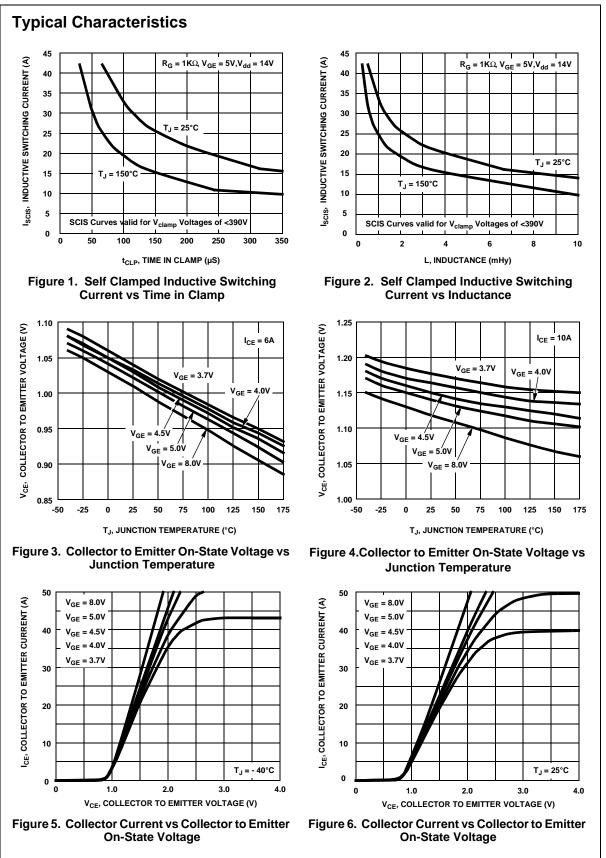
#### Package Symbol COLLECTOR JEDEC TO-263AB JEDEC TO-262AA JEDEC TO-220AB <sup>E</sup>c<sub>G</sub> D<sup>2</sup>-Pak <sup>Е</sup>с<sub>б</sub> GATE R, Е COLLECTOR COLLECTOR (FLANGE) (FLANGE)

# Device Maximum Ratings T<sub>A</sub> = 25°C unless otherwise noted

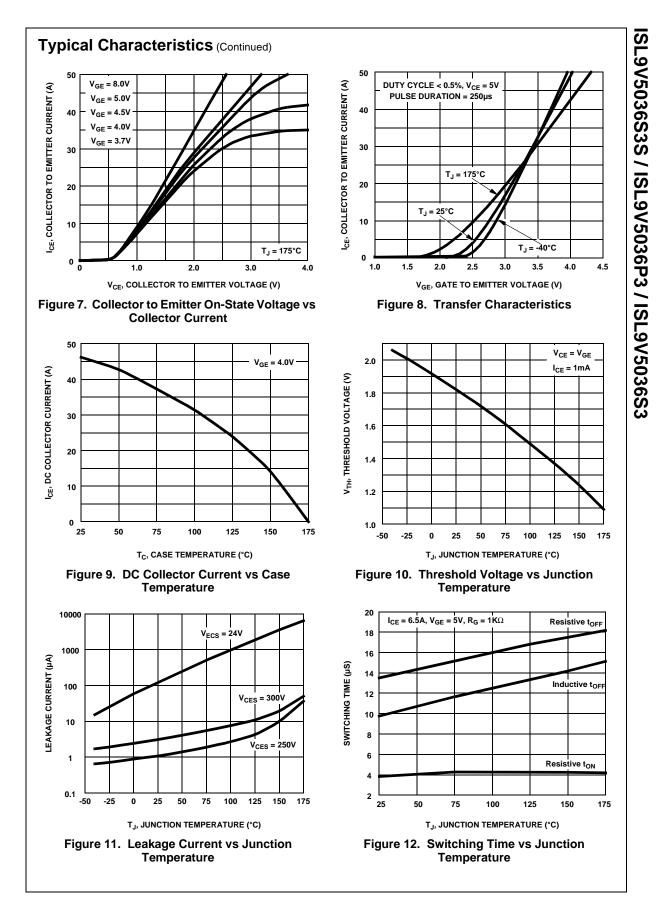
Symbol	Parameter	Ratings	Units
BV <sub>CER</sub>	Collector to Emitter Breakdown Voltage (I <sub>C</sub> = 1 mA)	390	V
BV <sub>ECS</sub>	Emitter to Collector Voltage - Reverse Battery Condition (I <sub>C</sub> = 10 mA)	24	V
E <sub>SCIS25</sub>	At Starting $T_J = 25^{\circ}$ C, $I_{SCIS} = 38.5$ A, L = 670 $\mu$ Hy	500	mJ
E <sub>SCIS150</sub>	At Starting $T_J = 150^{\circ}$ C, $I_{SCIS} = 30$ A, $L = 670 \mu$ Hy	300	mJ
I <sub>C25</sub>	Collector Current Continuous, At T <sub>C</sub> = 25°C, See Fig 9	46	Α
I <sub>C110</sub>	Collector Current Continuous, At T <sub>C</sub> = 110°C, See Fig 9	31	Α
V <sub>GEM</sub>	Gate to Emitter Voltage Continuous	±10	V
PD	Power Dissipation Total $T_C = 25^{\circ}C$	250	W
	Power Dissipation Derating $T_{C} > 25^{\circ}C$	1.67	W/°C
ТJ	Operating Junction Temperature Range	-40 to 175	°C
T <sub>STG</sub>	Storage Junction Temperature Range	-40 to 175	°C
ΤL	Max Lead Temp for Soldering (Leads at 1.6mm from Case for 10s)	300	°C
T <sub>pkg</sub>	Max Lead Temp for Soldering (Package Body for 10s)	260	°C
ESD	Electrostatic Discharge Voltage at 100pF, 1500 $\Omega$	4	kV

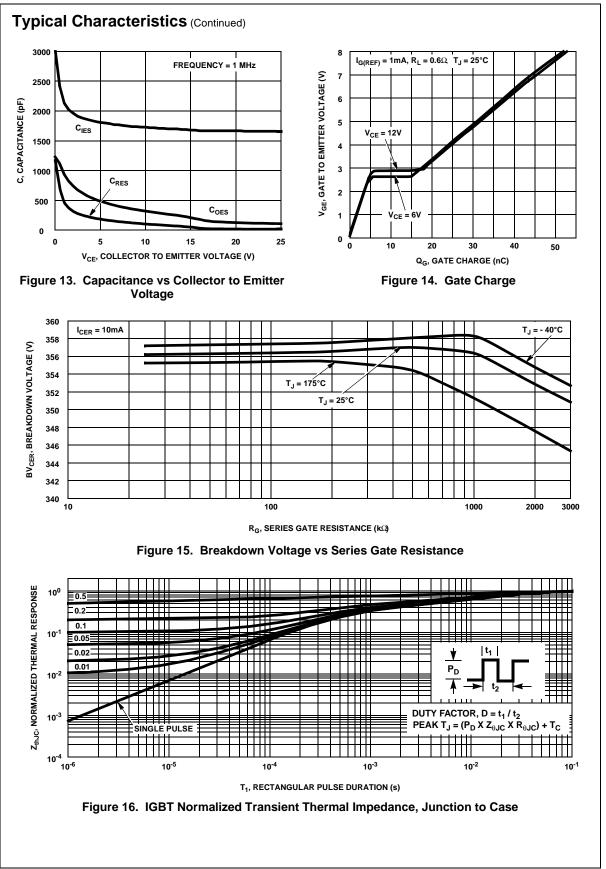
Device Marking		g Device		Package	Reel Size	e	Tape Wic	lth	Quantity
V5036S		ISL9V5036S3ST		TO-263AB	330mm		24mm		800
V50	36P	ISL9V5036P3		TO-220AA	Tube		N/A		50
V5036S		ISL9V5036S3		TO-262AA	Tube	N/A			50
V5036S		ISL9V5036S3S		TO-263AB Tube			N/A		50
	al Char	acteristics T <sub>A</sub> = 25	5°C un	less otherwise n	oted				
Symbol		Parameter		Test Con	1	Min	Тур	Мах	Units
f State	Characte	eristics							
BV <sub>CER</sub>	Collector	to Emitter Breakdown Vo	ltage	I <sub>C</sub> = 2mA, V <sub>GE</sub> = R <sub>G</sub> = 1KΩ, See T <sub>J</sub> = -40 to 150°	e Fig. 15	330	360	390	V
BV <sub>CES</sub>	Collector	Collector to Emitter Breakdown Voltage		$I_{C} = 10mA, V_{GE} = 0,$ $R_{G} = 0, See Fig. 15$ $T_{J} = -40 \text{ to } 150^{\circ}\text{C}$		360	390	420	V
BV <sub>ECS</sub>	Emitter to	Collector Breakdown Vo	oltage	$I_{C} = -75$ mA, $V_{GE} = 0$ V, $T_{C} = 25$ °C		30	-	-	V
$BV_{GES}$	Gate to E	mitter Breakdown Voltag	е	$I_{GES} = \pm 2mA$		±12	±14	-	V
I <sub>CER</sub>	Collector	to Emitter Leakage Curre	ent	$V_{CER} = 250V,$	$T_{C} = 25^{\circ}C$	-		25	μA
				R <sub>G</sub> = 1KΩ See Fig. 11	T <sub>C</sub> = 150°C	-	-	1	mA
I <sub>ECS</sub>	Emitter to	Collector Leakage Curre	Emitter to Collector Leakage Current		$T_{C} = 25^{\circ}C$	-	-	1	mA
	1						-	10	mA
	-			Fig. 11	T <sub>C</sub> = 150°C	-		40	
R <sub>1</sub> R <sub>2</sub>		ite Resistance mitter Resistance		Fig. 11	1 <sub>C</sub> = 150°C	- - 10K	- 75	40 - 30K	Ω Ω
R <sub>2</sub>	Gate to E	mitter Resistance	tage		$T_{\rm C} = 150^{\circ}{\rm C}$ $T_{\rm C} = 25^{\circ}{\rm C},$ See Fig. 4	-	75	-	Ω
R <sub>2</sub>	Gate to El	mitter Resistance	-	I <sub>C</sub> = 10A,	T <sub>C</sub> = 25°C,	-	75	- 30K	Ω Ω V
R <sub>2</sub> n State ( V <sub>CE(SAT)</sub>	Gate to El	mitter Resistance eristics to Emitter Saturation Vol to Emitter Saturation Vol	-	I <sub>C</sub> = 10A, V <sub>GE</sub> = 4.0V I <sub>C</sub> = 15A,	T <sub>C</sub> = 25°C, See Fig. 4	- 10K -	75 - 1.17	- 30K 1.60	Ω Ω V
R2           n State           VCE(SAT)           VCE(SAT)           /namic           QG(ON)	Gate to E	mitter Resistance eristics to Emitter Saturation Vol to Emitter Saturation Vol eristics	-	$I_{C} = 10A, V_{GE} = 4.0V$ $I_{C} = 15A, V_{GE} = 4.5V$ $I_{C} = 10A, V_{CE} = V_{GE} = 5V, See$	$T_{C} = 25^{\circ}C,$ See Fig. 4 $T_{C} = 150^{\circ}C$ 12V, Fig. 14	- 10K -	75 - 1.17	- 30K 1.60	Ω Ω V
R <sub>2</sub> n State ( V <sub>CE(SAT)</sub> V <sub>CE(SAT)</sub>	Gate to Ei	mitter Resistance eristics to Emitter Saturation Vol to Emitter Saturation Vol eristics	tage	$I_{C} = 10A, V_{GE} = 4.0V$ $I_{C} = 15A, V_{GE} = 4.5V$ $I_{C} = 10A, V_{CE} = V_{GE} = 5V, See$ $I_{C} = 1.0mA,$	$T_{C} = 25^{\circ}C,$ See Fig. 4 $T_{C} = 150^{\circ}C$ = 12V, Fig. 14 $T_{C} = 25^{\circ}C$	- 10K -	75 - 1.17 1.50	- 30K 1.60	Ω           Ω           Ω           V           V
$R_{2}$ n State (VCE(SAT)) $VCE(SAT)$ $VCE(SAT)$ $Q_{G(ON)}$ $V_{GE(TH)}$	Gate to Ei Characte Collector f Collector f Characte Gate Cha Gate to E	mitter Resistance eristics to Emitter Saturation Vol to Emitter Saturation Vol eristics rge mitter Threshold Voltage	tage	$I_{C} = 10A, V_{GE} = 4.0V$ $I_{C} = 15A, V_{GE} = 4.5V$ $I_{C} = 10A, V_{CE} = V_{GE} = 5V, See I$ $I_{C} = 1.0MA, V_{CE} = V_{GE}, See Fig. 10$	$T_{C} = 25^{\circ}C,$ See Fig. 4 $T_{C} = 150^{\circ}C$ = 12V, = 12V, = 12V, = 12V, = 150^{\circ}C $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$	- 10K - - 1.3 0.75	75 - 1.17 1.50 32 - -	- 30K 1.60 1.80 - - 2.2 1.8	Ω           Ω           Ω           V           V           nC           V           V
R2           n State           VCE(SAT)           VCE(SAT)           /namic           QG(ON)	Gate to Ei Characte Collector f Collector f Characte Gate Cha Gate to E	mitter Resistance eristics to Emitter Saturation Vol to Emitter Saturation Vol eristics rge	tage	$I_{C} = 10A, V_{GE} = 4.0V$ $I_{C} = 15A, V_{GE} = 4.5V$ $I_{C} = 10A, V_{CE} = V_{GE} = 5V, See$ $I_{C} = 1.0mA, V_{CE} = V_{GE}, V_{CE} = V_{GE}, V_{CE} = V_{GE}, V_{CE} = V_{CE}, V_{CE}, V_{CE} = V_{CE}, V_{CE} = V_{CE}, V_{CE} = V_{CE}, V$	$T_{C} = 25^{\circ}C,$ See Fig. 4 $T_{C} = 150^{\circ}C$ = 12V, Fig. 14 $T_{C} = 25^{\circ}C$	- 10K - - - 1.3	75 - 1.17 1.50 32	- 30K 1.60 1.80	Ω Ω V V V
R2           n State           VCE(SAT)           VCE(SAT)           VCE(SAT)           VGE(SAT)           VGE(ON)           VGE(TH)           VGEP	Gate to Ei Characte Collector f Collector f Characte Gate Cha Gate to E	mitter Resistance eristics to Emitter Saturation Vol to Emitter Saturation Vol eristics rge mitter Threshold Voltage	tage	$I_{C} = 10A, V_{GE} = 4.0V$ $I_{C} = 15A, V_{GE} = 4.5V$ $I_{C} = 10A, V_{CE} = V_{GE} = 5V, See I$ $I_{C} = 1.0MA, V_{CE} = V_{GE}, See Fig. 10$	$T_{C} = 25^{\circ}C,$ See Fig. 4 $T_{C} = 150^{\circ}C$ = 12V, = 12V, = 12V, = 12V, = 150^{\circ}C $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$	- 10K - - 1.3 0.75	75 - 1.17 1.50 32 - -	- 30K 1.60 1.80 - - 2.2 1.8	Ω           Ω           Ω           V           V           nC           V           V
R2           n State           VCE(SAT)           VCE(SAT)           VCE(SAT)           VGE(SAT)           VGE(ON)           VGE(TH)           VGEP	Gate to Ei Characte Collector f Collector f Characte Gate Cha Gate to E Gate to E Charact	mitter Resistance eristics to Emitter Saturation Vol to Emitter Saturation Vol eristics rge mitter Threshold Voltage	tage	$I_{C} = 10A, V_{GE} = 4.0V$ $I_{C} = 15A, V_{GE} = 4.5V$ $I_{C} = 10A, V_{CE} = V_{GE} = 5V, See I$ $I_{C} = 1.0MA, V_{CE} = V_{GE}, See Fig. 10$	$T_{C} = 25^{\circ}C$ , See Fig. 4 $T_{C} = 150^{\circ}C$ Fig. 14 $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$ $V_{CE} = 12V$	- 10K - - 1.3 0.75	75 - 1.17 1.50 32 - -	- 30K 1.60 1.80 - - 2.2 1.8	Ω           Ω           Ω           V           V           nC           V           V
R <sub>2</sub> n State ( V <sub>CE(SAT)</sub> V <sub>CE(SAT)</sub> ( Namic ( Q <sub>G(ON)</sub> ) V <sub>GE(TH)</sub> V <sub>GEP</sub>	Gate to Ei Characte Collector f Collector f Characte Gate Cha Gate to E Gate to E Gate to E Charact	mitter Resistance eristics to Emitter Saturation Vol to Emitter Saturation Vol eristics rge mitter Threshold Voltage mitter Plateau Voltage teristics	tage	$I_{C} = 10A, V_{GE} = 4.0V$ $I_{C} = 15A, V_{GE} = 4.5V$ $I_{C} = 10A, V_{CE} = V_{GE} = 5V, See I$ $I_{C} = 1.0mA, V_{CE} = V_{GE}, See Fig. 10$ $I_{C} = 10A,$	$T_{C} = 25^{\circ}C$ , See Fig. 4 $T_{C} = 150^{\circ}C$ Fig. 14 $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$ $V_{CE} = 12V$ = 1Ω 1KΩ	- - - 1.3 0.75 -	75 - 1.17 1.50 32 - - 3.0	- 30K 1.60 1.80 - 2.2 1.8 -	Ω           Ω           Ω           V           V           NC           V           V           V           V           V
$R_2$ n State of VCE(SAT) VCE(SAT) VCE(SAT) VCE(SAT) VGE(ON) VGE(TH) VGEP vitching t <sub>d(ON)R</sub>	Gate to Ei Characte Collector f Collector f Collector f Characte Gate Cha Gate to E Gate to E Charact Current Ti Current R	mitter Resistance eristics to Emitter Saturation Vol to Emitter Saturation Vol eristics rge mitter Threshold Voltage mitter Plateau Voltage teristics urn-On Delay Time-Resi	stive	$I_{C} = 10A, V_{GE} = 4.0V$ $I_{C} = 15A, V_{GE} = 4.5V$ $I_{C} = 10A, V_{CE} = V_{GE} = 5V, See I$ $I_{C} = 1.0mA, V_{CE} = V_{GE}, See Fig. 10$ $I_{C} = 10A,$ $V_{CE} = 14V, R_{L} = V_{GE} = 5V, R_{G} = T_{J} = 25^{\circ}C, See$ $V_{CE} = 300V, L = 100$	$T_{C} = 25^{\circ}C,$ See Fig. 4 $T_{C} = 150^{\circ}C$ Fig. 14 $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$ $V_{CE} = 12V$ = 1Ω 1KΩ Fig. 12 = 2mH,	- - - 1.3 0.75 -	75         -         1.17         1.50         32         -         3.0         0.7	- 30K 1.60 1.80 - 2.2 1.8 - -	Ω           Ω           Ω           V           V           NC           V           V           V           V           V           V           V           V           V           V           V           V           V           V           V           μs
$\begin{array}{c} R_2 \\ \hline R_2 \\ \hline$	Gate to Ei Characte Collector f Collector f Collector f Characte Gate Cha Gate to E Gate to E Gate to E Charact Current Ti Current Ti Current Ti Current Ti	mitter Resistance eristics to Emitter Saturation Vol- to Emitter Saturation Vol- to Emitter Saturation Vol- eristics rge mitter Threshold Voltage mitter Plateau Voltage teristics urn-On Delay Time-Resis- ise Time-Resistive urn-Off Delay Time-Induc- all Time-Inductive	stive	$\label{eq:constraint} \begin{array}{c} I_{C} = 10A, \\ V_{GE} = 4.0V \\ I_{C} = 15A, \\ V_{GE} = 15A, \\ V_{GE} = 5V, See \\ I_{C} = 1.0mA, \\ V_{CE} = V_{GE}, \\ See \\ Fig. 10 \\ I_{C} = 10A, \\ \end{array}$	$T_{C} = 25^{\circ}C,$ See Fig. 4 $T_{C} = 150^{\circ}C$ Fig. 14 $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$ $V_{CE} = 12V$ V <sub>CE</sub> = 12V = 1Ω, 1KΩ Fig. 12 = 2mH, 1KΩ Fig. 12	- 10K - - 1.3 0.75 - -	75         -         1.17         1.50         32         -         3.0         0.7         2.1	- 30K 1.60 1.80 - 2.2 1.8 - - 4 7	Ω           Ω           Ω           V           V           NC           V           V           V           V           V           V           V           V           V           V           V           V           V           V           V           μs
$R_{2}$ n State of VCE(SAT) VCE(SAT) VCE(SAT) VCE(SAT) VGE(ON) VGE(TH) VGEP vitching t <sub>d(ON)R</sub> t <sub>rR</sub> t <sub>d(OFF)L</sub>	Gate to Ei Characte Collector f Collector f Collector f Characte Gate Cha Gate to E Gate to E Gate to E Charact Current Ti Current Ti Current Ti Current Ti	mitter Resistance eristics to Emitter Saturation Vol to Emitter Saturation Vol eristics rge mitter Threshold Voltage mitter Plateau Voltage teristics urn-On Delay Time-Resi ise Time-Resistive urn-Off Delay Time-Indu	stive	$\label{eq:constraint} \begin{array}{c} I_{C} = 10A, \\ V_{GE} = 4.0V \\ I_{C} = 15A, \\ V_{GE} = 4.5V \\ \end{array} \\ \begin{array}{c} I_{C} = 10A, \\ V_{CE} = V_{GE}, \\ See \\ Fig. 10 \\ I_{C} = 10A, \\ \end{array} \\ \begin{array}{c} V_{CE} = 14V, \\ V_{CE} = V_{GE}, \\ See \\ Fig. 10 \\ I_{C} = 10A, \\ \end{array} \\ \begin{array}{c} V_{CE} = 14V, \\ V_{CE} = 5V, \\ See \\ V_{CE} = 5V, \\ R_{G} = \\ \end{array} \\ \begin{array}{c} V_{CE} = 300V, \\ L = \\ V_{GE} = 5V, \\ R_{G} = \\ \end{array} \\ \end{array}$	$T_{C} = 25^{\circ}C,$ See Fig. 4 $T_{C} = 150^{\circ}C$ Fig. 14 $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$ $V_{CE} = 12V$ $V_{CE} = 12V$ = 1Ω, 1KΩ Fig. 12 = 2mH, 1KΩ Fig. 12 = 2mH, 1KΩ Fig. 12 = 370 µH,	- - - - 1.3 0.75 - - -	75         -         1.17         1.50         32         -         3.0         0.7         2.1         10.8	- 30K 1.60 1.80 - 2.2 1.8 - - - 2.2 1.8 - - - - - - - - - - - - - - - - - - -	Ω           Ω           Ω           V           V           NC           V

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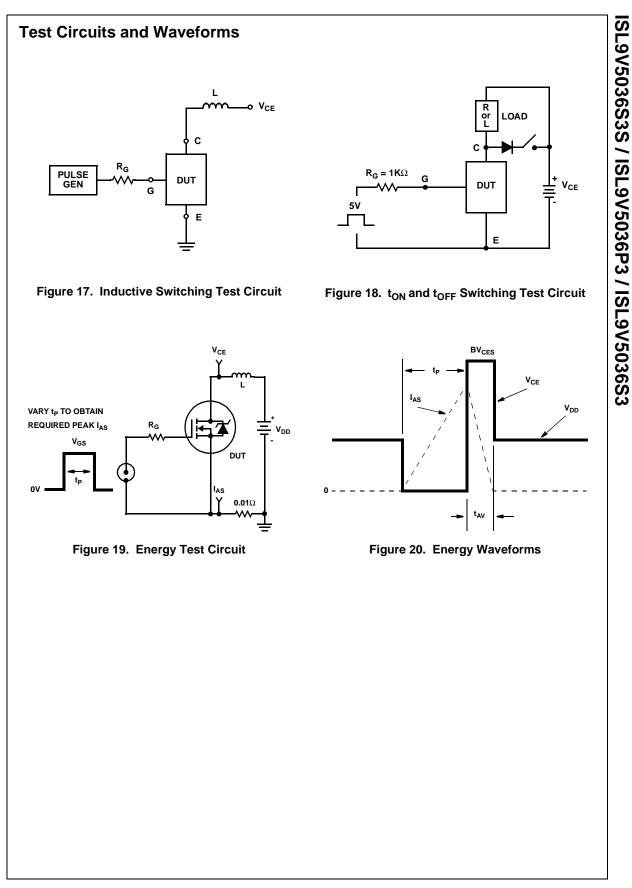


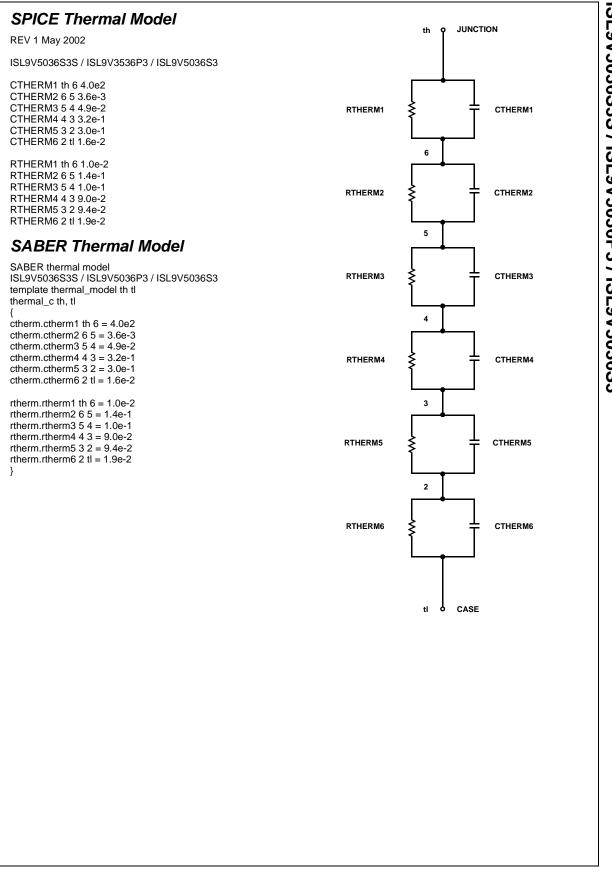
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