Vishay Semiconductors

650 V Power SiC Merged PIN Schottky Diode, 16 A



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PRIMARY CHARACTERISTICS					
I _{F(AV)}	16 A				
V _R	650 V				
V _F at I _F at 150 °C	1.65 V				
T _J max.	175 °C				
I _R at V _R at 175 °C	10 µA				
Q _C (V _R = 400 V)	45 nC				
Package	2L TO-220AC				
Circuit configuration	Single				

FEATURES

 Majority carrier diode using Schottky technology on SiC wide band gap material



COMPLIANT HALOGEN

FREE

- \bullet Positive V_{F} temperature coefficient for easy paralleling
- · Virtually no recovery tail and no switching losses
- Temperature invariant switching behavior
- 175 °C maximum operating junction temperature
- MPS structure for high ruggedness to forward current surge events
- Meets JESD 201 class 1A whisker test
- Solder Bath temperature 275 °C maximum, 10 s per JESD 22-B106
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

Wide band gap SiC based 650 V Schottky diode, designed for high performance and ruggedness.

Optimum choice for high speed hard switching and efficient operation over a wide temperature range, it is also recommended for all applications suffering from Silicon ultrafast recovery behavior.

Typical applications include AC/DC PFC and DC/DC ultra high frequency output rectification in FBPS and LLC converters.

MECHANICAL DATA

Case: 2L TO-220AC

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

Mounting torque: 10 in-lbs maximum

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage	V _{RRM}		650	V		
Average rectified forward current	I _{F(AV)}	T _C = 121 °C (DC)	16	А		
DC blocking voltage	V _{DC}		650	V		
Repetitive peak surge current	I _{FRM}	T_C = 25 °C, f = 50 Hz, square wave, DC = 25 $\%$	55.5			
Non-repetitive peak forward surge current	I _{FSM}	$T_C = 25 \text{ °C}, t_p = 10 \text{ ms}, \text{ half sine wave}$	120	А		
		T_{C} = 110 °C, t_{p} = 10 ms, half sine wave	110			
Power dissipation	P _{tot} ⁽¹⁾	T _C = 25 °C	89	w		
Power dissipation		T _C = 110 °C	38			
l ² t value	∫i ² dt	T _C = 25 °C	72	A2-		
		T _C = 110 °C	61	A ² s		
Operating junction and storage temperatures	T _J ⁽²⁾ , T _{Stg}		-55 to +175	°C		

Notes

⁽¹⁾ Based on maximum R_{th}

 $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J < 1/R_{0JA}

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ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
		I _F = 16 A	-	1.45	1.70		
Forward voltage	V _F	I _F = 16 A, T _J = 150 °C	-	1.65	1.95	V	
		I _F = 16 A, T _J = 175 °C	-	1.75	-		
Reverse leakage current	I _R	$V_R = V_R$ rated	-	-	85	μA	
		V _R = V _R rated, T _J = 150 °C	-	-	200		
		V _R = V _R rated, T _J = 175 °C	-	10	-		
Total capacitance	С	V _R = 1 V, f = 1 MHz	-	700	-		
		V _R = 400 V, f = 1 MHz	-	71	-	pF	
Total capacitive charge	Q _C	V _R = 400 V, f = 1 MHz	-	45	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS (T _A = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Thermal resistance, junction-to-case	R _{thJC}		-	1.3	1.7	°C/W	
Marking device				C16E	T07T		

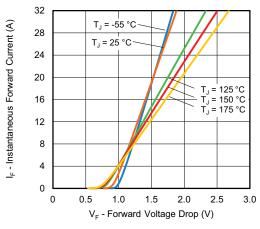


Fig. 1 - Typical Forward Voltage Drop Characteristics

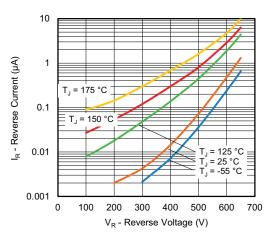


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

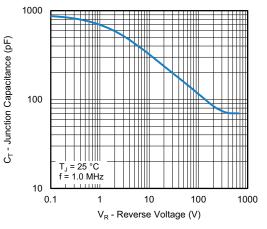


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

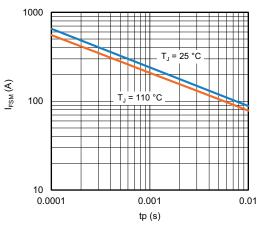


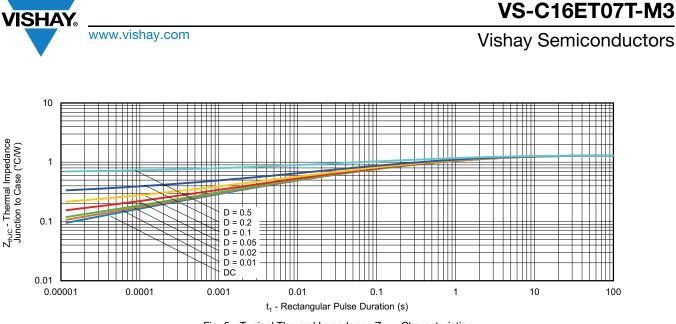
Fig. 4 - Non-Repetitive Peak Forward Surge Current vs. Pulse Duration (Square Wave)

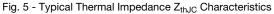
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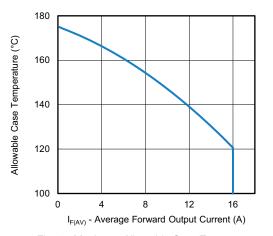


Fig. 6 - Maximum Allowable Case Temperature vs. Average Forward Current

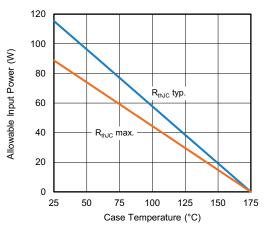


Fig. 7 - Forward Power Loss Characteristics

18 16 14 Capacitive Energy (µJ) 12 10 8 6 T_J = 25 °C f = 1.0 MHz 4 2 C V dV $E_I =$ 0 0 100 200 300 400 500 600 700 Reverse Voltage (V)

Fig. 8 - Typical Capacitive Energy vs. Reverse Voltage

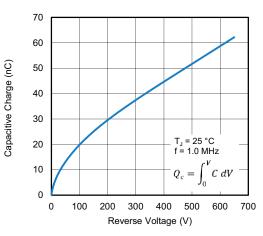


Fig. 9 - Typical Capacitive Charge vs. Reverse Voltage

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ORDERING INFORMATION TABLE

Device code	VS-	с	16	Е	т	07	т	-M3
		•		_	-	0.	•	
	1	2	3	4	5	6	7	8
	1	- Visl	nay Sem	niconduo	ctors pr	oduct		
	2	- C =	SiC dic	de				
	3	- Cur	rent rati	ng (16 =	= 16 A)			
	4	- E=	single o	diode				
	5	- Pac	kage T	D-220				
	6	- Vol	tage rati	ng: (07	= 650 V)		
	7	• T =	true 2 p	in				
	8	- Env	ironmer	ntal digit	:			
		-M3	3 = halo	gen-free	e, RoHS	-compli	ant, and	d termir

ORDERING INFORMATION							
PREFERRED P/N	BASE QUANTITY	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-C16ET07T-M3	50/tube	1000	Antistatic plastic tubes				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?96069				
Part marking information	www.vishay.com/doc?95391				



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