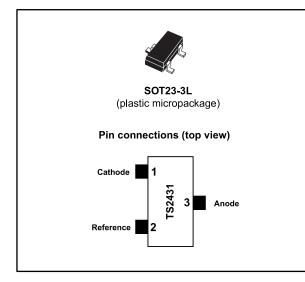


Adjustable shunt voltage reference

Datasheet - production data



Features

- Adjustable output voltage: 2.5 to 24 V
- Precision selection at 25 °C: ± 2%, ± 1% and ± 0.5%

This is information on a product in full production.

• Sink current capability: 1 to 100 mA

- Industrial temperature range: 40 to + 105 °C
- Performance compatible with industrystandard TL431

Applications

- Computers
- Instrumentation
- Battery chargers
- Switch mode power supplies
- Battery-operated equipment

Description

The TS2431 is an adjustable shunt voltage reference with guaranteed temperature stability over the entire temperature range of operations from - 40 to + 105 °C. The output voltage may be set to any value between 2.5 and 24 V with an external resistor bridge. Available in an SOT23-3L surface mount package, the device can be implemented for those applications where space-saving is of the utmost importance.

Table 1	: Device	summary
---------	----------	---------

Order code	Temperature range	Package	Packing	Precision	Marking
TS2431ILT				2%	L285
TS2431AILT	-40 to + 105 °C	SOT23-3L	Tape and reel	1%	L286
TS2431BILT				0.5%	L287

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Absolute maximum ratings and operating conditions

Table 2: Absolute maximum ratings			
Symbol	Parameter	Value	Unit
V _{ka}	Cathode to anode voltage	25	V
lκ	Reverse breakdown current	-100 to +150	mA
I _{REF}	Reference input current range	0.05 to +10	mA
Pd	Power dissipation ⁽¹⁾ SOT23-3L	360	mW
T _{std}	Storage temperature	-65 to +150	°C
	Human body model (HBM) ⁽²⁾	2	kV
ESD	Machine model (MM) ⁽³⁾	200	V
TLEAD	Lead temperature (soldering, 10 s)	260	°C

Notes:

 $^{(1)}Pd$ has been calculated with Tamb = 25 °C, Tjunction = 150 °C, Rthjc = 110 °C/W and Rthja = 340 °C/W for the SOT23-3 package.

 $^{(2)}$ Human body model: a 100 pF capacitor is charged to the specified voltage, then discharged through a 1.5 k Ω resistor between two pins of the device. This is done for all couples of connected pin combinations while the other pins float.

⁽³⁾Machine model: a 200 pF capacitor is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5 Ω). This is applied for all couples of connected pin combinations while the other pins float.

Table 3: Operating conditions

Symbol	Parameter	Value	Unit
VKA	Cathode to anode voltage	V _{REF} to 24	V
lκ	Cathode operating current ⁽¹⁾	1 to 100	mA
T _{oper}	Operating free air temperature range	- 40 to + 105	°C

Notes:

⁽¹⁾Maximum power dissipation must be strictly observed to avoid damaging the component.



2 Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit	
		$V_{K} = V_{REF}, I_{K} = 10$ mA		2.5			
		TS2431 (2%)	2.45		2.55	V	
Vref	Reference input voltage	TS2431A (1%)	2.475		2.525		
	J. J	TS2431B (0.5%)	2.488		2.512		
		TS2431B (1%), Ι _κ = 1 mA	2.475		2.525		
	Reference input	0 °C < T < + 70 °C		10	20		
ΔV _{ref}	voltage deviation over temperature	-40 °C < T < + 85 °C		17	30	mV	
	$V_{K} = V_{REF}, I_{K} = 10$ mA ⁽¹⁾⁽²⁾	-40 °C < T < + 105 °C		20	35		
Tc	Temperature coefficient ⁽²⁾	-40 °C < T < + 105 °C		50	100	ppm/°C	
	Minimum operating current	T = 25 °C		0.3	0.8		
I _{KMIN}		-40 °C < T < +105 °C			1	mA	
$\left \frac{\Delta V_{ref}}{\Delta V_K}\right $	Ratio of change in reference input voltage to change in cathode-to- anode voltage	lκ = 10 mA Vka = 24 to 2.5 V		0.3	2	mV/V	
	Reference input	T = 25 °C		0.5	2.5	μA	
$I_{REF} \qquad \begin{array}{l} \text{current } I_{K} = 10 \text{ mA,} \\ \text{R1} = 10 \text{ k}\Omega, \text{ R2} = \\ + \infty^{(3)} \end{array}$		-40 °C < T < +105 °C			3		
ΔI _{REF}	Reference input current deviation $I_{K} = 10 \text{ mA}, \text{ R1} =$ $10 \text{ k}\Omega, \text{ R2} = + \infty^{(3)}$	-40 °C < T < +105 °C		0.4	1.2	μΑ	
I _{OFF}	Off-state cathode current	V _K = 24 V, V _{REF} = 1 GND 1		10	500	nA	
Zka	Reverse dynamic impedance	$V_{\rm K}$ = $V_{\rm REF}$, $\Delta I_{\rm K}$ = 1 to 50 mA, f < 10 kHz		0.5	0.75	Ω	
E _N	Wide band noise	lκ = 10 mA 10 Hz < f < 10 kHz		300		nV/√Hz	

Notes:

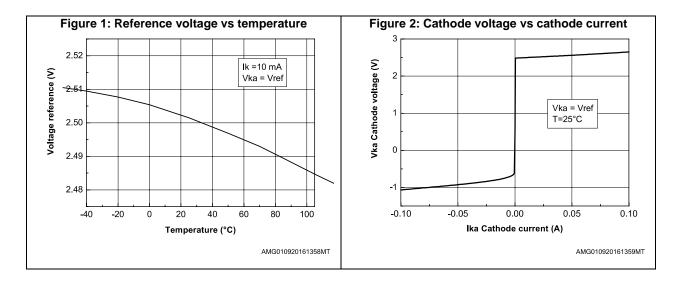
 $^{(1)}Limits$ are 100 % production tested at 25 °C. Overtemperature limits are guaranteed through correlation and by design.

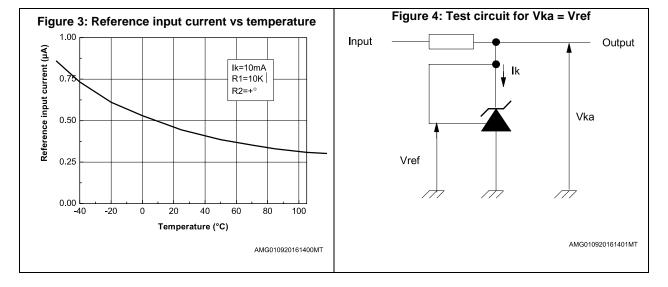
 $^{(2)}|\Delta V_{\text{REF}}|$ is defined as the difference between the maximum and minimum values of V_{REF} obtained over the full temperature range.

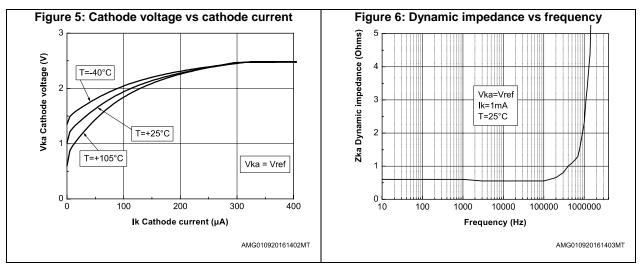
⁽³⁾Refer to *Figure 4: "Test circuit for Vka = Vref"*.

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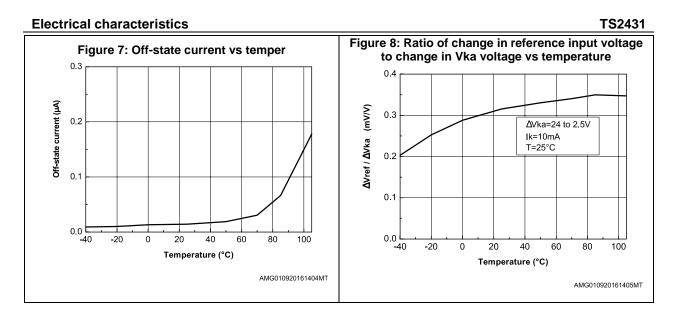


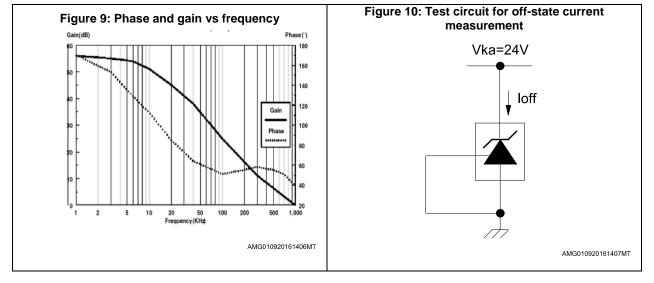


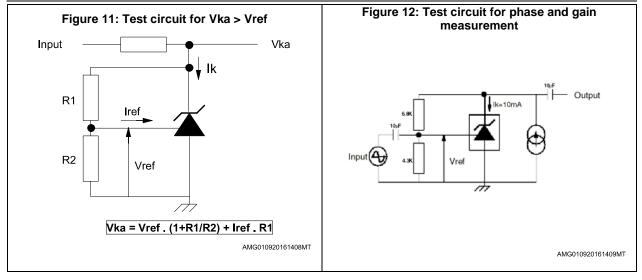
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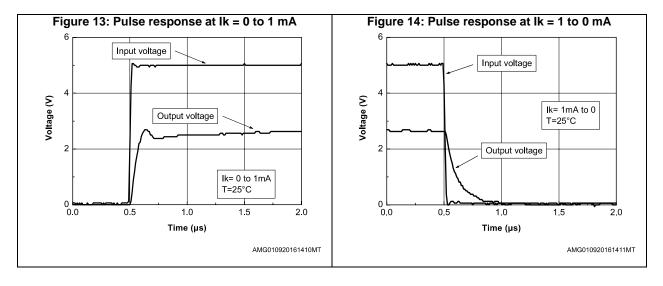
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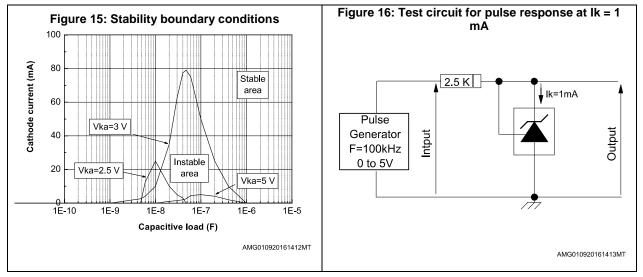
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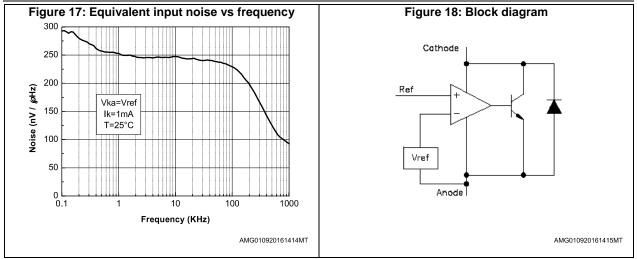




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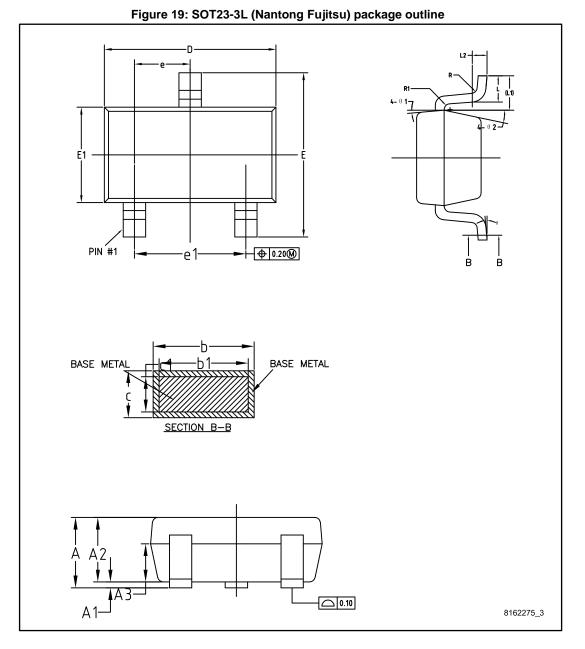






In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

3.1 SOT23-3L package information



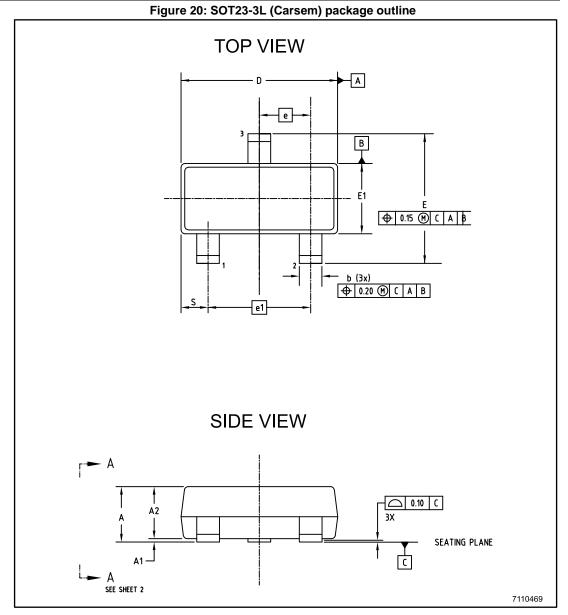
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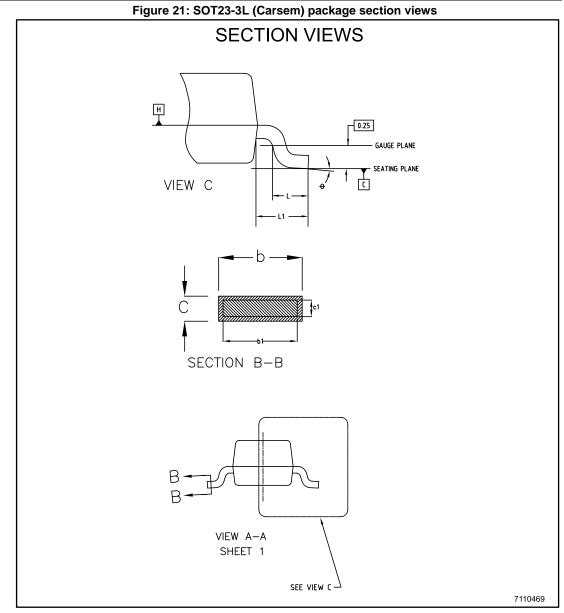
Table 4: SOT23-3L (Nantong Fujitsu) mechanical data

	Table 4: 50123-3L (Nant	ong Fujitsu) mechanical d	ata		
Dim.		mm			
	Min.	Тур.	Max.		
A			1.25		
A1	0		0.15		
A2	1	1.10	1.20		
A3	0.60	0.65	0.70		
b	0.36		0.50		
b1	0.36	0.38	0.45		
С	0.14		0.20		
c1	0.14	0.15	0.16		
D	2.826	2.926	3.026		
E	2.60	2.80	3.00		
E1	1.526	1.626	1.726		
е	0.90	0.95	1.00		
e1	1.80	1.90	2.00		
L	0.35	0.45	0.60		
L1		0.59 REF			
L2		0.25 BSC			
R	0.05				
R1	0.05				
θ	0°		8°		
θ1	3°	5°	7°		
θ2	6°		14°		





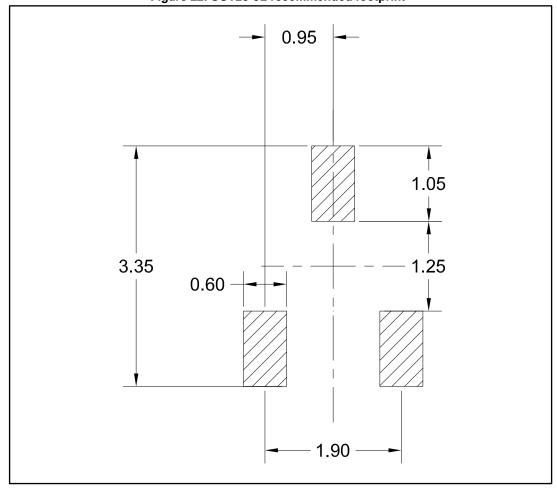






			Package information
	Table 5: SOT23-3	BL (Carsem) mechanical data	a
		Dimensions	
Def		Millimeters	
Ref.	Min.	Тур.	Max.
A	0.89	-	1.12
A1	0.013	-	0.10
A2	0.88	0.95	1.02
b	0.37	-	0.50
b1	0.37	0.40	0.45
с	0.085	-	0.18
c1	0.085	-	0.16
D	2.80	2.90	3.04
E	2.10	-	2.64
E1	1.20	1.30	1.40
е		0.95 BSC	
e1		1.90 BSC	
*L	0.28	0.38	0.48
L1		0.55 REF	
L2			
R	0.05		
R1	0.05		
θ	0°		80
s	0.45	-	0.60





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4 Revision history

Date	Revision	Changes
01-Feb-2002	1	Initial release.
10-Sep-2009	2	Updated document format. Modified footnote 1 under <i>Table 2: Absolute maximum ratings on page 3.</i> Added HBM and MM notes under <i>Table 2.</i>
11-May-2012	3	Removed: automotive grade order codes <i>Table 1 on page 1</i> .
22-Nov-2012	4	Added min. and max. values test condition TS2431B (1%), $I_{K} = 1$ mA <i>Table 4 on page 4</i> .
28-Nov-2016	5	Updated Section 3: "Package information". Minor text changes.
20-Oct-2017	6	Updated the title and the description in cover page. Minor text changes.



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