



2 CHANNEL LOW CAPACITANCE BI-DIRECTIONAL TVS ARRAY

Features

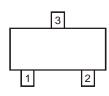
- Provides ESD Protection per IEC 61000-4-2 Standard: Air - ±30kV, Contact - ±30kV
- 2 Channels of Bi-directional ESD Protection
- Low Channel Input Capacitance
- Typically Used at Portable Electronics, Cellular Handsets and **Communication Systems**
- Lead Free/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

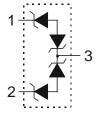
Mechanical Data

- Case: SOT523
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Weight: 0.002 grams (approximate)





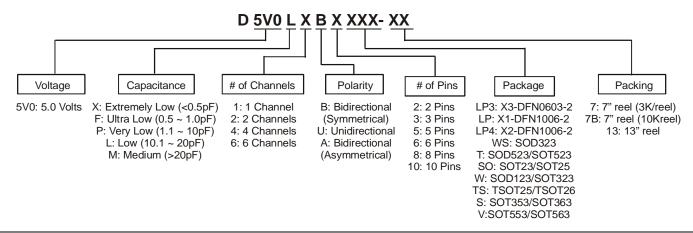




Pin Configuration

Device Schematic

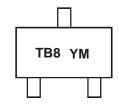
Ordering Information (Note 3)



Part Number	Case	Packaging
D5V0L2B3T-7	SOT523	3000/Tape & Reel

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. No purposely added lead. Halogen and Antimony free. Notes:
 - 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.
 - 3. For packaging details, go to our website at http://www.diodes.com.

Marking Information



TB8 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Z = 2012)M = Month (ex: 9 = September)

Date Code Kev

Year	201	1	2012		2013	20	14	2015		2016	2	2017
Code	Υ		Z		Α	E	3	С		D		Е
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	P _{PP}	84	W	8/20μs, Per Fig. 1
Peak Pulse Current	IPP	6	Α	8/20μs, Per Fig. 1
ESD Protection – Contact Discharge	V _{ESD_Contact}	±30	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V_{ESD_Air}	±30	kV	Standard IEC 61000-4-2

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	P _D	200	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ heta JA}$	625	°C/W
Operating Junction Temperature Range	TJ	-65 to +150	°C
Storage Temperature Range	T _{STG}	-65 to +150	°C

Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	V_{RWM}	-	-	5.0	V	-
Breakdown Voltage	V_{BR}	6	7	8	V	$I_R = 1.0 \text{mA}$
Reverse Leakage Current (Note 6)	I _R	-	10	100	nA	VRWM = 5V
		-	7.0	9.0	V	Ipp = 1A, $t_p = 8/20 \mu s$
Clamping Voltage (Note 4)	VCL	-	- 8.7 10.7 V Ipp = 3A, t _p = 8	$Ipp = 3A, t_p = 8/20 \mu s$		
	VCL	-	10.5	12.0	V	Ipp = $5A$, $t_p = 8/20 \mu s$
		-	11.5	14.0	V	Ipp = $6A$, $t_p = 8/20 \mu s$
Differential Resistance	R _{DIF}	-	0.2	-	Ω	$I_R = 1A$, $t_p = 8/20 \mu s$
Channel Input Capacitance	Ст	ı	15	20	pF	V _{IN} = 0 V, f = 1MHz (Channel to Pin 3)

Notes:

- 4. Measured from pin 1 to 3 or pin 2 to 3; Non-repetitive current pulse per Fig. 1.
- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.
- 6. Short duration pulse test used to minimize self-heating effect.

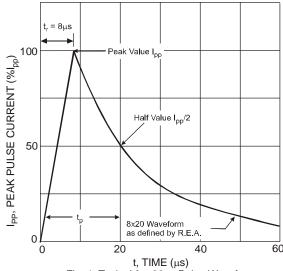
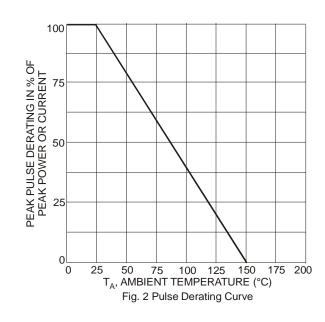
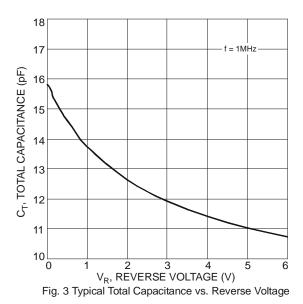
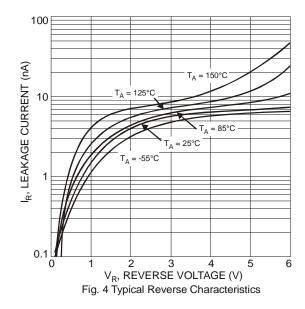


Fig. 1 Typical 8 x 20µs Pulse Waveform

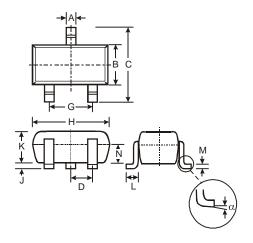






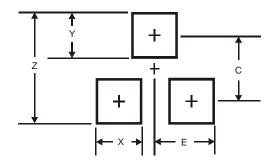


Package Outline Dimensions



SOT523							
Dim	Min	Max	Тур				
Α	0.15	0.30	0.22				
В	0.75	0.85	0.80				
С	1.45	1.75	1.60				
D	_	_	0.50				
G	0.90	1.10	1.00				
Н	1.50	1.70	1.60				
J	0.00	0.10	0.05				
K	0.60	0.80	0.75				
L	0.10	0.30	0.22				
M	0.10	0.20	0.12				
N	0.45	0.65	0.50				
α	0°	8°	_				
All Dimensions in mm							

Suggested Pad Layout



Dimensions	Value (in mm)		
Z	1.8		
Х	0.4		
Y	0.51		
С	1.3		
E	0.7		



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