## 45V Nch+Nch Power MOSFET

V <sub>DSS</sub>	45V
R <sub>DS(on)</sub> (Max.)	46mΩ
I <sub>D</sub>	±4.5A
$P_{D}$	2.0W

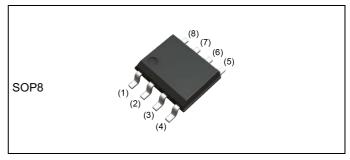
## Features

- 1) Low on resistance
- 2) Small Surface Mount Package (SOP8)
- 3) Pb-free lead plating; RoHS compliant
- 4) Halogen Free
- 5) AEC-Q101 Qualified

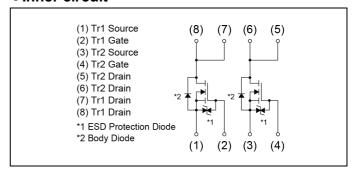
# Application

Switching

#### Outline



## •Inner circuit



# Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Basic ordering unit (pcs)	2500
	Taping code	ТВ
	Marking	SP8K22

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified) < Tr1 and Tr2>

Parameter	Symbol	Value	Unit	
Drain - Source voltage	$V_{DSS}$	45	V	
Continuous drain current	I <sub>D</sub>	±4.5	Α	
Pulsed drain current	I <sub>DP</sub> *1	±18	Α	
Gate - Source voltage	V <sub>GSS</sub>	±20	V	
Davis a discipation (total)	P <sub>D</sub> *2	2.0	W	
Power dissipation (total)	P <sub>D</sub> *3	1.4		
Junction temperature	T <sub>j</sub>	150	°C	
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C	

# ●Thermal resistance

Doromotor	Cymahal	Values			l limit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registeres in action, ambient (total)	R <sub>thJA</sub> *2	-	-	62.5	°C/W
Thermal resistance, junction - ambient (total)	$R_{thJA}^{*3}$	1	-	89.2	C/VV

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Davanastav	Cy reals al	Conditions	Values			Unit	
Parameter	Parameter Symbol Conditions -		Min.	Тур.	Max.	Offic	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	45	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I <sub>D</sub> = 1mA referenced to 25°C	-	46.8	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	<sub>SS</sub> V <sub>DS</sub> = 45V, V <sub>GS</sub> = 0V		-	1	μA	
Gate - Source leakage current	$V_{DC} = V_{DC} = \pm 20$		-	-	±10	μA	
Gate threshold voltage	$V_{GS(th)}$ $V_{DS} = 10V, I_D = 1mA$		1.0	-	2.5	V	
Gate threshold voltage temperature coefficient ΔV <sub>G</sub>		I <sub>D</sub> = 1mA referenced to 25°C	-	-3.9	-	mV/°C	
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 4.5A	-	33	46		
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 4.5A	-	41	57	mΩ	
on state resistance		V <sub>GS</sub> = 4.0V, I <sub>D</sub> = 4.5A	-	46	64		
Gate resistance	R <sub>G</sub>	f = 1MHz, open drain		5.0	-	Ω	
Forward Transfer Admittance	$ Y_{fs} ^{*4}$ $V_{DS} = 10V, I_D = 4.5A$		3.5	-	-	S	

<sup>\*1</sup> Pw  $\leq$  10 $\mu$ s, Duty cycle  $\leq$  1%

<sup>\*2</sup> Mounted on a ceramic board (30×30×0.8mm)

<sup>\*3</sup> Mounted on a FR4 (25×25×0.8mm)

<sup>\*4</sup> Pulsed

# ●Electrical characteristics (T<sub>a</sub> = 25°C) <Tr1 and Tr2>

Parameter	Symbol	Conditions		Unit			
Parameter	Symbol Conditions –		Min.	Тур.	Max.	Orill	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	550	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 10V	-	140	-	pF	
Reverse transfer capacitance C		f = 1MHz	-	70	-		
Turn - on delay time	t <sub>d(on)</sub> *4	V <sub>DD</sub> ≈ 25V,V <sub>GS</sub> = 10V	-	12	-		
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 2.5A	-	18	-		
Turn - off delay time ${\mathsf t_{\mathsf d(off)}}^{*4}$		$R_L = 10\Omega$	-	42	-	ns	
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	12	-		

# ullet Gate charge characteristics (T<sub>a</sub> = 25°C) <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*4}$		-	6.8	9.6	
Gate - Source charge	Q <sub>gs</sub> *4	$V_{DD} \approx 25V$ , $I_D = 4.5A$ $V_{GS} = 5V$	-	2.0	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	1.00	-	2.9	-	

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

# <Tr1 and Tr2>

Darameter	Cymahal	Conditions	Values			1 1:4	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Continuous forward current	I <sub>S</sub>	T - 25°C	-	-	1.66	Δ	
Pulse forward current	I <sub>SP</sub> *1	T <sub>a</sub> = 25°C	-	-	18	А	
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = 4.5A	-	-	1.2	V	

Fig.1 Power Dissipation Derating Curve

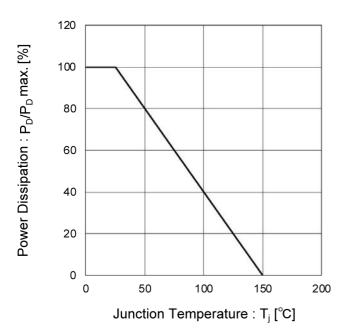
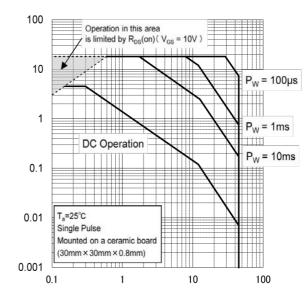


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage: V<sub>DS</sub>[V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

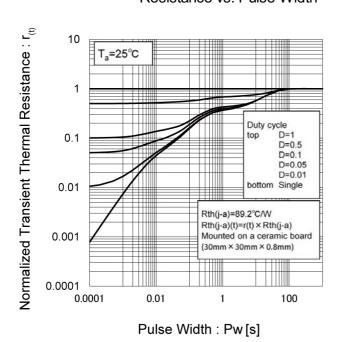
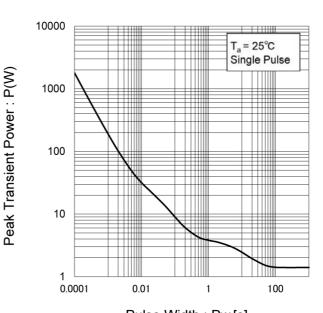


Fig.4 Single Pulse Maximum Power dissipation



Pulse Width: Pw[s]

Fig.5 Typical Output Characteristics(I)

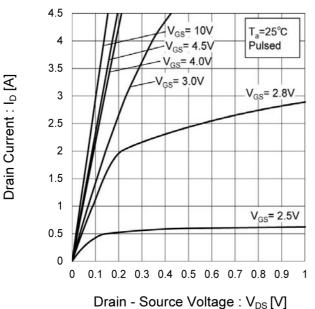
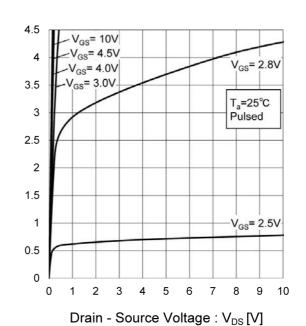


Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

0 501.

Fig.7 Breakdown Voltage vs. Junction Temperature

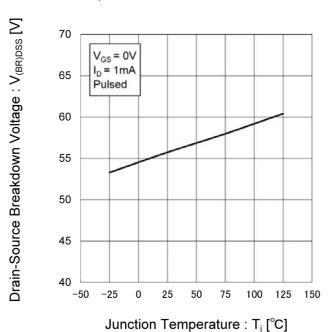


Fig.8 Typical Transfer Characteristics

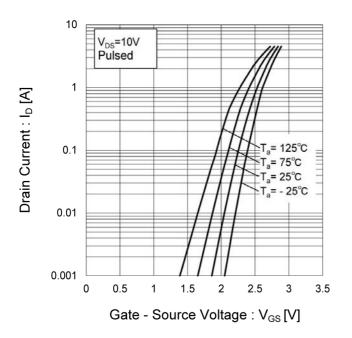
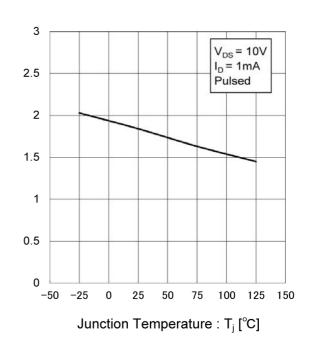


Fig.9 Gate Threshold Voltage vs. Junction Temperature



Gate Threshold Voltage : V<sub>GS(th)</sub> [V]

Fig.10 Forward Transfer Admittance vs. Drain Current

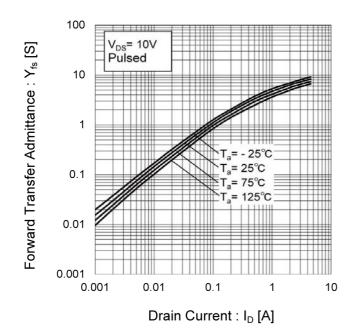


Fig.11 Drain Current Derating Curve

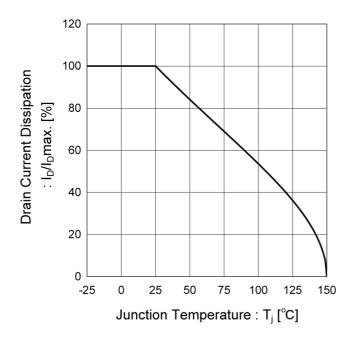


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

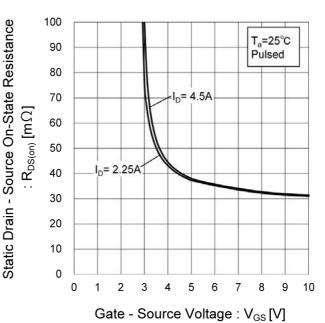
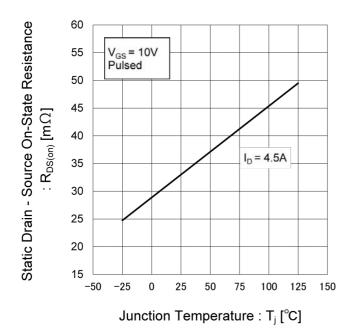


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



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Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

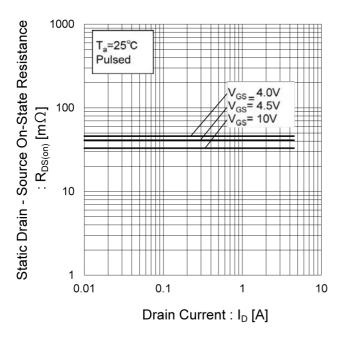


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

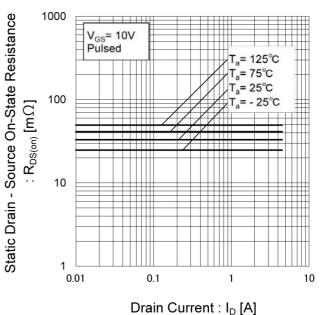


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

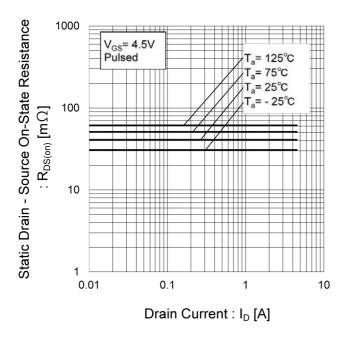


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)

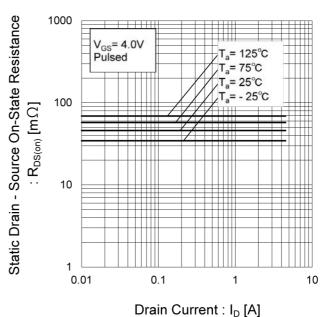


Fig.18 Typical Capacitance vs. Drain -Source Voltage

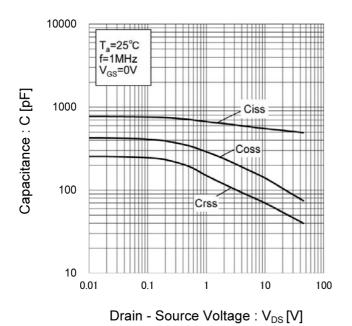


Fig.19 Switching Characteristics

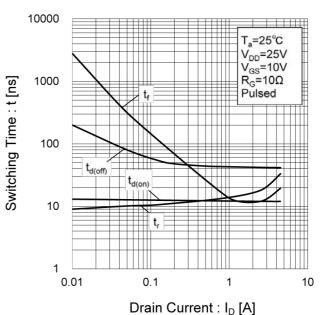
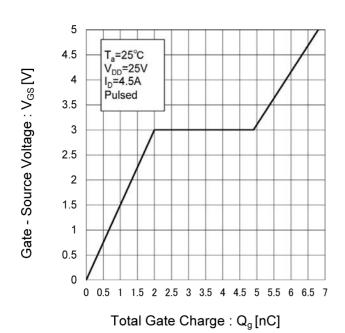


Fig.20 Dynamic Input Characteristics



10 V<sub>GS</sub>=0V Pulsed 1 T<sub>a</sub>= 125°C

Voltage

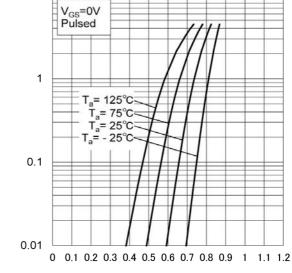


Fig.21 Source Current vs. Source Drain

Source-Drain Voltage: V<sub>SD</sub>[V]

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Source Current : Is [A]

# • Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

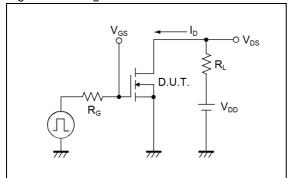


Fig.2-1 Gate Charge Measurement Circuit

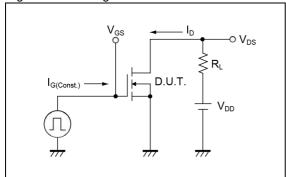


Fig.1-2 Switching Waveforms

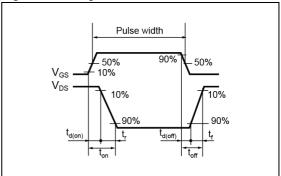
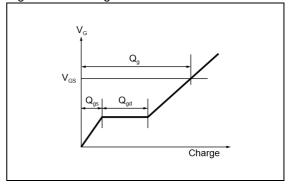
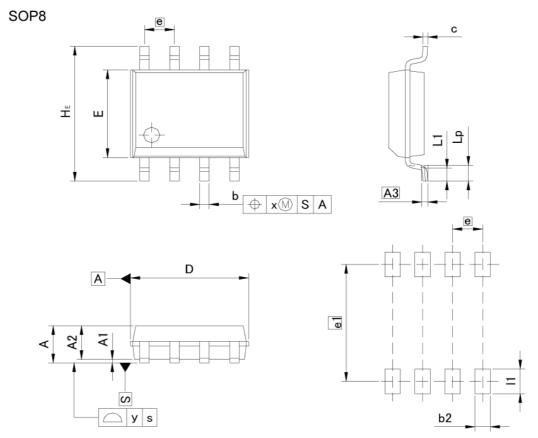


Fig.2-2 Gate Charge Waveform



# Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	<u>₩</u>	1.75	***	0.069
A1	0.	15	0.0	06
A2	1.40	1.60	0.055	0.063
A3	0.	25	0.0	10
b	0.30	0.50	0.012	0.020
С	0.10	0.30	0.004	0.012
D	4.80	5.20	0.189	0.205
E	3.75	4.05	0.148	0.159
е	1.	27	0.0	50
HE	5.70	6.30	0.224	0.248
L1	0.40	0.60	0.016	0.024
Lp	0.65	0.85	0.026	0.033
y y	0.15		0.0	06
	0.	0.10		0.004
DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
LO.		0.65		0.006

 b2
 0.65
 0.026

 e1
 5.15
 0.203

 I1
 1.15
 0.045

Dimension in mm/inches



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JÁPAN	USA	EU	CHINA
CLASSⅢ	CL A CC TT	CLASS II b	CLASSⅢ
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSIII

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  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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