

#### **Features**

- RoHS compliant\*
- Protects up to four I/O ports
- Unidirectional configuration
- ESD protection: 30 kV max.
- Low capacitance: 15 pF

### **Applications**

- Ethernet 10/100 Base T
- Computer I/O ports SCSI, FireWire and USB
- Set-top box protection
- Video cards

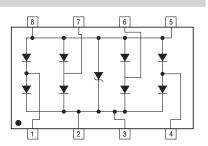
### CDNBS08-SRDAxx-4 Series - Steering Diode/TVS Array Combo

#### **General Information**

The markets of portable communications, computing and video equipment are challenging the semiconductor industry to develop increasingly smaller electronic components.

Bourns offers Steering Diode/Transient Voltage Suppressor Array combination diodes for surge and ESD protection applications in an eight lead narrow body SOIC package size format. Bourns® Chip Diodes conform to JEDEC standards, are easy to handle on standard pick and place equipment and their flat configuration minimizes roll away.

The Bourns device will meet IEC 61000-4-2 (ESD), IEC 61000-4-4 (EFT) and IEC 61000-4-5 (Surge) requirements.



### Electrical Characteristics (@ T<sub>A</sub> = 25 °C Unless Otherwise Noted)

Barramatar	Symbol	CDNBS08-				I I mild
Parameter		SRDA3.3-4	SRDA05-4	SRDA12-4	SRDA15-4	Unit
Minimum Breakdown Voltage @ 1 mA	V <sub>BR</sub>	4.0	6.0	13.3	16.7	V
Working Peak Voltage	V <sub>WM</sub>	3.3	5.0	12.0	15.0	V
Maximum Clamping Voltage V <sub>C</sub> @ I <sub>P</sub> <sup>1</sup>	V <sub>F</sub>	8.0	9.8	19.0	24.0	V
Maximum Clamping Voltage @ 8/20 $\mu$ s V <sub>C</sub> @ I <sub>PP</sub> <sup>1</sup>	V <sub>F</sub>	10.9 V @ 43 A	13.5 V @ 42 A	25.9 V @ 27 A	30.0 V @ 17 A	V
Maximum Leakage Current @ V <sub>WM</sub>	I <sub>D</sub>	125	20	1	1	μΑ
Maximum Cap. Bidirectional @ 0 V, 1 MHz	C <sub>J(SD)</sub>		1	5		pF
ESD Protection per IEC 61000-4-2 Contact - Min. Contact - Max. Air - Min. Air - Max.	ESD	±8 ±30 ±15 ±30		kV		
Peak Pulse Power (t <sub>p</sub> = 8/20 $\mu$ s) <sup>2</sup>	P <sub>PP</sub>		50	00		W
Maximum Forward Voltage @ 10 mA	V <sub>F</sub>		1	.1		V

#### Notes

- 1. See Pulse Waveform.
- 2. See Peak Pulse Power vs. Pulse Time.
- 3. Measured between pins 8 or 5 to 1, 2, 3, 4, 6 and 7.

#### Thermal Characteristics (@ T<sub>A</sub> = 25 °C Unless Otherwise Noted)

Parameter	Symbol	Max.	Unit
Operating Temperature	TJ	-55 to +150	°C
Storage Temperature	T <sub>STG</sub>	-55 to +150	°C

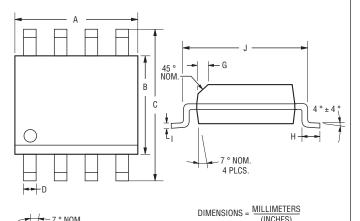


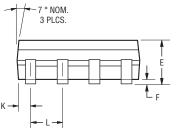
WARNING Cancer and Reproductive Harm - www.P65Warnings.ca.gov

# **CDNBS08-SRDAxx-4 Series - Steering Diode/TVS Array Combo**

#### **Product Dimensions**

This is an RoHS compliant molded JEDEC narrow body SO-8 package with 100 % Sn plating on the lead frame. It weighs approximately 15 mg and has a flammability rating of UL 94V-0.

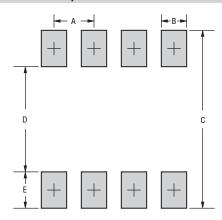




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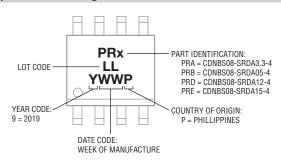
Dimensions		
А	4.80 - 5.00 (0.189 - 0.197)	
В	3.81 - 4.00 (0.150 - 0.157)	
С	$\frac{5.80 - 6.20}{(0.228 \pm 0.244)}$	
D	0.36 - 0.51 (0.014 - 0.020)	
E	1.35 - 1.75 (0.053 - 0.069)	
F	<u>0.102 - 0.203</u> (0.004 - 0.008)	
G	<u>0.25 - 0.50</u> (0.010 - 0.020)	
Н	<u>0.51 - 1.12</u> (0.020 - 0.044)	
ı	<u>0.190 - 0.229</u> (0.0075 - 0.0090)	
J	4.60 - 5.21 (0.181 - 0.205)	
К	<u>0.28 - 0.79</u> (0.011 - 0.031)	
L	1.27 (0.050)	

#### **Recommended Footprint**



Dimensions		
А	1.143 - 1.397 (0.045 - 0.065)	
В	<u>0.635 - 0.889</u> (0.025 - 0.035)	
С	6.223 (0.245) Min.	
D	3.937 - 4.191 (0.155 - 0.165)	
E	1.016 - 1.27 (0.040 - 0.050)	

#### **Typical Part Marking**

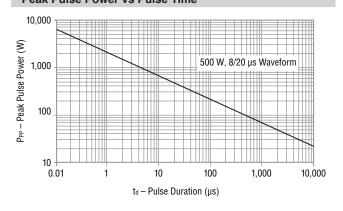


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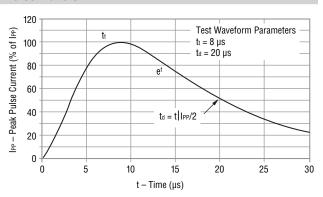
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#### **Performance Graphs**

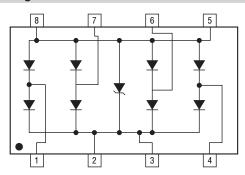
#### **Peak Pulse Power vs Pulse Time**



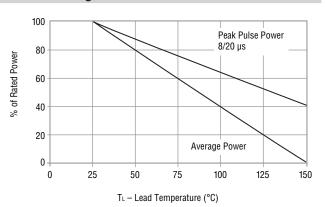
#### **Pulse Waveform**



#### **Block Diagram**



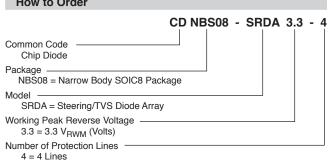
#### **Power Derating Curve**



#### **Device Pinout**

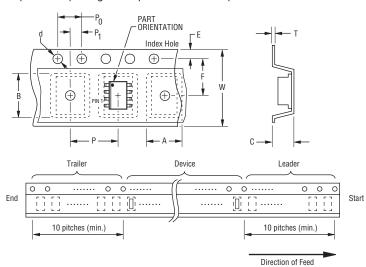
Pin	Function	
1	I/O 1	
2	+V <sub>REF</sub>	
3	+V <sub>REF</sub>	
4	I/O 2	
5	GND	
6	I/O 3	
7	I/O 4	
8	GND	

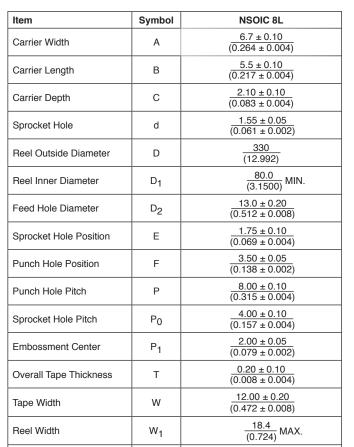
#### **How to Order**



#### **Packaging Information**

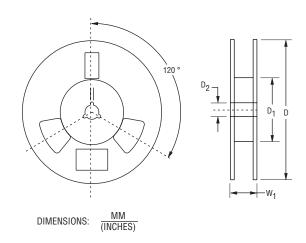
The product is packaged in tape and reel format per EIA-481 standard.





2500

Quantity per Reel



#### REV. 01/20

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